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Find the *Broadcom / PLX Technology PCI 9080RDK-401B* at our website: ***Click HERE***

PCI 9080RDK-401B

Reference Design Kit with PCI SDK

Features

- PCI Version 2.1 compliant board based on the powerful PCI 9080 Bus Master I/O Accelerator chip
- Full Bus Master and Burst Management–Directly supports PCI Target, PCI Master, DMA, and I_2O messaging transfers
- IBM PowerPC 401GF 32-bit 50MHz RISC processor
- Complete OrCAD schematics, FPGA verilog code, and hardware reference manual
- Sample PCI 9080 and Data Book

PCI SDK:

- PLXMon 98, a comprehensive GUI tool for PCI bus monitoring and debugging
- Flexible PCI API libraries for host and the local CPU are included
- Sample Windows NT device drivers and source code are included
- PLXRom firmware debugging monitor with loader
- Comprehensive reference manuals for PLXMon 98 and PCI SDK

Compatible Tools:

- Compatible with the PCI I_2O Manager and I_2O SDK available from PLX Technology for complete I_2O development solutions
- Compatible with PowerPC development tools available from IBM and other third parties



The PLX PCI 9080RDK-401B simplifies your PCI design providing you with a complete hardware and software development environment. The reference design kit includes a PCI v2.1 compliant board based on the powerful combination of the PLX PCI 9080 Bus Master I/O Accelerator Chip and the IBM PowerPC 401GF 32-bit embedded RISC processor as the local CPU. Also included in the reference design kit is the PLX PCI software development kit (SDK) which features a complete set of software development tools including host and local side PCI APIs for the PCI 9080, the Board Support Package (BSP) "C" source code necessary for porting to the target design, and sample Windows® NT® drivers which significantly reduce the time necessary to develop PCI software. Also included is PLXMon® 98, a robust and comprehensive Windows NT based GUI tool that allows for interactive PCI and local bus device monitoring, debugging, configuration, and code download to the RDK board.

The PCI 9080RDK-401B addresses the need to quickly bring your embedded design to market while providing you with a longer term investment, a software development platform that will enable you to migrate from the PCI 9080 to future PLX products such as the IOP 480 (Intelligent I/O processor).

The PCI 9080 Bus Master I/O Accelerator interface chip available from PLX provides a compact, high performance PCI bus master interface for adapter boards and embedded systems. The PCI 9080 includes two high performance DMA channels, programmable burst modes, and messaging functions. In addition, the programmable local bus can be configured to directly connect to a wide variety of processors, controllers, and memory subsystems.



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PCI 9080RDK-401B	
PCI Bus Interface	PCI 9080 Interface Chip
Processor	IBM PowerPC 401GF
Processor Speed	50MHz
Local Bus Speed	25MHz
PCI Bus Speed	Same as Local Bus, or set by Host-33MHz max
DRAM	16 MB
SRAM	512 KB
FLASH	512 KB
Memory Controller	FPGA
RS-232 Serial Ports	1
JTAG	Compatible with IBM RISCWatch® debugger
Expansion Capability	POM interface
Complete OrCAD Schematics, FPGA Verilog Code, Hardware Reference Manual and PCI 9080 Data Book	Yes
PLD/FPGA	Yes
Sample Chip	PCI 9080
Evaluation Compiler	IBM High "C" limited compiler

PLX PCI SDK Features

Features	Benefits
PCI 9080 Software API Libraries for IBM PPC, Motorola PPC and Intel i960 family of processors	PC implementation of PCI software and migration to future PLX devices
Board Support Package (BSP) "C" Source Code	Allows easy porting to customer design
Sample Windows NT Drivers and "C" Source Code	Allows effortless creation of PLX device drivers for RTOS or custom software implementation
PLXMon 98 PCI Debugger	Allows smooth configuration and text modification of PCI/Local bus devices
PLXRom Debug Monitor Firmware with Loader	Sample boot FLASH code with serial debugger and supports downloading of code onto RDKs or customer design
Programmer's Reference Manual	Supporting documentation
Software Development Tools	Compatible with IBM High "C" and third party tools

Product Ordering Information

PCI 9080	PCI to Local Bus Master I/O Accelerator Chip
PCI 9080RDK	PCI 9080 Reference Design Kits
PCI 9054	PCI to Local Bus Master Chip
PCI 9054RDK-860	PCI 9054 Reference Design Kit with Motorola MPC860 CPU
CompactPCI 9054RDK-860	PCI 9054 CompactPCI Reference Design Kit with Motorola MPC860 CPU
I_2O SDK	I_2O Software Development Kit for PCI 9080
PCI SDK	PCI Software Development Kit for PCI 9080

* See PLX web site for latest version and product support information

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