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Find the *Abaco Systems / SBS / BSI 4422-V64* at our website: *Click HERE*

4422-V64

Telemetry Module

I/O & Communications

Features

Supports PCM Decommutation
- Up to 20 Mbps

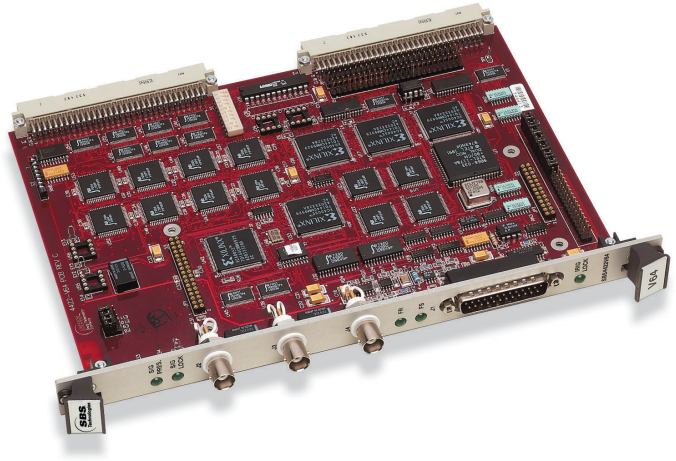
Variable Word Length Processing
- 3 - 16 bits

VME-64 Bus
- Interface for High-Speed Data Transfers

Subframe Methods
- SFID, FAC, & URC

Full-fidelity PCM Simulation
- Up to 10 Mbps

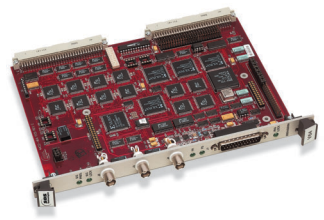
Options
- Minor Frame IRIG Time Insertion
- PCM Bit Synchronizer Daughter Card



4422-V64 is a complete, system level telemetry module for the VME bus. The module supports all of the functions required for telemetry data acquisition for input data streams at rates up to 20 Mbps.

The 4422-V64 contains an optional bit synchronizer, PCM decommutator, optional IRIG time code reader, and dynamic PCM simulator. The optional bit synchronizer accepts all PCM codes and includes a tape driver output. The decommutator supports variable frame and word length formats. The simulator features dynamic and unique data word generation. It can operate autonomously. The optional time code reader accepts IRIG (A or B) time codes. You can program it to utilize system time in the absence of an IRIG time source.

The 4422's VME-64 interface allows high rate block mode transfers across the VME backplane. This makes the 4422-V64 the optimal platform for multi-stream configurations. It is the perfect choice for ground stations requiring premium performance in the most compact form factor.



4422-V64

Configurations

Model Number	Configuration
4422-V64	Telemetry Module for VME bus
-1XXX	IRIG lime Code Reader option
-X1XX	Unique Recycling Code option
-XX1X	64KB Dual Port RAM Upgrade option

Specifications

Performance

Decommutator

- Input data rate: 10 bps to 20 Mbps
- Word length: Variable (VWL), programmable 3 to 16 bits on a word-by-word basis
- Minor frame length: 2 to 8,191 words per minor frame
- Bit order: MSB-first or LSB-first (programmable word-by-word unless PDD operation selected)
- Frame sync pattern: Up to 64 bits
- Frame sync location: Leading or trailing the frame
- Frame sync strategy: Adaptive mode (search-verify-lock) & burst mode (search-lock)
- Sync error tolerance: Programmable 0 to 15 bits
- Sync slip window: Programmable 1- or 3-bit window
- Data polarity: Normal, inverted, or automatic
- Major frame length: Up to 256 frames
- Subframe sync: FCC, SFID or URC (optional)
- URC Location: May be any 64-bit window within the first minor frame
- SFID Location: Any contiguous bits not including the last bit in the minor frame
- Parallel output: TTL-compatible. May be patched to VME bus P2 User I/O pins
- System output: Buffered PCM data blocks up to 256 frames

Simulator

- Output levels: TTL-compatible RS-422 except Bipolar PCM Output (1 V p-p into 75 ohms)
- Outputs: 0° & 90° clocks, minor frame strobe, NRZ-L data, and encoded PCM data (TTL and bipolar)
- PCM codes: PCM Outputs may be NRZ-L/M/S, BIØ-L/M/S, DM-M/S, M^2, M^2-S, RZ RNRZ11, RNRZ15, inverted NRZ-L, BIØ-L, or RZ
- Output data rate:
  1.0 bps to 10.0 Mbps (NRZ codes)
  1.0 bps to 5.0 Mbps (BIØ, DM, RZ codes)
- TTL-Compatible external clock: 1 Hz-10 MHz
- Word length: Variable (VWL), programmable 3 to 16 bits on a word-by-word basis
- Bit order: MSB/LSB-first
- Minor frame length: 2 to 4,095 words per minor frame
- Frame sync pattern: Up to 256
- Major frame length: Up to 256 frames
- Subframe sync: FCC, FAC or SFID
- SFID location: Any contiguous bits in any word in the minor frame
- Common words: Based on word location, up to 2,048 values
- Unique words: Up to 7 programmable
- Dynamic words: Up to 5 programmable via look-up tables

IRIG Time Code Reader

- Input format: IRIG A or B
- Carrier (non-real time): 250 Hz to 20 kHz
- Input level: 1 V (peak-to-peak) nominal
- Latency: 2 µs (maximum)
- Outputs: Automatic time tags for PCM data blocks
- Alternate Source: System Clock

Bit synchronizer (optional): See bit synchronizer data sheet

- 4400-TF (250 bps to 10.0 Mbps)

Mechanical

- Form factor: Single 6U x 160 mm VME card
- Connectors: 3 BNCs, DB25 & 50-pin parallel output header

Environmental

- Operating
  Temperature: 0˚ to 50˚ C
  Humidity: 0% to 90% non-condensing
- Non-Operating (in proper packing)
  Temperature: -25˚ to +70˚ C
  Humidity: Packing must prevent contact with moisture and contaminants
- Special Handling
  Standard ESD handling methods are recommended

Customer Support

- Two-year warranty
- Hardware documentation included on CD. Hard copies of the documentation are available upon request.

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