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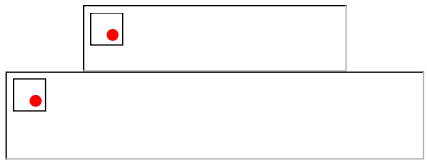
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CES Processor Boards





- PowerPC 603rxx / 604r / Arthur 740 from 166 to 466 MHz
- 1 Mbyte L2 cache
- Up to 256 Mbytes EDO DRAM
- Up to 16 Mbytes Flash EPROM
- CompactPCI™ system or peripheral slot
- I2o compatible message unit
- 2 x DMA channels
- Address re-mapping
- Two PCI mezzanine (PMC) slots
- Ethernet with 10baseT
- One parallel interface port
- Two serial ports
- BSP for VxWorks and ChorusOS™ with enhanced specifications
- Complete Lynx-OS self-hosted development and target packages with CES enhanced specifications

CES is capitalising on its expertise in real-time systems to introduce its first CompactPCI™ real-time oriented PowerPC card, the RIOC 4063 / 4064.

Its design characteristics have been aimed at maximum throughput, without compromise on any of the busses.

The RIOC CompactPCI™ module features a PowerPC processor (603rxx - 604r - Arthur 740) clocked at 166 to 400 MHz, up to 1 Mbyte L2 cache, 16 to 128 Mbytes EDO DRAM, 8 Mbytes of Flash EPROM, 128 Kbytes SRAM and Ethernet. The Flash EPROM is available for storage of the monitor, bootstrap procedures, ROMable kernels and user code. Two serial ports and TTL I/O signals can be connected via the front-panel or the backplane connectors. Centronics and PMC I/O lines are also implemented on backplane connectors.

One of the most critical performance bottlenecks arises in the arbitration of the system memory, which has to be shared between processor, PCI mezzanines and CompactPCI™ bus. The RIOC PowerPC-to-PCI chipset features a double arbitration structure in order to provide simultaneous RAM and PCI (80 Mbytes/sec)

accesses.

The RIOC has a primary PCI backbone with 2 PMC slots. The PMC I/O signals are routed on J3 rear-connector to provide front or rear cabling facility.

The CompactPCI™ interface is implemented through a PLX 9080 device which supports DMA chained block transfers with no additional CPU load and I2o / mailbox / doorbell synchronization for multiprocessing support.

The RIOC 4063 / 64 comes with a powerful low-level on-board monitor (PPCMon) that contains self test routines, and allows the user to examine memory locations. An optional extended monitor provides more utilities, such as board configuration, PCI resources access, user-code downloading, flash EPROMs reprogramming.



CES offers BSPs for most popular real-time operating systems (VxWorks, ChorusOS™), as well as a self-hosted Lynx-OS development and target packages. They have been specially tuned to the RIOC's functionality, and provide the user with a high-performance and comfortable development environment.
Software compatibility has been maintained with the RIO2 family, so that software development can be done on a VME platform and then downloaded to the CompactPCI™ target. Ready for CES PCI Backplane Driver !

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