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Find the *Chrislin Industries CI-VME80* at our website: ***Click HERE***

VMEbus

32MB TO 512MB HIGH-SPEED VME, VSB, &VME64
PARITY MEMORY FOR VMEbus SYSTEMS



CI-VME80

The CI-VME80 is a high-speed, high-capacity DRAM memory board with VME, VSB and VME64 interfaces. The CI-VME80 is optimized for Block Transfer Cycles yielding a bus transfer rate up to 80 megabytes per second. The memory module has cycle times of 95 nanoseconds and access times of 30 nanoseconds during block cycles. The CI-VME80 is an excellent choice for high-speed DMA and dedicated data acquisition devices that require maximum memory transfer rates into large memory capacities. The CI-VME80 fully supports the current ANSI/VITA 1-1994, ANSI/VITA 1.1-1997 and VSB Rev. C Bus Specifications. Flexible address selections allow the CI-VME80 to be placed into any VMEbus environment. The CI-VME80 has low power requirements of 2.5A (typical) at +5 volts.

FEATURES:

* Up to 80MB per second bus transfer rates
* 32MB to 512MB in one 6U VMEbus slot
* ANSI/VITA 1-1994, ANSI/VITA 1.1-1997
* 64 bit data path for higher data transfer rates
* Byte Parity Error Detection
* Addressable in 24 or 32 bit through 4 Gigabytes
* Memory start and end addresses selectable on 1MB boundaries
* LED indicators for VMEbus, VSB, D64, ALU access, and parity ERROR status

Chrislin Industries, Inc.

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SPECIFICATIONS

CI-VME80

CHARACTERISTIC	SPECIFICATION
Capacity	32, 64, 128, 256, 512 megabytes
Cycle Time	95ns BLOCK CYCLE, 195ns SINGLE CYCLE
Access Time Write/Read	30/30ns BLOCK CYCLE, 90/140ns SINGLE CYCLE
Word Size	8, 16, 24, 32, or 64 bits
Memory Addressing	32 or 24 bits, 4 GB maximum selectable on 1 MB boundaries
CSR Addressing	16 bit, selectable on 256 byte boundaries using VMEbus SHORT I/O
Modes of Operation MEMORY CSR	READ, WRITE, READ-MODIFY-WRITE, BLOCK TRANSFERS, UNALIGNED TRANSFERS READ, WRITE
ANSI/VITA 1-1994, ANSI/VITA 1.1-1997 Compatibility	SLAVE, A16, A24, A32, D8, D16, D24, D32, D64, RMW, BLT, MBLT, UAT, AD0
VSB Rev. C Compatibility	SLAVE, D32, A32, SAS, ATLAS, AD0, BLT(32)
Address Modifiers	Responds to AM Codes 3F,3E,3D,3B,3A,39,0F,0E,0D, 0B,0A,09,2D, & 29, AM Codes are programmable, ALU Cycles are selected with AM Codes 10-1F
Refresh	Internal, Distributed
Parity/Error Handling	Byte Parity
1 RED, 4 GREEN LED Indicators	RED - Parity Error, GREEN - VME, VSB, D64 & ALU cycles
Designed Temperature Range	Operating: 0 deg. C to +50 deg. C non-condensing Storage: - 20 deg. C to +70 deg. C non-condensing
Power Requirements +5 Volts	TYPICAL MAXIMUM 2.5A 2.9A 64MB Option
Dimensions	160mm x 233.35mm, 6U form factor

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