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Fastcom®: ESCC-PCI

**Dual Channel
Synchronous Communications
Adapter**

**w/2ft. Cable
DB37 Plug to two DB25 Plugs**

\$649



The FASTCOM: ESCC-PCI adapter is the one of the most advanced synchronous communications adapters in the industry. The FASTCOM: ESCC-PCI supports data rates up to 10 Mbits/s¹. The FASTCOM: ESCC-PCI is a dual channel adapter, with each one being individually configurable to use HDLC/SDLC, ASYNC, or BISYNC protocols. Although similar to the HSCX adapter, the ESCC-PCI expands on our previous adapters with options that allow for:

- CRC generation either CRC-32 or CRC-CCITT polynomials (CRC-16/CCITT for BISYNC)
- CRC reset state all 1's or all 0's
- Manchester line encoding
- FM encoding (Bi-Phase Space/Bi-Phase Mark)
- Preamble transmission (with repetition)
- BISYNC operating mode, receiving and sending BISYNC formatted data
- ASYNC operating mode, receiving and sending ASYNC formatted data, in true asynchronous (no clocks, data over sampled) or isosynchronous (async data format with no over sampling)
- Software drivers are included for Windows 2000, XP, 2003, 2003x64, and Linux kernels 2.4 and 2.6

SPECIFICATIONS:	<ul style="list-style-type: none"> • COMM. CONTROLLER: <ul style="list-style-type: none"> ◦ SIEMENS SAB82532 (Data Sheet) • DRIVERS/RECEIVERS: RS422 / RS485 • CONNECTOR CONFIGURATION: <ul style="list-style-type: none"> ◦ ANSI/EIA/TIA-530-A-1992(Category 1) • BUS INTERFACE: PCI
POWER REQUIREMENTS:	<ul style="list-style-type: none"> • +5V @ 300mA (typical)
ENVIRONMENT:	<ul style="list-style-type: none"> • Operating Temperature Range: 0 to 70 C • Humidity: 0 to 90% (non-condensing)
FEATURES:	<ul style="list-style-type: none"> • High speed; up to 10Mbits/s¹ • User programmable on-board clock generator • Dynamic data rates

- Drivers: RS422 / RS485 multi-drop
- Support Full-Duplex and Half-Duplex modes
- Excellent line noise immunity
 - Cable lengths up to 4000 feet
- Easier to program and use than other HDLC adapters
- **RS485 MODE:**
 - 32 Fastcom: ESCC-PCI adapters can share the same "twisted pair"
 - Driver control is automatic
- **SERIAL INTERFACE:**
 - **Two independent full duplex serial channels**
 - On chip clock generation or external clock source
 - On chip DPLL for clock recovery of each channel
 - Two independent baud rate generators
 - Independent time-slot assignment for each channel with programmable time-slot length
 - Async, sync character oriented (MONOSYNC/BISYNC) or HDLC/SDLC modes (including SDLC LOOP)
 - Transparent receive/transmit of data bytes without framing
 - NRZ, NRZI, FM and Manchester encoding
 - Modem control lines (RTS, CTS, DTR, DCD, DSR)
 - **CRC support:**
 - HDLC/SDLC: CRC-CCITT or CRC-32
 - BISYNC: CRC-16 or CRC-CCITT
 - Statistical multiplexing
 - Continuous transmission of 1 to 32 bytes possible
 - Programmable preamble (8 bit) with selectable repetition rate (HDLC/SDLC and BISYNC)
 - Collision resolution
 - Programmable bit inversion
- **Protocol Support: (HDLC/SDLC)**
 - **Various types of protocol support depending on operating mode**
 - Automatic (automatic handling of S- and I-frames; not auto configure)
 - Non-auto mode (manual)
 - Transparent mode
 - Handling of bit-oriented functions
 - Support of LAPB/LAPD/SDLC/HDLC protocol in auto mode
 - Programmable time-out and retry conditions
 - Programmable maximum packet size checking
 - Modulo 8 and 128 operation
 - Storage of up to 17 short received frames
- **MP interface and Ports**
 - 64 byte FIFO's per channel and direction
 - 8/16 bit microprocessor bus interface
 - All registers directly accessible

- | | |
|--|--|
| | <ul style="list-style-type: none">• Cable, documentation, and software drivers included• Designed and manufactured by COMMTECH, INC. Wichita, Kansas, USA• For a more complete and detailed list of features available on this card, please see the documentation for the Siemens ESCC2 SAB82532. |
|--|--|

¹Capable of 10 Mbits/s, actual rate depends on interrupt handling capabilities of your PC.



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