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Specification Sheet: VP PSE/P34

- High Performance 850MHz Pentium® III VME SBC

CENTRAL PROCESSOR

- Pentium III operating at 850MHz:-
  - using a Socket-370 FC-PGA (Flip-Chip Pin Grid Array) CPU
  - 32 Kbytes internal (Level 1) CPU cache
  - 256 Kbytes of internal secondary (Level 2) cache - called Advanced Transfer Cache - full speed, 256-bits wide, 8-way associative
- supports 100MHz system bus frequencies
- utilizes Intel's 82440BX chipset
- no CPU fan
- ITP debug port for Pentium III FC-PGA

DRAM

- supporting up to 256 Mbytes S-DRAM:-
  - 64 or 128 Mbytes soldered to board; and,
  - 64 or 128 Mbytes provided by a 144-pin SO-DIMM
- 64-bits wide using 3.3 Volt S-DRAMs
- accessible from Pentium III, PCI bus and VME

HARD DISK INTERFACES

- Ultra-SCSI 8-bit and Ultra-Wide SCSI 16-bit:-
  - single-ended asynchronous or synchronous, with active termination and signal negation
  - utilizing Symbios Logic 53C875 SCSI I/O Processor
  - synchronous SCSI data rates

PCI EXPANSION INTERFACE FOR PMC

- 1 x PMC slot with I/O via front panel - within a single VME slot
- provides connection to a PMC Carrier Board (which provides 2 PMC slots - these 2 slots are in addition to the on-board PMC)

OTHER PERIPHERAL INTERFACES

- keyboard interface via PS/2™ type connector on front panel
- mouse interface via PS/2 type connector on front panel
- PC-compatible Real Time Clock
- floppy disk interface via P2 connector
- parallel printer port interface (ECP, EPP and IEEE1284) via P2 connector.
- two USB (Universal Serial Bus) interfaces: one via front panel, other via P2 connector
- Watchdog timer

FLASH EPROMS

- 512 Kbytes of BIOS Flash EPROM

FIRMWARE SUPPORT

- PhoenixPICO™ BIOS
- comprehensive Power-On Self-Test (POST)

SOFTWARE SUPPORT

- support for VxWorks®, WindowsNT®, Solaris™, Linux®, MS-DOS® and QNX®

VME INTERFACE

- implemented using Tundra Universe II device

    - up to 40 Mbytes/sec
    - accessed via P2 connector
- EIDE interface:-
    - accessed via P2 connector
    - supports Ultra-DMA 33 for high performance drives
    - connects to an optional hard/floppy/flash drive board (Mass Storage Module) (DS MSS/IFP)
- DiskOnChip (optional):
    - available via a 32-pin (DIL) dual-in-line socket

ETHERNET INTERFACE

- supports 10Base-T and 100Base-Tx for UTP CAT-5
- implemented by a 21143 LAN Controller
- accessed by an RJ45 front panel connector
- programmable station ID

GRAPHICS INTERFACE

- implemented by a Asiliiant Technologies (ex CHIPS®) 69030 providing:
    - 4 Mbytes video memory
    - resolutions up to 1600 x 1200
    - up to 16M colors
    - flat panel supported via DFP (Digital Flat Panel) interface

SERIAL INTERFACE

- 1 x RS232 serial channel
- 16550 compatible UART
- accessed by 1 x 9-pin D-type connector on front panel

- VME Master/Slave
- A32/A24/A16 /D64/D32/D16/D8(EO) /MBLT64
- fast hardware byte swapping
- auto system controller detect
- full interrupter / interrupt handler support
- 4 x 32-bit mailbox register
- location monitor

ELECTRICAL SPECIFICATION

- all voltages to be within ±5%
- +5V@6.7A (typical with 64 Mbyte DRAM)
- +12V@0.0A; -12V@0.0A
- +12V and -12V routed to PMC Expansion from VME bus

ENVIRONMENTAL SPECIFICATION

- 0°C to +55°C (operating)
- 10% to 90% Relative Humidity (operating)
- -40°C to +70°C (storage)
- 10% to 90% Relative Humidity (storage)

MECHANICAL SPECIFICATION

- 6U form-factor
- single VME slot - front panel width 0.8inch (20.3mm)
- utilizes a 160-way DIN connector for P1 and P2
- compatible with normal 96-way DIN connectors

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