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Find the *Concurrent Technologies PP 110/012* at our website: *Click HERE*

Specification Sheet: PP 110/01x

 Photograph  Block Diagram  Full Datasheet

CENTRAL PROCESSOR

- Pentium III Processor operating at 1.2GHz or 800MHz:-
  - uses a Flip-Chip package
  - 32 Kbytes internal (Level 1) on-die cache
  - 512 Kbytes of secondary (Level 2) on-die cache
  - no CPU fan; 0.13 micron low power processor
- utilizes 64-bit ServerWorks' ServerSet™ III LE chipset:-
  - supports 133MHz bus frequency
- Pentium III (ITP) debug port

DRAM

- supporting up to 1.5 Gbytes of 133MHz ECC S-DRAM:-
  - 512 Mbytes or 1 Gbyte soldered on the board; and,
  - up to 512 Mbytes provided via a SO-DIMM socket
  - single bit error correction; double-bit error detection
- accessible from Pentium III processor or CompactPCI bus

HARD DISK INTERFACES

- EIDE interface:-
  - supports up to Ultra-DMA 100 for high performance drives
  - two channels (primary and secondary)
  - secondary channel can be used for on-board 2.5 inch hard disk drive (within a single slot); or, support for 1 or 2 CompactFlash modules or IBM Microdrive Type II drives
  - primary channel is accessible via Transition Module

ETHERNET INTERFACES

- implemented by Intel 82546EB LAN Controller via 64-bit PCI bus
- two channels supporting 10 Base-T, 100 Base-TX, 1000Base-T
- support for PICMG 2.16 - Packet Switching Backplane (PSB)
- optional support for rear panel RJ45's via Transition Module
- one channel switchable to front panel

OTHER PERIPHERAL INTERFACES

- keyboard and mouse interfaces accessed via a 26-way high-density connector on front panel
- PC-compatible Real Time Clock (Year 2000 compliant)
- Watchdog timer
- system fan monitor; speaker interface; CPU temperature monitor; voltages monitor:-
  - all accessible via IPMI
- 1 x USB (Universal Serial Bus) interface via Transition Module
- 1 x 32-bit Long Duration Timer with processor interrupt capability
- LPC (Low Pin Count) bus via J5 to enable the AD PP5/001 Transition Module support for:-
  - floppy disk interface
  - parallel printer port interface (ECP, EPP and IEEE1284)
  - 2 x RS232 serial channels (COM2 and COM3)

FIRMWARE SUPPORT

- PhoenixPICO™ BIOS
- comprehensive Power-On Self-Test (POST)
- LAN boot firmware included

SOFTWARE SUPPORT

- support for VxWorks®, Windows NT®, Windows® 2000, Windows® XP Embedded, Solaris® and Linux®

COMPACTPCI INTERFACE

- compliant with PICMG 2.0 R3.0; 3.3V or 5V signaling levels
- 33/66MHz; 32/64-bit interface accessed via J1/J2 connectors
- utilizing a PCI-PCI bridge for off-board accesses
- operates as a System Slot controller or in a Peripheral slot
- PICMG 2.1 R2.0 Hot Swap Specification compatible
- option to disable CompactPCI interface (Satellite Mode):-
  - receives power from CompactPCI bus
  - board can be hot swapped in this mode
- J4 connector not fitted

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- one channel switchable to front panel RJ45, supports 10 Base-T and 100 Base-Tx

GRAPHICS INTERFACE

- implemented by an Asiliant Technologies' 69030 providing:-
  - 4 Mbytes video memory
  - resolutions up to 1600 x 1200
  - supporting up to 16M colors
- accessed via a 26-way high-density connector on front panel

PMC (PCI MEZZANINE CARD) INTERFACE

- 2 x PMC sites; for both sites:-
  - I/O via front panel
  - I/O via J3 and J5
  - 32/64-bit PCI operation, and
  - 33/66MHz PCI operation
- Transition Module provides rear I/O for both PMC sites

SERIAL INTERFACE

- 1 x RS232 serial channel (COM1)
- 16550 compatible UART
- accessed via a 26-way high-density connector on front panel
- channel supports RI, CTS, RTS, DSR, DTR and DCD via front panel
- Transition Module supports 2 additional RS232 serial channels

BATTERY BACKED SRAM

- 512 Kbytes battery backed SRAM

FLASH EPROMs

- 32 Mbytes of Application Flash EPROM - 8-bits wide
- 512 Kbytes of BIOS Flash EPROM - 8-bits wide

IPMI (INTELLIGENT PLATFORM MANAGEMENT INTERFACE)

- PICMG 2.9 R1.0 (System Management Specification)
- implements the IPMB0 interface
- on-board BMC (Baseboard Management Controller)
- auxiliary IPMB interface to either J2 (IPMB1)
- supports 8 Kbytes of non-volatile memory

ELECTRICAL SPECIFICATION

- +5V@5.0A (typical at 1.2GHz with 512 Mbytes DRAM); +5% / -3%
- +3.3V@3.9A; +5% / -3%
- +12V@0.0A; -12V@0.0A
- +12V and -12V routed to PMC expansion slots

SAFETY

- PCB (PWB) manufactured with flammability rating of 94V-0

ENVIRONMENTAL SPECIFICATION

- 0°C to +55°C (operating); for extended temperature version see PP 110/010-E
- 10% to 90% Relative Humidity (operating non condensing)
- -40°C to +70°C (storage)
- 10% to 90% Relative Humidity (storage non-condensing)

MECHANICAL SPECIFICATION

- 6U form-factor: 9.2inches x 6.3inches (233mm x 160mm)
- single-slot: 0.8inches (20.3mm)
- connectors: IEC-1076-4-101 for J1-J5
- shock:
  - 20g, 11ms, ½ sine (operating)
  - 30g, 11ms, ½ sine (non-operating)
- vibration:
  - 5Hz-2000Hz at 2g, 0.38mm peak displacement (operating)
  - 5Hz-2000Hz at 5g, 0.76mm peak displacement (non-operating)

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