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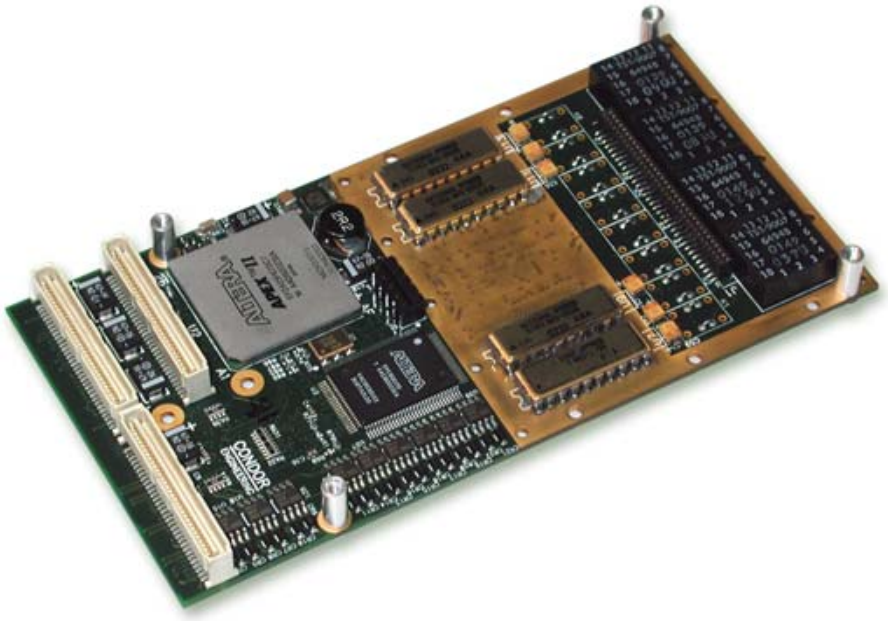
Find the *Abaco Systems / Condor Engineering QPMC-1553-2M-W* at our website: *Click HERE*

MIL-STD-1553 High Density PMC Interface QPMC-1553

Features

1, 2 or 4 Independent MIL-STD-1553 Dual Redundant Channels

- **Multi-function Features**
  - Simultaneous Bus Controller, 31 Remote Terminals and Bus Monitor
- **Single-function Features**
  - Bus Controller or 31 Remote Terminals or Bus Monitor
- **Bus Controller - BC**
  - BC->RT, RT->BC, RT->RT
  - Mode Codes, Broadcast and single-shot messaging
  - Programmable time delays
  - Major/Minor frames
  - Real-time conditional branching
  - Two aperiodic messaging methods
- **Remote Terminal - RT**
  - RT data wrapping
  - Multiple RT buffers
  - Dynamic Bus Control
  - Automatic Mode Code and Status Bit responses
  - Programmable response time
  - RT Map Monitoring
- **Bus Monitor - BM**
  - Full error detection
  - Multiple monitoring methods
  - 45-bit time-tagging
  - Adv. interrupts and triggers
- **Architecture**
  - BC & RT error injection/detection
  - DYNAMIC architecture
  - BC & RT link list structures
  - 1 Mbyte RAM per channel
  - Direct & transformer coupling
  - Environmental options
- **Software Support**
  - Advanced, high-level API
  - Source code included
  - ***BusTools*** Analyzer optional



Condor's QPMC-1553 provides new levels of performance and flexibility for MIL-STD-1553A/B Notice II in the PMC form factor. Available in commercial, ruggedized and conductively cooled versions with one, two or four dual-redundant channels, the QPMC-1553 includes advanced API (Application Programming Interface) software that reduces application development time. Standard features include selectable transformer or direct coupling, 1 Mbyte of RAM per channel, 45-bit message time-tagging, triggers, extensive BC & RT link-list structures, error detection/insertion, automatic/manual RT Status Bit and Mode Code responses, along with advanced BC functionality. An IRIG B signal Receiver/ Generator with GPS synchronization, variable voltage output and RS-422/485 outputs are optional. With the highest speed encoder/ decoder in the industry, the QPMC-1553 Bus Monitor provides unparalleled error detection and 100% monitoring of a fully loaded buses.

Multi-function Interfaces

QPMC-1553 multi-function interfaces are easily configured to operate with simultaneous Bus Controller, 31 Remote Terminals and Bus Monitor functionality.

Single-function Interfaces

Single-function QPMC-1553 interfaces have all the features and functionality of the multi-function versions, but only one major operational mode is enabled at a time. Each interface can emulate either a Bus Controller or 31 Remote Terminals or Bus Monitor.

Software

Condor provides our advanced 1553 API in source code, along with support for Windows XP, 2000, Me, NT, 98, 95, Linux, VxWorks and other operating systems (Solaris support optional). To access 1553 functionality without software development, ***BusTools***/1553, Condor's MIL-STD-1553 bus analysis, simulation and data logging/monitoring solution is available.

CONDOR ENGINEERING

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MIL-STD-1553 High Density PMC Interface QPMC-1553

Specifications

Physical
- Single-wide PMC card (74mm x 149mm)

Environmental
- Commercial operating temp. range: 0°C to +70°C
- Optional temperature range : -40°C to +71°C
- VITA 20-200x conduction cooled option
- Available in Commercial, Ruggedized and Conduction Cooled configurations

Software Support
- API - High-level libraries with source code included for Windows XP, 2000, Me, NT, 98, 95, Linux and VxWorks
- GUI - Optional ***BusTools***/1553 GUI bus analyzer
- Contact the factory about Solaris support

Connections
- Direct or transformer coupling
- Input and output triggers
- Front panel bezel or P14 I/O connections
- Transition cabling to 1553 cable jacks included on front panel configurations

Multi-function Operational Modes
- Simultaneous BC, 31 RTs and BM

Single-function Operational Modes
- BC or 31 RTs or BM

Power (4 channels at 75% duty cycle)
- +5 VDC @ 2.20A
- +3.3 VDC @ 150mA
- 7.2 W power dissipated on board

On-board Shared RAM
- 1 Mbyte (per dual-redundant channel)

Options
- Variable voltage transceivers
- IRIG B Receiver/Generator
- Multiple avionics discretes
- RS-485/422 support for EBR/MMSI only

PCI Signal Compatibility
- Universal (5V or 3.3V)

Warranty: 3 year limited hardware warranty
No cost library card driver upgrades

Description

Bus Controller
- Programmable control over:
  - Major and minor frame content and timing
  - Intermessage gap times
  - Response time-out and late response
  - Multiple BC retry
- Modify messages, data or setup while card is running
- Insert aperiodic messages into a running BC list
- "Oneshot" mode for simplified BC operation
- Conditional message sequencing based on real-time message data or status
- Selectable interrupt generation and status messages
  - Full range of system conditions
  - All detected errors
- Full error detection
  - Invalid word
  - Bit count error
  - High word
  - Low word
  - Inverted sync
  - Manchester
  - Late response
  - Early response
  - No response
  - Incorrect RT address
  - Parity error
- Extensive programmable error injections (on a per word basis)
- Synchronize BC operation to external time source

Remote Terminal
- Multiple RT simulation (up to 31 RTs)
- Programmable error injection (on a per word basis)
- Modify data, status words or setup while card is running
- Programmable message content (linked message buffers)
- Interrupts can be generated on a per message basis upon End of Message and error conditions
- RT Map Monitoring

Bus Monitor
- Capture 100% fully loaded bus traffic with:
  - Time-tagging
  - Word status
  - RT response time
  - Error status
  - Message status
- Interrupts can be selected by RT / SA / WC
- Extensive filtering and triggering options
  - By individual RT/subaddress
  - Transmit, receive or broadcast mode codes
  - Internal or external triggering
  - Trigger output on user specified data
- Real-time bus playback with RT edit mode
- 45-bit, microsecond resolution timetagging
- IRIG/GPS synchronization

Available Configurations

See our on-line Configuration Guide for a complete list of available configurations

QPMC-1553-1M	MIL-STD-1553 multi-function, single channel, fixed voltage PMC interface
QPMC-1553-2M	MIL-STD-1553 multi-function, two channel, fixed voltage PMC interface
QPMC-1553-4M	MIL-STD-1553 multi-function, four channel, fixed voltage PMC interface
QPMC-1553-1S	MIL-STD-1553 single-function, single channel, fixed voltage PMC interface
QPMC-1553-2S	MIL-STD-1553 single-function, two channel, fixed voltage PMC interface
QPMC-1553-4S	MIL-STD-1553 single-function, four channel, fixed voltage PMC interface
-D suffix	Rear I/O (P14) configuration
-W suffix	IRIG B Receiver/Generator option
	Other configurations available (special order)

A channel is a dual-redundant A/B pair

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