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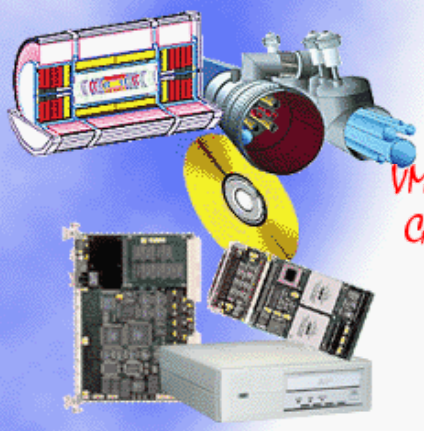
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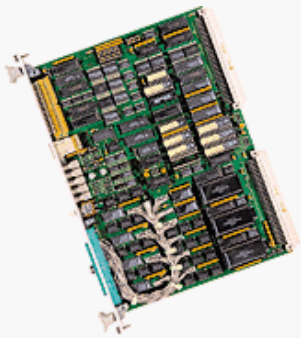
Find the *Creative Electronic Systems CBD 8210* at our website: ***Click HERE***

CES Products for Physics



CBD 8210
CCA2 2110

VME to CAMAC A2 Controller
CAMAC A2 Crate Controller



CBD 8210: companion of the CCA2 2110

- Drives a full CAMAC branch from a VME board
- Supports both 16-bit and 24-bit data transfers
- Complete set of CAMAC parameters memory mapped
- Multi-crate addressing capability
- Extended GL handling facilities
- Front panel interrupts

The CBD 8210 allows a CAMAC branch (EUR 4600) to be driven from VME. Up to seven CAMAC crates can be directly accessed on the branch highway from a VME master.

The command mode used is "programmed transfer" which allows simple software with efficient transaction speed for most applications.

CCA2 2110: companion of the CBD 8210

- Front panel display of N, X, Q, BD, EnBD, ACL, REQ, I and crate control
- User programmable identification (32 bytes)
- Crate control output and extended CAMDEB

The A2 crate controller is a CAMAC parallel crate controller designed to support simultaneous accesses to the dataway from both host and auxiliary processors. The auxiliary control bus (ACB) located at the rear of the unit allows connection of up to eight auxiliary processors. The CAMAC access protocol takes care of all possible conflicts in dataway access.

Two features have been incorporated into the A2 Crate Controller with the object of providing more

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Real-time events are handled via a pair of front panel interrupt inputs which are equipped with "ACK" outputs to provide simple interrupt handling.

Software interrupts can also be generated via the Interrupt "ACK" outputs. A manual branch Z switch is located on the front panel. A programmed BZ is also available. A branch control signal output (on the front panel), monitors the activity on the branch generated by the CBD board.

A comprehensive set of LEDs displays all the important CAMAC parameters driven by the module.

complete information on the behavior of the crate in real-time: when the host computer has gained control of the crate, both visual indication and logic signals are available from the front panel, secondly an extension of the ACB specifications renders the "EN" encoded N lines bidirectional; thus, they can be latched in the ACB and dataway service module. These two features enable a permanent monitoring of the two most important parameters in multiprocessor systems, namely which processor (host or auxiliary) gained control of the crate and which station was accessed.

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