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Scalable System Solutions Balancing Space, Power & Price with Next Generation Processor Technology

2840 MultiComputer

The 2840 MultiComputer utilizes AltiVec™ Technology, which provides leading edge, general-purpose processing performance, while concurrently addressing high-bandwidth signal processing and algorithmic-intensive computations in a single-chip solution. The MPC7400 processor combined with Myrinet gigabit-per-second network technology allows the 2840 MultiComputer to deliver an open and expandable system solution for high performance and signal processing computing applications. Packaged in an industry standard 6U VME form factor, the 2840 MultiComputer delivers over 14 GFLOPS of peak computational power for a variety of applications, including Radar, Sonar and Simulation.

The 2840 MultiComputer uses the same two-level MultiComputer architecture, as other processor modules in the 2000 SERIES family. It is designed with an 8-port crossbar switch to interconnect the four processing nodes on the board and allows for additional off-board communication. Each processing node consists of two levels: a first level for managing network communications, and a second level for compute processing. The 64-bit Local Area Network Application Interface (LANai) includes a 32-bit RISC processor, a dedicated 2 MB fast buffer memory (SRAM), the Myrinet high-speed network interface, and DMA engines for packet control. The second level, for compute processing, consists of a processor interface and a PowerPC™ MPC7400 processor with AltiVec™ Technology with 1 MB of L2 Cache, 64 MB of SDRAM and 8 MB of flash memory for boot-up and self-test. With this implementation, data transfers do not affect floating computational speed.

The high-performance, scalable two-level MultiComputer design, utilizing the latest processor technology, makes the 2840 MultiComputer the best choice for real-time embedded applications.



128-bit by 32 Words Register File for Fast Data Transfer

162 New Instructions for Programming Efficiency

Parallel Processing for Fast Data Processing

Vector Operations for On-chip Signal Processing

Simultaneous Execution of up to Four 32-bit Operations in a Single Clock Cycle for Maximum Performance

"One Part -- One Code Base" for Easier Programming

100% Compatibility with PowerPC™ Architecture for Code Portability

2820 MultiComputer

The versatile design of the 2820 MultiComputer allows the user to purchase this product as either a Motherboard or a Daughtercard. The 2820 MultiComputer consists of two processing nodes. As a Motherboard, the 2820 MultiComputer can stand alone or be coupled with a 2820 Daughtercard,growing the system to a 2840 MultiComputer. The 2820 MultiComputer Daughtercard can also be

configured with other 2000 SERIES products, such as the Front Panel Data Port (FPDP), a 32-bit parallel direct interface to the high-speed Myrinet network. Both the 2820 MultiComputer Motherboard and Daughtercard provide the flexibility needed for developing a powerful real-time embedded system.

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