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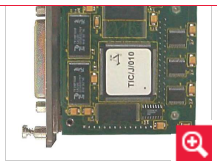
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Bi-level input (counters, time tagging) - 24ch



Description

The KAD/DSI/003 monitors the status (high/low) of up to 24 D/E discrete channels. The first eight input channels have alternative functions as programmable counters, of which four can be configured as resettable event counters. The remaining 16 channels can be used to trigger time-tagged events.

All 24 discrete signals are sampled simultaneously and can be read as a 24-bit word (16-bit + 8-bit or 12-bit + 12-bit). The KAD/DSI/003 uses 24 separate 14-bit A/Ds operating continuously at 111ksps to sample each input so the threshold and hysteresis can be programmed separately for each channel.

The first eight counters (channels 0 to 7) can be programmed as PERIOD, FREQUENCY, EVENTS_SINCE_LAST_READ, EVENTS or ELAPSED time. The range of each counter is programmable as is the threshold, hysteresis sensitivity to the rising/falling edge, and roll-over. If the counter uses more than 16 bits, they are treated as two words.

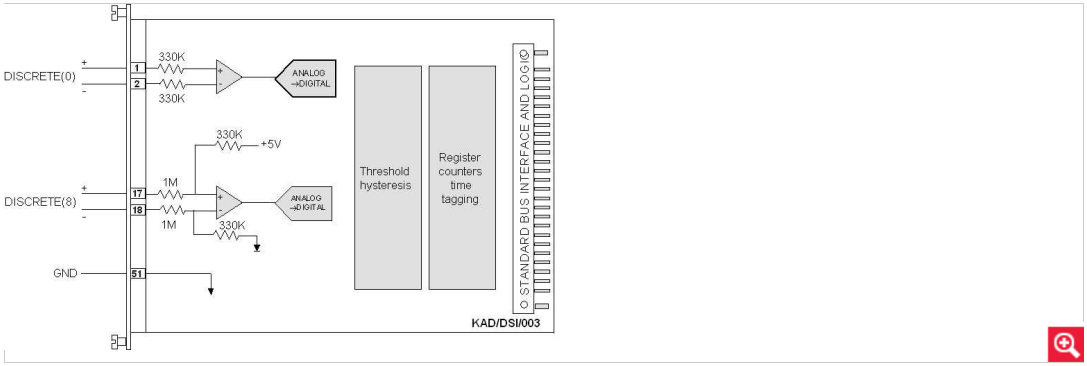
The remaining 16 channels (8 to 23) control the time tagging to the 8K FIFO. For each input, time tagging can be triggered by a rising and/or falling edge, or neither (when the channel is disabled). Every time a trigger occurs, a 64-bit word is written to the FIFO consisting of the status of the 16 inputs (after the change) and the Binary Coded Decimal (BCD) time of when the event happened (up to 23 hours 59 minutes 59.99 9999 seconds).

Features

- 24 differential ended (D/E) discrete bi-level input channels
- 8 x 32-bit counters (Period, Frequency, Events Since, Elapsed, Read, Events, Reset, Counter)
- First In First Out (FIFO)-based time tagging
- Resettable counters
- Independent channel programmability- Threshold- Hysteresis (for a slow slew rate, high noise applications)- Falling/rising edge detect

Applications

- Data acquisition systems
- Engine speed measurement



First of eight counter inputs and first of sixteen discrete inputs

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