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Camera Link Module

(CAML-MOD3)

The Camera Link module provides either two [base] camera inputs or one [medium/full] camera input. Base mode provides 8-bit input; medium and full provide 16- and 24-bits respectively. 24-bit is suitable for RGB cameras (R: 8-bit, G: 8-bit and B: 8-bit). Ideal applications include inspection systems and medical imaging.

Figure 7: CAML-MOD3 Block Diagram

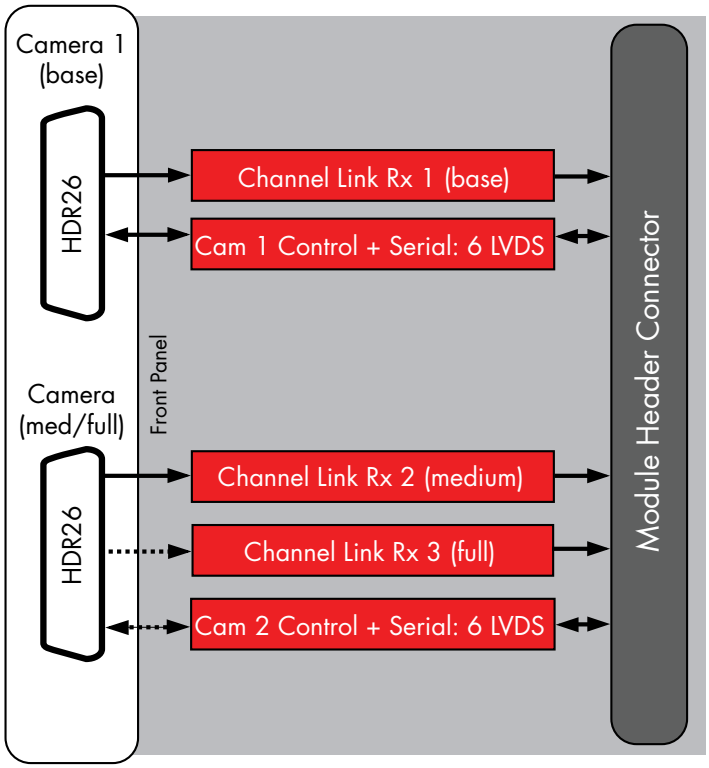
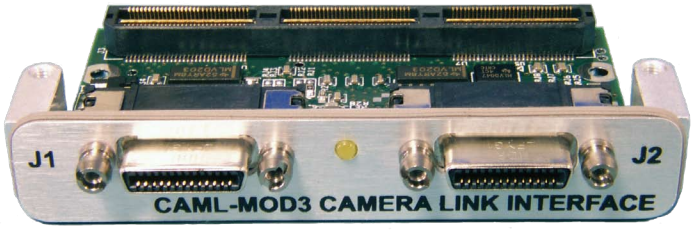


Table 3: CAML-MOD3 Specifications

Camera Link Inputs	
Front Panel J1	Base camera
Front Panel J2	Medium/Full extension, or 2nd Base camera
Connector type	Mini Camera link, Honda HDR-EA26LFYPG1- SLG
Channel Link Receiver	National Semiconductor DS90CR288A
Maximum Bandwidth (at 85MHz Camera Link clock)	Base mode: 255MB/s, per camera Medium mode: 510MB/s, 1 camera Full mode: 680MB/s, 1 camera
Camera Control & Serial Communication	CC[4..1]: Fully implemented Serial UART: Implemented in FPGA, 9600 to 921600 Baud

Figure 8: Camera Link Interface Module



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