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CHAMP-C6x



Board Photo

Processor

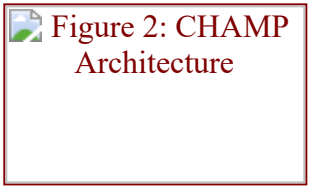
- Four TMS320C6701 processors at 167 MHz or Four TMS320C6201 processors at 200 MHz
- Up to 4 GFLOPS floating point processing or 6400 MIPs integer processing
- 250 MHz MPC8240 PowerPC™ processor provides 6.1 SPECint95 of user processing



Functional Block Diagram

Enhanced I/O

- 1 Gbyte/sec cumulative I/O data movement
- Advanced CHAMP architecture allows for concurrent I/O and VMEbus transfers
- IXStar™ DSP to PCI bridge supports dual DMA with 3D-striding and broadcast operations
- Support for two industry standard 64-bit/66 MHz PMC I/O expansion modules



CHAMP Architecture

Memory

- Up to 32 Mbytes SDRAM per 'C6x DSP
- Up to 64 Mbytes SDRAM for MPC8240 global memory
- Up to 1 Mbyte SBSRAM per 'C6x DS



General Description

The CHAMP-C6 is the latest in the line of high-performance Digital Signal Processing (DSP) solutions from Ixthos. In a single VMEbus compatible 6U sized slot, the CHAMP-C6 packs up to 4 GFLOPs of floating point power or 6400 MIPS of integer processing. Supporting variants with either the TMS320C6701 floating point processor or the TMS320C6201 fixed point DSP, the CHAMP-C6 is ideal for a wide variety of radar, sonar, and image processing applications that require very high throughput and a flexible I/O structure supporting industry-standard PCI Mezzanine Cards (PMC). Availability of rugged and conduction-cooled versions allow the application of the CHAMP-C6 in harsh aerospace, ground mobile and industrial environments, including avionics, vetronics, and space-based platforms.

The architecture of the card, shown in Figure 1, includes the ability to configure three isolated sections of a 64-bit, 66 MHz PCI bus, allowing the embedded PowerPC™ processor to access the VMEbus concurrently with I/O operations between both PMC sites and the DSPs. PCI bridges allow for efficient data transfers between the independent processing elements. The sections of the PCI bus are backward compatible with 33 MHz and/or 32-bit PMC modules. The embedded PowerPC™ processor has access to the VMEbus, I/O, and the Host Port Interface from each ‘C6x processor element allowing for flexible data and control path implementations.

Digital Signal Processors

The CHAMP-C6 may be equipped with either two or four digital signal processor devices. The ability to install either the 167 MHz TMS320C6701 or the 200 MHz TMS3206201 allows for application of the CHAMP-C6 in a wide range of floating point and fixed point intensive applications. Each processor has access to both PMC sites through the IXStar™ bridge, as well as access to the global memory resources associated with the MPC8240 PowerPC™ processor. Accesses may be either DMA controlled or single cycle initiated from the DSPs.

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