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IXI1222

2 Channel 12-bit, 6.25 MHz A/D & 16 MHz D/A Converter

Features

- 2 Dual 12-bit ADC & DAC
- 2 channels of input @ 8 MHz
- 2 channels of output @ 16 MHz
- 70 dB dynamic range
- +/- 1 V input range
- 4K deep FIFO for each channel
- Flexible acquisition control
- Simultaneous sampling

Board Photo

Click to view IXI1222 Architectural Diagram

Architectural Diagram

General Description

The IXI1222 is a standard IXI interface mezzanine designed for use with compatible IXZ baseboards for digital signal processing. It has two channels of analog-to-digital converters and two channels of digital-to-analog converters. The analog-to-digital conversion is 12 bits at 8 MHz while the digital-to-analog conversion is 12 bits at 16 MHz. Each channel has a 4K deep FIFO. The IXI1222 is optimized for high speed data acquisition and generation. Each channel is independent of the other three, but the board can synchronize the clocks for simultaneous conversions. There is also a dedicated, programmable clock for each channel. The board has 11 programmable control registers and a status register for complete control of the conversions. It can generate two programmable IXI interrupts (IOINT and TIMINT1) and two programmable IXI flags (IOFLAG0 and IOFLAG1). The board has two programmable (watermark) counters, one per A/D channel.

The IXI1222 contains 4k deep FIFOs that are 12 bits wide. Data are left justified in a 32-bit field. Bits 31 (MSB) through 20 (LSB) contain the data in offset binary format. When the analog value is +1 V, the data are all logic zeros, when the analog value is -1 V, the data are all ones. Bit 16 is the out of range indicator.

The IXI family of interface mezzanines complies with the IXI Design Specifications published by Ixthos. The electrical interface to an IXI mezzanine consists of 73 signals: 32 data lines, 24 address lines, 11 control lines, digital power and ground, three analog powers (+5, +12, -12) and analog ground. These connections to the IXZ baseboard are made via an elastomeric connector that provides a high density, low resistance, zero insertion force connection. Standard board separations of 0.125" to 0.375" are low enough to ensure a single-slot solution for most applications while providing enough room for double-sided population.

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