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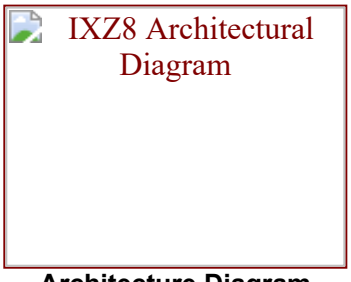
IXZ8

Real-Time MultiProcessing VME Board
Octal SHARC® with I/O





Board Photo



Architecture Diagram



Features

- 8 SHARCs™ (ADSP-2106x); 2 Clusters of 4
- 960 MFLOPS on a Single 6U Board
- 16 off-board SHARC™ IXLinks (40MB/s each)
- Three Global Memory Sites; up to 64MB each
- MultiProcessor Resource (MPR)
  Provides fast, deterministic HW semaphores, signals, and states between all SHARCs™ in system
- P2 Personality Module for flexible interconnect
- Full Featured A32/D64 VME64 Master/Slave Interface
- Compatible & Scalable with IXZ Family Boards
  (I/O and Processing)
- Comprehensive Software Support:
  - Optimizing ANSI C Compiler
  - Optimized C-callable Function Libraries
  - Multiprocessor Target Debuggers
  - OS/Host Drivers, Standard I/O and Utilities
  - Multiprocessing Applications Interface (API)

General Description

The IXZ8 is a multiprocessing building block in the IXZ Family of VME products. Eight SHARC DSPs, packing nearly a GFLOP in a 6U slot, combined with architectural flexibility, I/O, scalability, and comprehensive multiprocessing software tools, make the IXZ8 an excellent choice for a wide range of compute intensive applications. The IXZ8 may either be used as a stand-alone DSP subsystem or in arrays with other IXZ Family boards.

The IXZ8's processors are arranged in 2 clusters of 4 SHARCs. The large dual-ported internal memories in the SHARCs are augmented by up to 3 banks of global memory, as well as 1 bank of zero wait state SRAM for each cluster. Each of the 3 IXR global memory module sites on the IXZ8 may be populated with either SRAM or DRAM. A Global Resource Switch allows each of the IXR modules to be accessed by both cluster buses and the VMEbus - such that 3 separate global memory accesses can occur simultaneously.

An integrated Board Controller decouples the SHARC clusters from the VMEbus and associated hosts to reduce overhead and free the DSPs to process real time data. The IXZ8 also boasts a unique MultiProcessor Resource (MPR) that facilitates coordination of large numbers of SHARC clusters over multiple IXZ boards.

Off-board interconnectivity and I/O flexibility are key features of the IXZ8. The 16 off-board SHARC IXLink ports provide up to 640MB/s of scalable interprocessor communication bandwidth suitable for building large multiprocessor systems. A personality module allows a multitude of high speed interface options to be implemented on the 5-row VME P2 connector.

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