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Find the *Curtiss-Wright / Pentland Systems VGD4/P2/96* at our website: ***Click HERE***

VGI

VGI

Features:

- 160 MB/sec (max) Transfer Rate
- Transmit master / receiver

The VGI1 module provides for direct data connectivity to many computing platforms at board and system level via a 160 Mbytes/s FPDP interface. FPDP is emerging as a VITA standard for a front panel data highway or sub-bus in VME systems. VGI1 allows data from a VGX acquisition sub-system to be connected into Raceway (Mercury Computer Systems), array processing architectures from SKY Computers, Power PC Computers and many SHARC based DSP Processor cards.

Pentland have augmented the FPDP specification with schemes for data broadcasting to multiple receivers and also multiple transmitters to connect many VGX solutions to a single FPDP highway.

User Manual Data Sheet

SPECIFICATION

VSB INTERFACE	
Sole Master Mode	VGM1 only VSB bus master (no separate VSB arbiter)
Default Mode	More than 1 VSB bus master (VSB slot 1 arbiter required)
Address Range	Full 32 bit address capability System space
Data Transfers	D32 Block Transfer Multiple Blocks Supported
Data Transfer Rate	30 Mbytes/s (typical) Power Requirements
VME (VGM1)	+5v @ 1.5A maximum
VGX (Solution)	3A (typical)
Transfer Rate	160 MB/sec (maximum)
Protocol Compatibility	FPDP Protocol Connector Details
Connector Type	Standard FPDP type connector Mechanical Details
Form Factor	6U (when mounted on a VGX motherboard)
Solution Requirements	1 slot (when mounted on a VGX motherboard)

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