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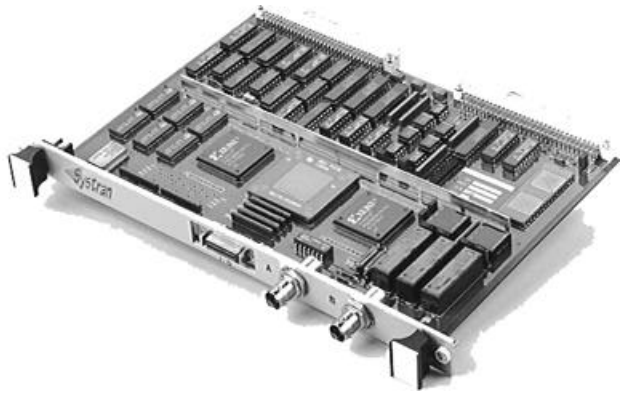
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1553 Solutions™

Gold-VMER MIL-STD-1553 Interface

- **Gate Array and DSP Technology for Ruggedized MIL-STD-1553 Applications**
- **Meets requirements for extended temperature and humidity ranges and increased shock and vibration**
- **Multi-Function capability provides Bus Controller, Remote Terminal, and Bus Monitor functions in a single board**
- **Provides Advanced Error Injection capability**



Improve system reliability.

The Gold VMER uses the latest gate array technology to eliminate most of the components on a typical TTL-based MIL-STD-1553 board. This architecture also significantly reduces power consumption. The host interface is software programmable, eliminating jumpers and switches for memory address, interrupt level and interrupt vector. The MIL-STD-1553 connection is also programmable for type of coupling, termination and connect/disconnect for self test purposes.

Simulate an entire MIL-STD-1553 system while monitoring all bus traffic.

You can analyze bus traffic by using the Chronological Bus Monitor to set up filters that sort data transfers down to the subaddress level, and monitor mode commands down to the individual mode codes.

Increase fault-handling capability with expanded interrupt packets.

Interrupt packets capture a detailed description of each interrupt. Each 12-word packet gives you a thorough explanation of each interrupt by identifying the operational mode that generated the interrupt. It also gives you interrupt type, data buffer address, transmit and receive command and status words, error information, monitor snapshot and link word information.

Insure data integrity.

Meet minimum intermessage gap time requirements with our multiple data buffer design. The data buffers are designed with handshaking logic to insure data integrity during independent BIU and host data transfers.

Two independent timers.

The BC mode has its own timer that can be used in conjunction with commands in the Interface Library for bus list timing. The Bus Monitor mode has a separate elapsed timer to time stamp all messages with 32-bit time tags. This timer uses either an external RS-422 signal or the internal 1 MHz clock.

Simplify system integration.

The memory-mapped Gold VMER, with its software programmable host and MIL-STD-1553 bus interfaces, is easy to install and configure to your system requirements.

Minimize host system integration time.

Software drivers are included with every order. Each board comes with a Bus Interface Library (BIL), which contains routines to access all registers and data structures, and Hardware Confidence Test (HCT) software. The HCT runs as an application program in the host and checks the memory, registers and failsafe timer. Source code is also included.

Save time and money with software compatibility with other Gold Cards.

As part of Systran's 1553 Solutions Gold Series, any software developed on the Gold VMER will run with minimum changes on the Gold VME, Gold VME/SE, or other Gold series boards.

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Systran™

Specifications

Hardware Features:

- Single slot 6U size VME Board
- Weight: 12 oz.
- 64K x 16 Bit Dual Port RAM

- **VME address modifiers supported:**
  Memory: A24D16, A24D32
  A32D16, A32D32
  Register: A16Dl6; 16 words

- **32-Bit Elapsed Timer (1μsec res.)**

- **External Discrete RS422 Signals:**
  Bus Instruction Execution - Output
  Elapsed timer Reset - Input
  External Clock - Input
  Bus list execution Trigger - Input

MIL-STD-1553 Features:

- 1553A and 1553B timing and protocol

- **Bus Controller Emulation**
  Instructions include: BC-RT, RT-RT, RT-BC Mode Codes, Conditional Jump (error or exception), Jump to Subroutine, Pause, Return, Unconditional Jump, Wait for ELT, and Halt

- **Multiple RT (up to 32) Emulation**
  All 32 RTs and subaddresses

- **Multiple Data Buffers**
  User-selectable Data Buffers include single message and multiple message formats

- **Chronological Bus monitor**
  Filter to RT, DIR, SA level
  Snapshot Mode/Priority Message Notification

- **Advanced Error Injection**
  Wrong Parity
  Incorrect Word Count
  No Response
  Status Exception
  Sync Errors
  Mid Sync & Mid Bit Zero Crossing Skew
  Bi-Phase encoding Error
  Bit Count Low (1 or 2 bits)
  Bit Count High (1, 2, or 3 bits)

Environmental Specifications:

- **Temperature Range:**
  Operating: -40° to +80°C
  Storage: -55° to +100°C

- **Relative Humidity** (conformally coated)**:**
  To 100% at temp. 38°0 to 65°C for 10 days
  To 93% at temp. 40°C for 56 days

- **Shock:**
  Half sine wave 6g, 11ms, three shocks in both positive and negative directions.

- **Vibration** (sinusoidal)**:**
  .25g at 75-100-75 Hz
  .12g at 150-220-150 Hz

- **Power Requirements (Max.):**

Voltage (volts)	Current (amps)
+5	2.6
+12	.190
-12	.0125

- **Gold dipped VME pin-out connectors**

- **Conformal Coating**

1553 Bus Connections:

- Jumper selectable to front panel or P2 connector

- Software selectable direct or transformer coupling, internal or external bus termination, connect/disconnect

- **Cabling Requirements:**
  Trompeter BJ 77 or equivalent

Software Support:

- Motorola UNIX drivers included (with source code)

Warranty:

- One year parts and labor on hardware and software.

Product specifications are subject to change without notice.
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