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MM-6340 Data Sheet *

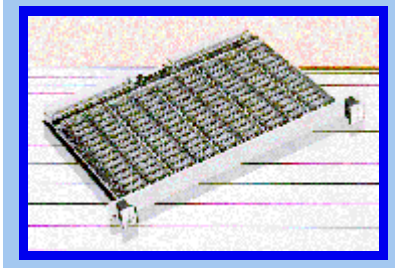
A VME/VSBbus Compatible, Dual Port, Error Correction, 16 Mbyte DRAM

This page contains features, a description, specifications, and ordering information for the MM-6340 memory module.

FEATURES:

- **Capacity:** 4M, 8M, 12M, 16M bytes
- **Error Detection and Correction** (EDC) for single-bit errors; detection for multiple-bit errors
- **Dual-Port,** VME/VSBbus-compatible
- **Cycle/Access Time (nsec) Cache Hits:**
  Longword Write: 150/125
  Word Write: 300/275
  Read: 250/275
- **Cycle/Access Time (nsec) Cache-Misses:**
  Longword Write: 240/220
  Word Write: 425/400
  Error-Free Reads: 350/375
- **Block Mode Transfer** (BLT) for sequential access of both ports
- **4K to 16K bytes Cache** utilizing Page mode
- **Module selection** on 1 Mbyte boundaries within a 16 Mbyte bank jumper-selectable for lower and upper limits of each bus. Bank selection for 16 Mbyte blocks are within the 4 Gbyte address space.
- **CSR** for error logging, reporting and diagnostics
- **Lifetime Warranty**

DESCRIPTION:



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The MM-6340D is a dual-port, high reliability Error Correcting memory module optimized for VME/VSB buses. It employs 1M DRAMs to provide up to 16M bytes in a single card slot.

The board provides high data integrity by correcting single-bit errors and detecting multiple-bit errors on each transfer. It conforms to VMEbus specification REV. C1 with 256 bytes of sequential access on block transfer (BLT) and VSBbus specification REV. B with 64 Kbytes of sequential access in the block mode.

A Control Status Register (CSR), located in the short VME I/O address space, stores and reads the status of syndrome and error bits.

Address modifiers (in a socketed IFL) are decoded to allow 24 and 32 bit masters, residing on the same bus, to access the memory simultaneously and one or the other, only if it is so required.

Reliability is also ensured by burn-in and running memory diagnostics that check operation for 48 hours while temperature-cycling boards between 0 and 60 degrees (Celsius).

SPECIFICATIONS:

Capacity
4M, 8M, 12M, 16M bytes

Error Correction
Single-bit Correction and Multiple Error detection (32 bits data + 7 syndrome bits)

Cycle/Access Time (nsec) Cache Hits:
Longword Write: 150/125
Word Write: 300/275
Read: 250/275

Cycle/Access Time (nsec) Cache-Misses:
Longword Write: 240/220
Word Write: 425/400
Error-Free Reads: 350/375

Address
32 bits for VMEbus, 32 bits multiplexed with 32 data bits for the VSBbus

Data-in/Data-out
8, 16, 32-bit (UAT)

Sequential Access
Block mode transfer of 256 bytes on the VMEbus, 64 Kbytes on the VSBbus

Address Modifiers
6 bits, decoded by a socketed IFL, supports user and supervisory modes and allows A24- and A32-bit masters to reside on the same bus

Control Status Register
16 bits, jumper-selectable, occupies one word location in the Short VME I/O space

Interface
VME/VSB-bus compatible (48 mA drivers)

Modes of Operation
Read, Write, Read-Modify-Write, Refresh and Error Scrubbing

Refresh
Hidden Refresh, one cycle (275 nsec) every 15 usec

Memory Selection
Two independent memory ports, jumper-selectable on 1 Mbyte boundaries by setting the starting/ending address for each 16 Mbyte bank in the 4 Gbytes address field

Operating Temperature
0 to +60 degrees (Celsius)

Storage Temperature
-40 to +85 degrees (Celsius)

Relative Humidity
Up to 95% without condensation

Power Requirements
Standby: 6.1 A
Operate: 6.5 A

ORDERING INFORMATION:

MM-6340D/XX (XX = Mbytes with Error Correction)

XX = 4M, 8M, 12M, 16M bytes

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