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9540 Vassar Ave. Chatsworth, CA 91311
Tel: 818-998-0070 Fax: 818-998-4459
sales@micromemory.com www.micromemory.com

MM-6395D 512 Mbyte, 80 Mbyte/sec Transfer Rate, Dual-Port VME/VME64/VSB DRAM Memory Module

A high-speed, dual port, single card slot, high-density memory module, the **MM-6395D** provides up to 512 Mbytes for VME/VME64/VSB systems.

It is intended for high performance applications employing Page Mode and Block Transfer at data rates greater than 80 Mbytes/sec. It also supports sequential access (BLT/ MBLT) of 256/2K bytes on the VME/VME64 and 256 bytes on the VSB bus.

The Control Status Register (CSR), located in the short I/O address space, stores and reads byte-wide parity errors detected on the VME/VSB buses. The other bits enable Write-Wrong parity for diagnostics, disable the VME-port and determine whether the memory is CACHEable on the VSB bus.

Parity generation and checking are provided for each byte; parity status is stored and transmitted via BERR*.

Reliability is ensured by burn-in and running memory diagnostics that check operations for 48 hours while temperature-cycling boards from 0° to 65° C. Quality is guaranteed by Micro Memory Inc.'s 1 year warranty.

FEATURES:

- Capacity: 64 M, 128 M, 256 M and 512 Mbytes
- Cycle/Access Time (nsec):
  - Write: 100/20 (Block Transfer - BLT/MBLT)
  - Read: 100/30
- Data Transfer Rate:
  - VME64: 80Mbytes/sec
  - VME/VSB: 40 Mbytes/sec
- Compatible with VMEbus Rev D and VSBbus Rev C with A32/A24/D64/D32/ D16/D8 (UAT) for VME/VME64 and A32/D32 for VSB
- Block Transfer (BLT) 256 bytes for VME, MBLT of 2 Kbytes for VME64, and 256 bytes for VSB
- Parity generation and checking for error detection on each byte
- Write-Wrong parity for diagnostics under software control
- Module selection on 1 Mbyte boundaries, switch-selectable for positive visual indication on selecting the lower and upper limits on each bus
- VMEbus Control Status Register (CSR) for parity reporting and control
- 1 Year Warranty

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MM-6395D Specification *(Preliminary)*

Parameter	Specification
Capacity	64M, 128M, 256M and 512Mbytes
Cycle/Access Time (nsec)	Block Transfer (BLT/MBLT) • Write: 100/20 • Read: 100/30
Data Transfer Rate	VME64: 80 Mbytes/ sec, VME/VSB: 40 Mbytes/sec
Address	VMEbus: 32 bits VSBbus: 32 bits multiplexed with 32 data bits
Data In/Data Out:	8, 16, 32, 64 bits (UAT) for VMEbus, 32-bit slave for VSBbus
Sequential Access	Block transfer (BLT/MBLT) of 256/2 Kbytes, 8 Kbytes on the VME/VME64bus, 256 bytes on the VSBbus
Address Modifiers	AM0-AM5 decoded by socketed PLD with the following codes: A24: 39, 3A, 3D and 3E A24 with BLT: 3B, 3F A32: 09, 0A, 0D and 0E A32 with BLT: 0B, 0F A24 with MBLT: 3B, 3C A32 with MBLT: 0B, 0C
Parity Checking	Byte-wide parity generation and checking; parity output stored and transmitted via CSR, Write-Wrong parity for diagnostics
Control Status Register (CSR)	Read/Write, 8 bits wide, jumper-selectable in VME short I/O space (A16) with VMEbus low limit address tracking
CSR Format	D0 Enable Parity Out D6 VMEbus Disable D1 Write-Wrong Parity D7 Parity Error D2 Memory CACHEable for VSB Others: Don't Care
Interface	VME Rev D and VSB Rev C
Modes of Operation	Read, write, read-modify-write, and refresh
Memory Selection	Two independent memory ports, switch-selectable on 1 Mbyte (100000_H) boundaries for each port by setting the upper and lower limits
Operating Temperature	0° to +65° C
Storage Temperature	-40° to +85° C
Relative Humidity	Up to 95% without condensation
Power Requirements	+5V @ 5.5 Amps

Ordering Information

Part Number	Memory Capacity
MM-6395D/XXX	XXX=64M, 128M, 256M and 512Mbytes

Specifications are subject to change without notice. Micro Memory Inc. reserves the right to make changes to improve reliability, function, or design. Micro Memory Inc. does not assume any liability arising from application of this product.

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