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Find the *Curtiss-Wright / VMETRO VBT-325C* at our website: ***Click HERE***

STIM200-PB:
Bus Master, Pattern Generator and 200 MHz Timing Analyzer

This Piggyback for the VBT-325 supports VMEbus, VSB, SCSI 2, and User defined P2.

- **Combines 200MHz Timing Analyzer and Stimuli - Pattern Generator in one piggyback module.**
- **107ch / 200MHz Timing Analyzer monitors bus events down to 5ns (identical to TIM200-PB).**
- **Pattern Generator emulates Bus Masters with user-controlled timing of 99 VMEbus or 64 P2 signals (VSB, SCSI-II or User-def).**
- **Cycle Generator with commands just as debug monitor, data read from slave captured in buffer.**
- **Timing Template for easy manipulation of cycle timing.**
- **Explicit Pattern Buffer editing with Comb, Fill, Search commands.**
- **Bus Grant and slave handshake for VME or VSB, allows operation together with other Bus Masters.**

The STIM200-PB represents a unique combination of a full-featured 200MHz Timing Analyzer and an advanced Bus Master based on a Pattern Generator.

The powerful Timing Analyzer capabilities of the STIM200-PB are identical to those of the TIM200-PB, described on the previous pages. The STIM200-PB is actually an enhanced TIM200-PB where the acquisition logic and trace buffer can be placed in reverse, thus forming a pattern generator.

BUS MASTER WITH FULL HANDSHAKE

The STIM200-PB contains circuitry that controls the pattern issue process as a function of the bus arbitration mechansim as well as slave response handshake (DTACK*/ BERR* on VME, ASACK/ ACK*/ ERR* on VSB). Together with dynamic daisy-chain control of the Bus Grant (BG3IN/ OUT* on VME and BGIN/ OUT* on VSB), this allows the module to emulate bus masters in a real bus system among other bus masters.

USER-DEFINED SIGNAL TIMING

The user has several ways to control the signal timing of the generated bus cycles. This can be used to verify integrity and margins of bus interface designs, when operating the bus close to or beyond limits defined in the bus timing specifications.

CYCLE GENERATOR

The Cycle Generator software hides for the user the pattern generator nature of the stimuli functions. Simple commands similar to that of a debug monitor are offered, like Read/Write a single address, display a screen with memory contents, fill memory, test memory etc.

The hybrid analyzer / pattern generator nature of the module even allows data from READ cycles to be captured and shown directly on the screen or compared with expected results (test memory).

TIMING TEMPLATE

Actual bus timing for the cycles made by the Cycle Generator are defined in a user-alterable Timing Template. Here, a number of bus timing parameters like ADDRESS-to-AS*, DATA-to-DS*, ADDRESS-off-after-DTACK* etc. are directly given in nanoseconds. By default, a nominal set of values are given to make bus cycles that complies with the VMEbus specifications.

PATTERN EDITOR

For more complex timing situations, such as introducing glitches to emulate noise etc., the Pattern Editor can be used. This operates directly on the pattern buffer, and offers functions like Comb, Fill and Search patterns. Each line in the pattern buffer represents 5ns on the bus.

The pattern editor has a column called "3St-Ctr" that controls the tri-stating of control signals, address and data busses. There is also a "Progress" column that controls the pattern issue process, with commands like Wait-for-DTACK*-on, Run, Wait-for-DTACK*-off, Release and Re-request bus etc.

The STIM200-PB is basically a 200MHz Timing Analyzer where the trace buffer can be reversed to become a pattern generator, emulating a Bus Master with user-controlled timing on VMEbus or P2.

OTHER FEATURES

- **32K Pattern Buffer with wrap-around (128K optional).**
- **200MHz pattern issue rate, 5ns edge-to-edge resolution. (10ns minimum pulse width.)**
- **Dump/Load pattern to/from PC, edit pattern locally on PC with simulator.**
- **Start stimuli on VBT-325 trigger or manually.**
- **Outputs definable as 3-state or open-collector with 48mA/24mA drive capability.**

ORDERING INFORMATION

For ordering information see Piggyback Ordering Information. For VBT-325 ordering see VBT-325 ordering information.

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