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Find the *Curtiss-Wright / VMETRO VBT-325C* at our website: ***Click HERE***

TIM200-PB:
200 MHz Timing Analyzer for VME and P2

This Piggyback for the VBT-325 supports VMEbus, VSB, SCSI 2, and User defined P2.

- **107ch / 200MHz Timing Analyzer monitors bus events down to 5ns.**
- **For VMEbus, VSB, SCSI 2 or User-defined P2 bus.**
- **VXI or Futurebus+ supported with adapter.**
- **99 VMEbus signals or 64 P2 signals captured (VSB, SCSI or User-defined).**
- **8 channels dedicated for external signals.**
- **Full-width 1/0/X trigger with pattern duration filter (> / < 5-635ns in 5ns steps.)**
- **5ns trigger sensitivity and sampling resolution.**
- **32K Trace Memory depth (128K optional).**
- **Simultaneous state and timing analysis when used together with VBT-325.**
- **Load/Dump trace memory from/to PC file.**
- **Field upgradeable to STIM200-PB Stimuli / Pattern Generator.**

The TIM200-PB is a high-performance piggyback module for the VBT-325 VME & P2 Analyzer, offering 200MHz Timing Analysis for the VMEbus or the most common P2 busses such as VSB, SCSI I or II and even proprietary buses. The TIM200-PB is pre-configured to capture 99 VMEbus signals or up to 64 P2 signals, and in addition, eight external signals can be connected to pin header inputs.

SEE TIMING DETAILS OF BUS CYCLES

The 200 MHz Timing Analyzer allows the user to study the details of bus cycles in the waveform display with 5ns resolution. In order to track glitches or other timing problems as short as 5ns, the module includes a powerful trigger comparator that works on all captured signals at a full speed of 200MHz/5ns.

POWERFUL AND FLEXIBLE TRIGGERING

The 200MHz Analyzer of the TIM200-PB has two possible trigger sources:

1) Trigger using an onboard full-speed pattern trigger (1/0/X word recognizer) that covers 8 external signals plus all the 99 VMEbus channels or the 64 P2 signals. The specified pattern can also be qualified with a "pattern duration filter" that provides a trigger only if the pattern is valid for less than or greater than a given time period in the range 5-635 ns.

2) Cross-trigger from the VBT-325 analyzer on which the module is mounted, e.g. to see a cycle found from a complex sequencer trigger from the VME or P2 part of the VBT-325.

USER-DEFINED P2 BUSES

The TIM200-PB can be configured by the user to monitor a user-specific bus on the P2 a/c connector. With the Signal Field Editor as shown below the user may assign names of individual signals as well as buses, and define polarity, hex or binary presentation etc., just like any general purpose logic analyzer. The setup is automatically stored in non-volatile memory in the carrier board (VBT-325 or VPC-MkII), but may also be stored to a file on a PC/host computer.

UPGRADEABLE TO A STIM200-PB STIMULI / PATTERN GENERATOR, FOR BUS MASTER FUNCTIONS

The TIM200-PB is upgradeable to a STIM200-PB module, which adds the capability to perform bus cycles on VME or P2 as a bus master by means of a stimuli / pattern generator function. This is possible since the TIM200-PB is based on the same hardware as the **S**TIM200-PB, which allows the acquisition logic and trace buffer to be placed in reverse, thus forming a pattern generator. Only a firmware/PLD change is necessary for the upgrade. Please refer to the following pages for details about the remarkable capabilities of the STIM200-PB.

ORDERING INFORMATION

For ordering information see Piggyback Ordering Information. For VBT-325 ordering see VBT-325 ordering information.

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