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Find the *Curtiss-Wright / VMETRO VBT-325C* at our website: ***Click HERE***

VDRIVE-PB

Master - Slave - System Controller
Interrupt Generator & Handler

- **Adds VMEbus Master, Slave and System controller to the VBT-325.**
- **VIC068 VME interface chip ensures compliance with VMEbus spec.**
- **VMEbus Master functions include memory inspect, patch, fill, test, loop.**
- **Supports normal, block and RMW cycles on any BR level, selectable RWD, ROR, ROC release options.**
- **VMEbus Slave memory with SW-controlled base address and window size (64K/128K/256K).**
- **A32, A24, A16 Address size.**
- **D32, D16, D8 + Unaligned 2- and 3-byte Data size.**
- **Interrupt Generator with user-defined IRQ-level and Vector on IACK.**
- **Interrupt Handler at user-defined IRQ level. Level and vector displayed on screen.**
- **Data patterns like Walking Ones & Zeros generated for testing purposes.**
- **Operates simultaneously and in parallel with VBT-325 analyzer.**

The VDRIVE-PB is a VMEbus Master, Slave and System Controller piggyback module. It is intended as a tool for testing VMEbus boards or systems by being able to generate and respond to nearly all VMEbus cycle types under full user control at the command level.

USE AS REFERENCE UNIT

The VDRIVE-PB uses the VIC068 VMEbus interface chip to provide real VMEbus cycles with nominal bus timing. As such, the VDRIVE-PB can act as a reference unit that generates cycles on the VMEbus with known characteristics. This is useful for testing new boards or to assist in the debugging of faulty boards. The VDRIVE-PB can also be used to inspect or patch data in memory without intervening with the operation of the CPUs on the bus.

PARAMETERS SET BY COMMANDS, NOT JUMPERS

There are no configuration jumpers for slave base addresses or cycle parameters like Address size, AM codes, Bus Levels, Interrupt level etc. All this is set by software commands in a user-interface fully integrated with that of the VBT-325.

IDEAL FOR AUTOMATED TESTING

This jumper-less approach makes the VDRIVE-PB ideal for making automated testing of VME boards during manufacturing. In such cases, a PC or other host computer can run command scripts to generate sequences of bus cycles, interrupts etc., where all the various Bus Request levels, IRQ levels, cycle types and transfer sizes can be automatically exercised. Also, various data patterns like "walking ones and zeros" and "address as data" can be generated.

SIMULTANEOUS ANALYSIS & BUS MASTER

The powerful VMEbus State and Timing analysis features of the VBT-325 on which the VDRIVE-PB is mounted are fully available while operating the VDRIVE-PB. This further strengthens the capabilities with respect to automated test and verification applications, where the analyzer can run in parallel with the VDRIVE-PB to capture a trace of the bus activity. e.g. for later comparison with a reference trace.

VOLTAGE & TEMPERATURE READOUT

The VDRIVE-PB provides direct readout of the actual temperature encountered by the board, as well as the 5V supply voltage. These values are always displayed on the status line on the screen.

SLAVE MEMORY

The module contains a 256KByte slave memory that can be located anywhere in the VMEbus address map by a command. Address sizes

of A32, A24 or A16 can be selected, and the window can also be limited to 128 or 64K. During master block cycles, this memory acts as a source/destination for the data.

DTACK* GENERATOR

The VDRIVE-PB also contains a "DTACK* generator" that has its own address decoder for a 64K, 128K or 256K window separate from that of the local memory. This may be set to assert DTACK* after a user-defined delay of 30-590ns, in response to address broadcasts from another module or the VDRIVE-PB itself. No data is read or written, this is used e.g. to emulate planned but not yet existing modules.

LOW-COST MODEL A, FULL-FEATURED MODEL-B

The VDRIVE-PB is offered in two versions, Model A and B. Model B is the full-featured version, while model A is a lower-cost alternative that lacks the slave memory, block cycle support and interrupt handler / generator.

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