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Find the *Curtiss-Wright / VMETRO VBT-325C* at our website: ***Click HERE***

# XMEM325-PB: Extended Trace Buffer



The XMEM-325 series of piggyback boards for the VBT-325 creates a 8 Trigger Events / Dual Analyzer with the following features:

- **Offers extended trace memory with the depth of 1M, 512K, 256K or 128K samples.**
- **Doubles number of active trigger events to 8 in sequencer *.**
- **Extended buffer is a full analyzer in itself, giving dual analyzers for VME and P2 *.**
- **Allows simultaneous state and timing analysis with cross triggering *.**
- **Separate trace buffers for VME and P2 (optional), both up to 1M depth.**
- **Access time measurements for VME and P2 *.**
- **VSB Address & Data de-multiplexed in HW *.**
- **Sampling rate up to 25 MHz.**
- **Supports VME, VME64, VSB, SCSI-II, VXI and User-defined P2 bus.**

** Not supported on 128K model.*

The XMEM325-PB adds a massive amount of test and debugging capabilities to the VBT-325, targeted at the most demanding bus analysis applications. With a remarkable storage capacity of up to 1M (1024K) samples of VME and P2 bus activity in its trace buffers, the module is well suited for e.g. "post-mortem" system analysis and for software verification tasks.

**DUPLICATES ANALYZER RESOURCES OF VBT-325**

In addition to offering deeper trace buffers, the XMEM325-PB actually duplicates most of the analyzer resources of VBT-325. To take maximum advantage of this fact, the user may either combine forces of the VBT-325 and XMEM325-PB into one extremely powerful analyzer, or use the duplicate resources as two completely independent analyzers.

**EIGHT TRIGGER EVENTS**

The combined forces of a VBT-325 and an XMEM325-PB provides as many as eight parallel trigger events. Utilizing these **8** events in the 16-level trigger sequencer enables the user to track down the most complicated errors and events on VMEbus or P2.

**SIMULTANEOUS TIMING AND STATE**

The XMEM325-PB implements a complete separate analyzer in addition to the VBT-325. This can be utilized to perform simultaneous timing and state analysis on both VME and P2, running as many as four analyzers in parallel!

Using the piggyback for state analysis with its very deep trace buffer and the VBT-325 for timing analysis with its 50MHz sampling rate, an extremely powerful one-slot tool is at hand for integrated hardware and software testing of both VME and VSB, SCSI or other P2 bus.

**ORDERING INFORMATION**

For ordering information see Piggyback Ordering Information. For VBT-325 ordering see VBT-325 ordering information.

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