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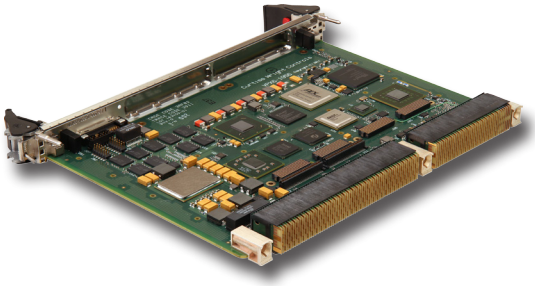
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Find the *Curtiss-Wright RTM6-1956-0000* at our website: ***Click HERE***

VPX6-1957

VPX Gen3 Intel Core i7 Quad-Core Single Board Computer

CURTISSWRIGHTDS.COM



Key Features

- 6U VPX form factor
- Gen3 Intel Core i7 Processor 2.1 GHz, Quad-core i7-3612QE CPU
  - + 6 MB L3 cache
  - + Intel AVX floating-point
  - + Intel VT-d and VT-d support
- 8 or 16 GB DDR3 SDRAM
- 32 GB NAND flash drive
- Quad 1 GbE and dual 10 GbE for flexible network connectivity
- 16-lanes PCIe Gen2 to backplane on Expansion Plane configurable to 4 x 4, 2 x 8, or 1 x 16-lane ports
- 4x 4-lane SRIO Gen2 ports to backplane on Data Plane
- 2 x XMC/PMC sites
- Intel Graphics: 2 x DVI
- Ruggedized air-cooled, conduction-cooled, and AFT cooling
- Windows WES7, Linux, and VxWorks

Applications

- Unmanned aerial and ground vehicles
- Tactical aircraft
- Armored vehicles
- Rugged naval systems

Overview

The VPX6-1957 is a rugged, high performance 6U OpenVPX single board computer (SBC) based on the Gen3 Intel® Core™ i7 processor. The VPX6-1957 is fully-featured 6U OpenVPX™ SBC, designed for harsh environments, and air- and conduction-cooled aerospace and defense applications. Each of the processor's four dual-threading cores delivers 2.1 GHz of performance, providing advanced Intel Architecture processing on the increasingly popular 6U OpenVPX form factor.

With a rich complement of on-board I/O, PCIe Express® (PCIe) Gen2, Serial RapidIO® (SRIO) Gen2, quad Gigabit Ethernet, dual 10 Gigabit Ethernet and XMC expansion, the VPX6-1957 satisfies the most demanding fielded applications for unmanned aerial and ground vehicles, tactical aircraft, armored vehicles and rugged naval systems.

Available in a full range of air- and conduction-cooled configurations, including air-flow through (AFT), the rugged VPX6-1957 combines the floating-point performance of the Intel Core i7 processor with the substantial bandwidth and system-enabling features of the 6U OpenVPX form factor. The board's Quad-Core processor features Intel AVX floating-point instructions to support math-intensive processing applications. The VPX6-1957 is available with 8 GB or 16 GB of high bandwidth DDR3 SDRAM (1333 MHz) and comes with a rich complement of high-speed I/O, including quad Gigabit Ethernet, dual 10 Gigabit Ethernet, 16 lanes of Gen2 PCIe on the Expansion Plane, 4 x 4-lane Gen2 SRIO ports on the Data Plane, four USB 2.0 ports, one XMC/PMC site, and one XMC site, each supported with eight lanes of Gen2 PCIe. The board's integral high-speed Gigabit Ethernet, PCIe fabric and XMC/PMC mezzanine module connectivity enables high bandwidth data flows. Data can also flow from the VPX backplane to the XMC site to support demanding high bandwidth applications such as video acquisition, processing and distribution, and radar and sonar. The VPX6-1957 features high bandwidth fabric to the backplane via Gen2 PCIe or Gen2 SRIO to the expansion and data plane connectors. This provides high bandwidth connectivity between the VPX6-1957 and additional boards in a computing environment.

Complemented with a wide range of DSP, GPU and FPGA processing modules, and supported by network and fabric switching modules, the VPX6-1957 satisfies the most demanding fielded applications for unmanned aerial and ground vehicles, tactical aircraft, armored vehicles and rugged naval systems.

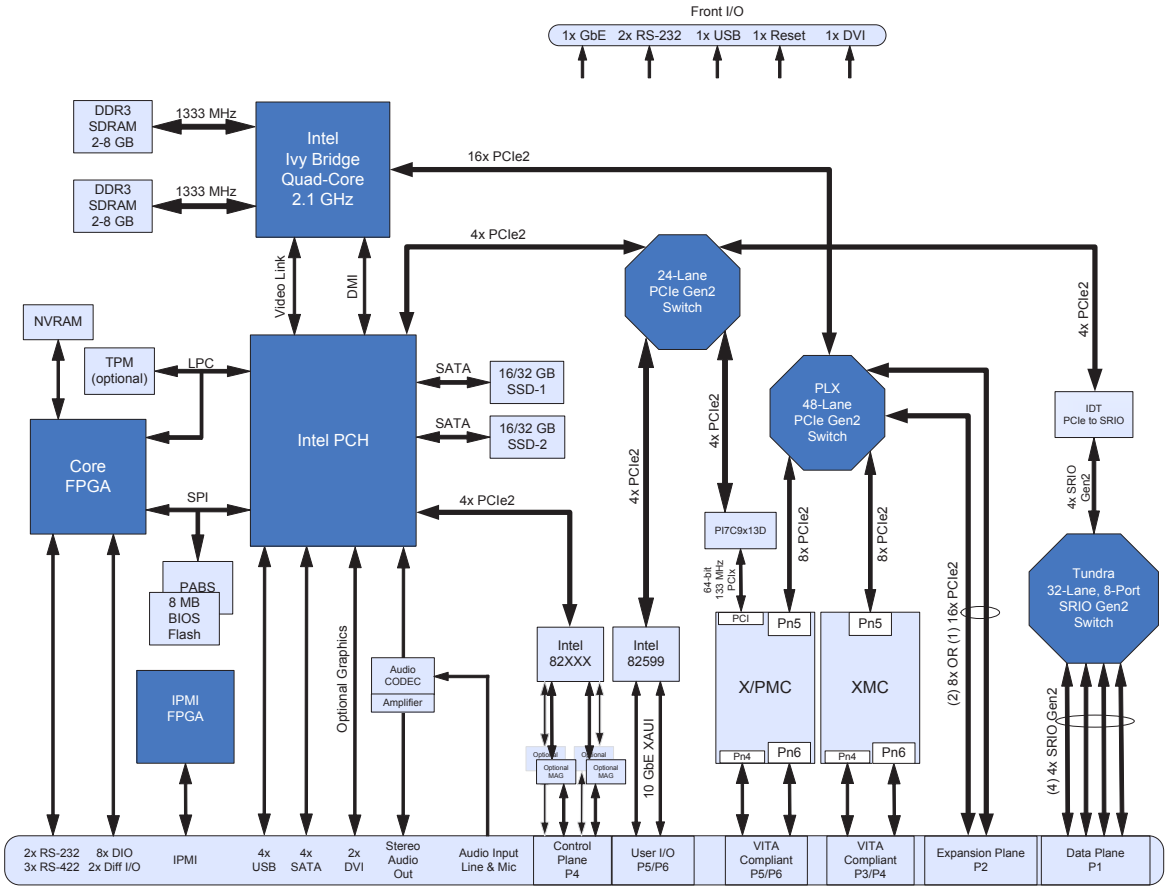


Figure 1: VPX6-1957 block diagram

Features and Specifications

Form factor

- 6U OpenVPX, supporting module formats MOD6-PAY-4F1Q2U2T-12.2.1-1, -2, -9, -11

Processor

- Intel Core i7 ‘Ivy Bridge” 3rd Gen processor
  - + Quad-Core (8 thread) i7-3612QE @ 2.1 GHz
  - + 256 KB L2 cache per core, 6 MB Intel Smart Cache
  - + Intel Streaming SIMD Extensions (SSE 4.1/4.2)
  - + Intel Advanced Vector Extensions (AVX) floating-point
  - + Intel Trusted Execution Technology (TxT)
  - + Intel Virtualization Technology (VT-x)
  - + Intel HD Graphics 4000

Platform

- 6 Series QM67 Chipset PCH

SDRAM memory

- 8 or 16 GB DDR3L at 1,333 MT/s
- Dual-channel memory configuration with ECC

Non-volatile memory

- 16 GB (dual 8 GB) SATA NAND (SLC) flash
- 256 KB NVRAM
- 16 MB SPI flash for BIOS functions

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Backplane fabrics

- 4 x 4-lane SRIO Gen2 (5.0 G bps) Data Plane ports
- 16-lane PCIe Gen2 Expansion Plane supporting 1 x 16, 2 x 8, and 4x4 configurations

Dual mezzanine sites

- PMC/XMC Mezzanine Site # 1
  - + 64-bit, 133 MHz PCI-X to PMC
  - + 8-lane PCIe Gen2 to XMC
  - + Jn4 and Jn6 I/O supporting VITA 46.9 mapping P5w1-P64s+X12d+X8d
- XMC Mezzanine Site # 2
  - + 8-lane PCIe Gen2 to XMC
  - + Jn6 I/O supporting VITA 46.9 mapping P3w3-X38s+P4w1-X12d+X8d
- Maximum power for mezzanine sites: 15 watts each

Ethernet ports

- 2 x Gigabit Ethernet ports supporting tri-speed 10/100/1000Base-T operation with auto negotiation and auto-MDIX
- 2 x Gigabit Ethernet ports supporting 1000Base-X (SerDes)
- 2 x 10GBase-KX4 (XAUI) 10G Ethernet ports

NOTE: Not all Ethernet ports are available simultaneously. See the Backplane Fabric and GbE Mode details in the Ordering Information selection.

Front I/O (air-cooled only)

- 2 x RS-232 serial channels
- 1 x USB 2.0 port
- 1 x Gigabit Ethernet port (10/100/1000Base-T)
- 1 x DVI-D graphics port
- Board local reset

Rear I/O

- 2 x RS-232 serial channels
- 3 x RS-422 serial channels
- 8 x Discrete I/O
- 4 x USB 2.0 ports
- 4 x SATA 2.0 ports
- 4 x Gigabit Ethernet ports – 2 x BT and 2 x BX
- 2 x 10 GbE ports
- 2 x DVI graphics ports
- 1 x RGB/VGA graphics port
- Intel AC97 stereo audio port

NOTE: Not all I/O available simultaneously. See the I/O Mode details in the Ordering Information selection.

Optional interfaces

- Trusted Platform Module (TPM) support

Software support

- Linux (Fedora, Red Hat Enterprise RHEL, Red Hawk)
- Wind River VxWorks
- Microsoft Windows Embedded Standard 7 (WES7)
- LynxOS

Built-in Test

- Power-up BIT (PBIT)
- User Initiated BIT (IBIT)
- Continuous BIT (CBIT)

Power

Power consumption will vary based on operational loading. Values below are guidelines for operation with four cores @ 2.1 GHz at room temperature (23°C) – contact Curtiss-Wright for more details on power consumption.

- Idle power consumption: 24 to 26 watts
- Typical power consumption: 75 watts
- Maximum power consumption: 95 watts

Note: power consumption is exclusive of optional mezzanine power

Environmental

- Air-cooled: Level 0 and Level 100
- Conduction-cooled: Level 200 and Level 300
- Air Flow-Through also available

Weight

- Air-cooled L0/L100: 22.5 oz (638 g)
- Conduction-cooled L200: 36.7 oz (1040 g)
- Conduction-cooled L300: <TBD>

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VPX Module Format

The Versatile Performance Switching (VPX) module format, governed by the VITA 46 specification and the associated VITA 48 Ruggedized Enhanced Design Implementation (REDI) was established to address the fundamental requirement to provide open architecture modules that incorporate the high-speed serial interconnect technology that is becoming pervasive in high performance computing. The VPX standard was developed by the leading providers of military Commercial Off the Shelf (COTS) modules to address the major issue of high-speed serial interconnect, as well as incorporate numerous improvements learned after years of integrating VME and CompactPCI® (cPCI) modules. The VPX standard provides (MOD6-PAY-4F1Q2U2T-12.2.1-1,2,9,11,12):

- 3U and 6U Eurocard form factors, preserving chassis mechanical designs
- Support for four 4x serial interfaces as the primary fabric
- Support of higher power modules and improved cooling
- Improved logistics with two-level maintenance and keying

The VPX module format provides many benefits to integrators of high performance multi-processor systems for radar, electro-optical and signal intelligence applications.

In particular, PCIe functions as a fast connection between processors and the new generation of XMC modules which can easily be placed on VPX format carrier cards.

VPX6-1957 Features

Powerful 3rd Gen Intel Core i7 CPU

The Intel Core i7 processor is based on Intel's industry-leading silicon technology and the latest micro-architecture enhancements. This 3rd Generation Core i7 processor builds on the tremendous success of the revolutionary micro-architecture and marks the next step in Intel's continual cadence for delivering contemporary technology to the aerospace and defense industry. Intel's 3rd Generation Core i7 delivers tremendous performance at the manageable power levels required to support today's size, weight and power (SWaP) sensitive designs.

Power consumption and CPU tuning

The VPX6-1957 provides extremely flexible and dynamic methods of controlling power consumption. From statically parking cores in the BIOS to dynamically adjusting CPU clocks at run time, the VPX6-1957 performance can be tailored to meet a wide range of processing and power requirements.

Intel Platform Controller Hub

The VPX6-1957 employs the Intel 6 Series QM67 PCH Chipset. This highly integrated PCH handles much of the flow of information between the board's I/O interfaces and the Intel Core i7 Processor.

Dual Data Rate (DDR3) SDRAM

The VPX6-1957 has two independent DDR3 memory controllers supporting independent banks of 72-bit DDR3 SDRAM (64-bit plus ECC). The VPX6-1957 may be fitted with 8 or 16 GB of DDR3 SDRAM. The DDR3 interface operates at 1333 MT/s, yielding a memory throughput performance of 21.3 GB/s.

To preserve data integrity, the SDRAM is provided with ECC circuitry that detects and corrects all single-bit data errors, detects all double-bit errors, and detects all 3-bit and 4-bit errors within the same nibble. The SDRAM is accessible from the processor as well as from the SRIO data plane, the Ethernet and PCIe interfaces.

Flash memory

The VPX6-1957 is configured with 16 GB of SATA NAND flash memory, arranged as two independent 8 GB SATA drives, as well as 16 MB SPI flash for BIOS functions. Using SLC technology to enhance reliability, the flash will retain data for a minimum of 20 years at +85°C. Read performance of the flash array is optimized in order to minimize system boot up time for applications such as avionics mission computers where fast restarts after power interruptions are critical.

For absolute security against inadvertent flash programming or corruption, a hardware jumper is provided to disable writing to flash. The firmware provides flash programming functions with support for downloading flash images over Ethernet.

Fabric ports

The VPX6-1957 provides 16 lanes of PCIe to the VPX Expansion Plane, used primarily to connect at the highest possible speeds to an adjacent VPX module. Supporting a flexible configuration of 1 x 16-lane, 2 x 8-lane, or 4 x 4-lane ports, the PCIe ports operate at Gen1 (2.5 GT/s) or Gen2 (5.0 GT/s) speeds, for a throughput of up to 8 GB/s for extremely fast data transfers. When connecting multiple SBCs together, a Non-Transparent Bridge (NTB) can be configured on any port. Common uses of the VPX Expansion Plane are to connect the SBC to sensor or signal acquisition modules, or to augment the SBC processor with a powerful graphics module or dedicated GPGPU or FPGA offering additional computational power.

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Dual Mezzanine sites

The VPX6-1957 is equipped with two mezzanine sites capable of supporting VITA 42 XMC modules. Each XMC site supports 8 x lanes of PCIe Gen1 or Gen2 rates directly to the on-board PCIe switch.

One XMC site provides full Pn6 I/O with VPX backplane signal mapping per VITA 46.9 as X38s+X12d+X8d. The second XMC site provides both Pn4 and Pn6 I/O with VPX backplane signal mapping as P64s+X12d+X8d.

The second mezzanine site also supports a PMC module with PCI-X bus access up to 133 MHz and 3.3V I/O.

On the conduction-cooled card configuration, the XMC site adheres to the VITA 20-2001 (R2005) conduction-cooled PCI Mezzanine Card standard specifications. To optimize the thermal transfer from XMC modules to the base card, the standard VPX6-1957 thermal frame incorporates both the primary and secondary thermal interfaces as defined by VITA 20-2001.

Four Gigabit Ethernet interfaces

The VPX6-1957 provides four Ethernet ports, with two ports configured for tri-speed 10/100/1000Base-T connectivity, and two ports configured for Base-X (SerDes) connectivity. The VPX6-1957 also offers dual 10 GbE ports configured as 10GBase-KX4 (XAUI) on the backplane.

For air-cooled modules, one of the backplane 1000Base-T Ethernet ports is re-directed to the front panel.

Serial ports

The VPX6-1957 provides two RS-232 and three RS-422 serial channels to the VPX backplane. The serial ports support asynchronous communications with baud rates independently configurable from 300 to 115200 KBaud.

Discrete Digital I/O (DIO)

The VPX6-1957 provides eight independent discrete digital I/O signals supported from the core functions FPGA. All DIOs are individually programmable as inputs or outputs, and can also be configured to support two differential DIO ports. Each DIO is capable of triggering an interrupt upon a change of state, programmable to detect either rising or falling edge. All DIOs are 5V-tolerant.

USB ports

The VPX6-1957 provides four USB 2.0 ports from the PCH. All ports include +5V VBUS power.

Each port can handle low speed (1.5 Mbps), full speed (12 Mbps), and high speed (480 Mbps) operation. When operating at low speed or full speed, each port is managed by independent OHCI-compliant controllers internal to the device. One EHCI-compliant controller manages any ports operating in high speed mode.

SATA ports

The VPX6-1957 provides four SATA 2.0 interfaces on the rear VPX backplane supporting transfer rates up to 3 Gbps.

Two additional SATA ports are used internally for SATA NAND SSD flash drives.

Display interfaces

The VPX6-1957's CPU supports Intel HD Graphics 4000, which includes a 650 MHz base clock with up to 1 GHz performance, and 16 graphics execution units with up to 141 GFLOPS performance. The VPX6-1957 provides one VGA and up to two DVI ports to the backplane.

Depending on the operating system, up to three independent display ports can operate simultaneously.

Audio and stereo interfaces

The VPX6-1957 provides one AC97 analog stereo audio port, offering 24-bit input and stereo output with a built-in headphone amplifier. One microphone input supporting up to 30 dB pre-amp boost is also provided.

Temperature sensors

The VPX6-1957 provides temperature sensors to measure board and processor temperatures. There is a sensor at each edge of the card and sensors built into the CPU. The card edge sensors can be read by software and they can be configured to generate an interrupt in case of an over temperature condition.

Optional Interfaces

Trusted Platform Module (TPM) support

The VPX6-1957 can support an optional Trusted Platform Module (TPM) device. The TPM can be used to create a secure computing environment, ensuring only trusted and signed BIOS and software can execute on the board. Please contact Curtiss-Wright for details on TPM operation and availability.

Software Support

Continuum™ Software Architecture (CSA)

The VPX6-1957 is supported by a suite of firmware, RTOS board support packages (BSP), communication libraries and signal processing libraries. The Continuum Software Architecture is Curtiss-Wright's suite of firmware and BSP APIs that is common to SBC (VME, cPCI and VPX) and multi-processor boards. Developers of mixed systems will find a common set of features and software interfaces for all future processing products from Curtiss-Wright. The Continuum Software Architecture is comprised of Continuum Firmware Monitor, Operating System Software, and Continuum Vector Library.

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Continuum Built-in Test (BIT)

Continuum BIT is a library of diagnostic routines to support Power-up BIT (PBIT), Initiated BIT (IBIT), and Continuous BIT (CBIT) for health management of the module. BIT operations are supported through BIOS and software APIs, and can be accessed in customer developed applications.

- Built-in Test PBIT for power-up self-test
- IBIT for user-initiated self-test
- CBIT for continuous self-test

Operating system software

The VPX6-1957 is supported with an extensive array of software items, which cover all facets of developing and running application code for the board. The following operating systems are currently supported for the VPX6-1957:

- Linux Fedora Core 20 remix, 64-bit (pre-installed from the factory)
- Wind River VxWorks 6.9, 64-bit
- Microsoft Windows Embedded Standard 7 (WES7), 64-bit
- Red Hat Enterprise Linux (RHEL) 6.5, 64-bit
- Concurrent Red Hawk 7.0

For information regarding additional operating systems support, please contact Curtiss-Wright.

Software drivers for most popular operating systems are also available:

- OFED middleware supporting the SRIO Data Plane fabric
- OpenGL® for graphics performance available for Linux and VxWorks
- OpenCL™ for computational GPGPU performance available for Linux
- Built-in graphics acceleration for Microsoft Windows

Continuum Vector Library

Continuum Vector provides the user with a choice of APIs with support for the Vector Signal Image Processing Library (VSIPL, Core Lite) standard and the popular API established by Floating Point Systems Inc. See the Continuum Vector product sheet for detailed information.

Front Panel I/O

To simplify the use of front-panel I/O in a development or lab environment, Curtiss-Wright offers a front panel breakout cable, model CBL-1905-0003. This cable breaks out the front panel's RS-232, 1000Base-T Ethernet, USB and DVI-D ports to industry standard connectors. A board reset pushbutton is also provided.

Rear Transition Module

For building systems in the lab environment, Curtiss-Wright provides a Rear Transition Module (RTM), part number RTM-1956-000x, that plugs into the backside of the VPX6-1957's backplane and provides access to most of the board connections on industry standard connectors. Several RTMs are available to support various module I/O modes.

Ruggedization Levels

To cost-effectively address a diverse range of applications, the VPX6-1957 is available in a range of ruggedization levels and mechanical formats. Air-cooled cards are available at Level 0 (0 to 50°C) and Level 100 (-40 to +71°C) and conduction-cooled cards are available at Level 200 (-40 to +85°C). The VPX6-1957 is also available in an Air Flow-Through design. Full details of Curtiss-Wright's standard Ruggedization Guidelines can be found on the Curtiss-Wright website.

Conduction-cooled modules are also offered at Level 300 with VITA 48 2-level maintenance covers to create a truly field-serviceable LRM.

Power Consumption

Power consumption of an Intel Core i7 SBC can vary greatly, depending on the processor configuration and loading characteristics. Curtiss-Wright has extensive background in characterizing Intel SBCs and can provide detailed power information to better understand the power vs. performance tradeoffs when designing systems around the VPX6-1957 module.

The VPX6-1957 BIOS provides a rich set of controls to statically or dynamically control CPU core parking and CPU clock speed.

- Typical power: 40 to 50 watts
- Maximum power: 56 watts

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Ordering Information

The VPX6-1957 is ordered with the following part number convention. Not all possible configurations are offered – see Ordering configurations below or contact Curtiss-Wright for additional configurations.

TABLE 1 Ordering information

PART NUMBER	DESCRIPTION
VPX6-1957-A0 vwxxyz	Air-cooled Level 0, 0 to 50°C
VPX6-1957-A1 vwxxyz	Air-cooled Level 100, -40 to +71°C
VPX6-1957-C2 vwxxyz	Conduction-cooled Level 200, -40 to +85°C card edge
VPX6-1957-F1 vwxxyz	Air-flow through (AFT) configuration
Standard prefix for 6U VPX cards	VPX6
Model number	1957
V: Mechanical format	1: 1.0” pitch front panel and metalwork for air-cooled modules 0.8” pitch for conduction-cooled modules 3: 0.85” pitch for conduction-cooled modules with 2LM covers 7: Air-flow through (AFT) Configuration
W: Backplane fabric and GbE mode (see Table 2)	A: SRIO, dual GbE TX, 1 x 10 GbE B: SRIO, dual GbE TX, + dual BX, 1 x 10 GbE E: SRIO, dual BX, 2 x 10 GbE F: Dual GbE TX
X: Memory configuration	DRAM/Flash 04: 4 GB SDRAM, 16 GB Flash 08: 8 GB SDRAM,16 GB Flash 16: 16 GB SDRAM, 16 GB Flash 09: Customer specific configuration
Y: Processor and software configurations	4: 35W Quad-Core @ 2.1 GHz processor, Fedora Linux with TPM 5: 35W Quad-Core @ 2.1 GHz processor, Fedora Linux
Z: P1 I/O mode (see Table 3)	0: Serial 3..4/DIO 1..2, DVI_3 1: Serial 3..4/DIO 1..2, SATA 3..4 2: USB 3..4, DVI_2, DVI_3 (*cannot combine with Fabric Modes B/E)

TABLE 2 Backplane fabric and GbE mode

MODE	DATA PLANE	FRONT PANEL	P4W11	P4W12	P4W13-14	P4W15-16	P6
A	SRIO	GbE-BT GE4	-	-	GbE-BT GE2	GbE-BT GE1	10G XAUI
B	SRIO	-	GbE-BX GE4	GbE-BX GE3	GbE-BT GE2	GbE-BT GE1	10G XAUI
E	SRIO	-	GbE-BX GE4	GbE-BX GE3	10G XAUI		10G XAUI
F	-	GbE-BT GE4	-	-	GbE-BT GE2	GbE-BT GE1	-

TABLE 3 I/O mode

I/O MODE	P3W15..16	P4W11..12	P5 SE G11..16	P6 SE G11.16	P6W11..12
0	Serial 3..4 DIO 1..2	GbE-BX GE3..4	DVI_3 DDC	VGA DDC	DVI_3
1			Reserved		SATA 3..4
2	USB3..4	DVI_2	DVI_3 DDC	DVI_2 DDC	DVI_3

Note: Cannot support Backplane Fabric and GbE Mode B/E with I/O Mode 2

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Ordering configurations

A summary of popular ordering configurations are shown below. Only I/O Mode 0 is shown (final model number digit), as this is a popular I/O model. For other models or configurations, consult Curtiss-Wright for availability.

TABLE 4 Ordering configurations with 4 x SRIO ports on data plane

DRAM MEMORY (GB)	RUGGEDIZATION		
	AIR-COOLED LEVEL 0	AIR-COOLED LEVEL 100	CONDUCTION-COOLED LEVEL 200
8	VPX6-1957-A01A0850	VPX6-1957-A11A0850	VPX6-1957-C21A0850
16	VPX6-1957-A01A1650	VPX6-1957-A11A1650	VPX6-1957-C21A1650

TABLE 5 Accessories

PART NUMBER	DESCRIPTION
RTM-1956-000x	Rear Transition Module for VPX6-1956 and 1957 › Provides breakout connectors for most board I/O › Select RTM product number 'x' based on SBC I/O mode (ie: if 1957 is I/O mode 0, select RTM-1956-0000)
CBL-1905-0003	Front panel breakout cable for VME-1905/1908 and VPX6-1956/7/8/9. Breaks out front panel high density connector into: › DB9 connectors for RS-232 ports COM1 and COM2 › RJ45 jack for Gigabit Ethernet › USB 2.0 receptacle › Pushbutton reset switch › DVI-D video connection
CBL-1905-0002	RTM breakout cable for RTM-1905, RTM-1956, RTM-1957 › Breaks out high density DMS-59 to dual DVI and includes a DVI to VGA adapter

TABLE 6 Software

PART NUMBER	DESCRIPTION
DSW-1957-6150-FED6	Fedora 15 64-bit BSP for VPX6-1957. Includes driver source code.
DSW-1957-6200-FED6	Fedora 20 64-bit BSP for VPX6-1957. Includes driver source code.
DSW-1957-0690-VXW6	Wind River VxWorks 6.9 64-bit BSP for VPX6-1957
DSW-1957-6601-RHL6	Red Hat Enterprise Linux (RHEL) 6.1 64-bit BSP for VPX6-1957. Includes driver source code.
DSW-1957-6650-RHL6	Red Hat Enterprise Linux (RHEL) 6.5 64-bit BSP for VPX6-1957. Includes driver source code.
DSW-1957-7700-WES6	Microsoft Windows Embedded 7 64-bit (WES7)
MNT-1957-LNX	Annual maintenance and Software Upgrade Program (SUP) for VPX6-1957 Linux (Fedora and RHEL)
MNT-1957-VXW	Annual maintenance and Software Upgrade Program (SUP) for VPX6-1957 Wind River VxWorks
MNT-1957-WES	Annual maintenance and Software Upgrade Program (SUP) for VPX6-1957 Microsoft Windows Embedded

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