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Find the *Curtiss-Wright CBL-416-020* at our website: ***Click HERE***

CABLE REQUIREMENTS

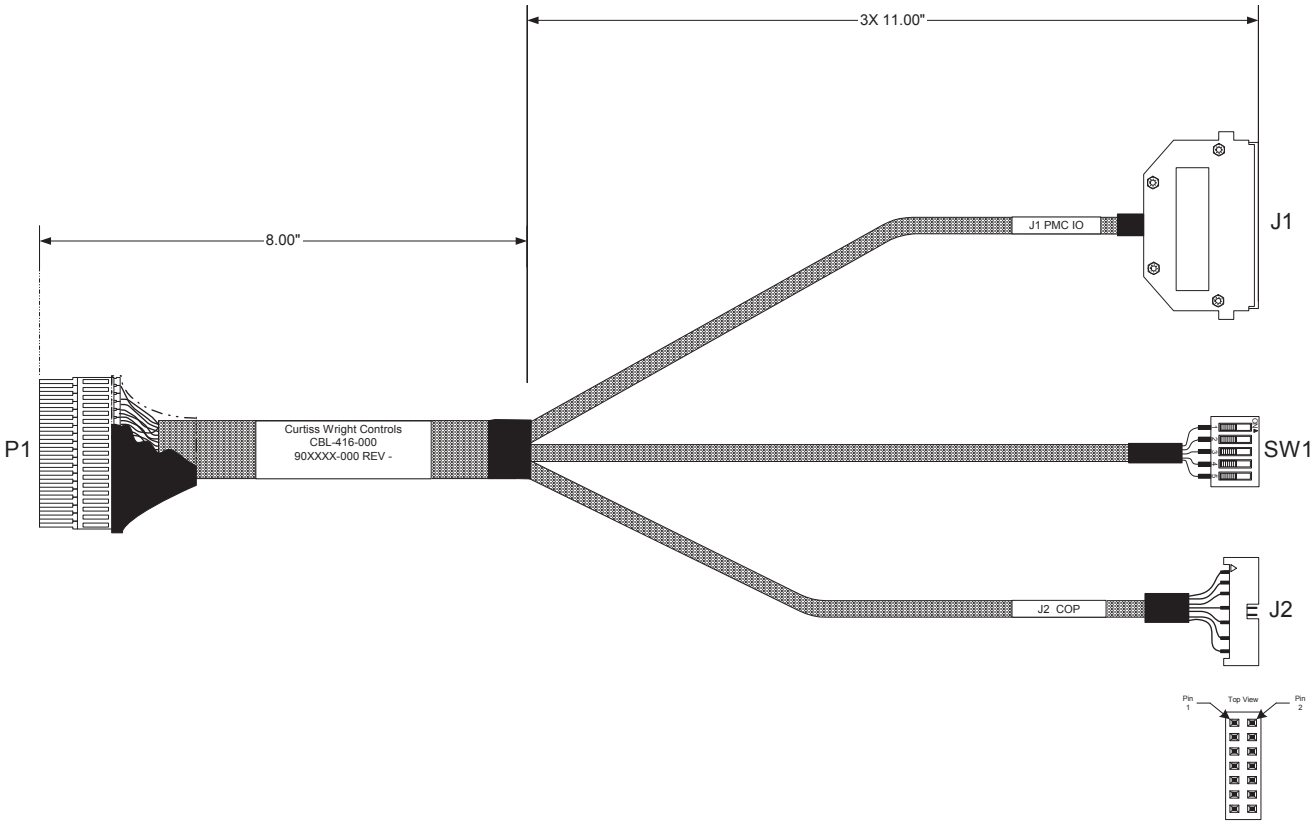
P0, P2, and Front Panel interface cables are available to support the CHAMP-AV IV. Specifically these are identified as:

- CBL-416-000 (CHAMP-AV IV Standard P0 Cable Assembly - see Figure 2.2)
- CBL-416-001 (CHAMP-AV IV P0 with Starfabric Cable Assembly - see Figure 2.3)
- CBL-416-010 (CHAMP-AV IV Standard P2 Cable Assembly - see Figure 2.4)
- CBL-416-011 (CHAMP-AV IV P2 Starfabric Cable Assembly - see Figure 2.5)
- CBL-416-020 (CHAMP-AV IV Front Panel Serial Cable Assembly)

CBL-416-000

Figure 2.2 shows a cable drawing of the CHAMP-AV IV Standard P0 Cable Assembly (CBL-416-000). The cable assembly provides access to the following CHAMP-AV IV signals: PMC user I/O signals (J1); JTAG/COP interface (J2); and Configuration Switches (SW1). SW1 is a 5-position DIP switch that allows the following signals to be grounded in the "ON" position (listed from left to right): JPROC0, JPROC1, and JPROC2. SW1 positions 4 and 5 are not used.

FIGURE 2.2: CBL-416-000 (CHAMP-AV IV Standard P0 Cable Assembly)

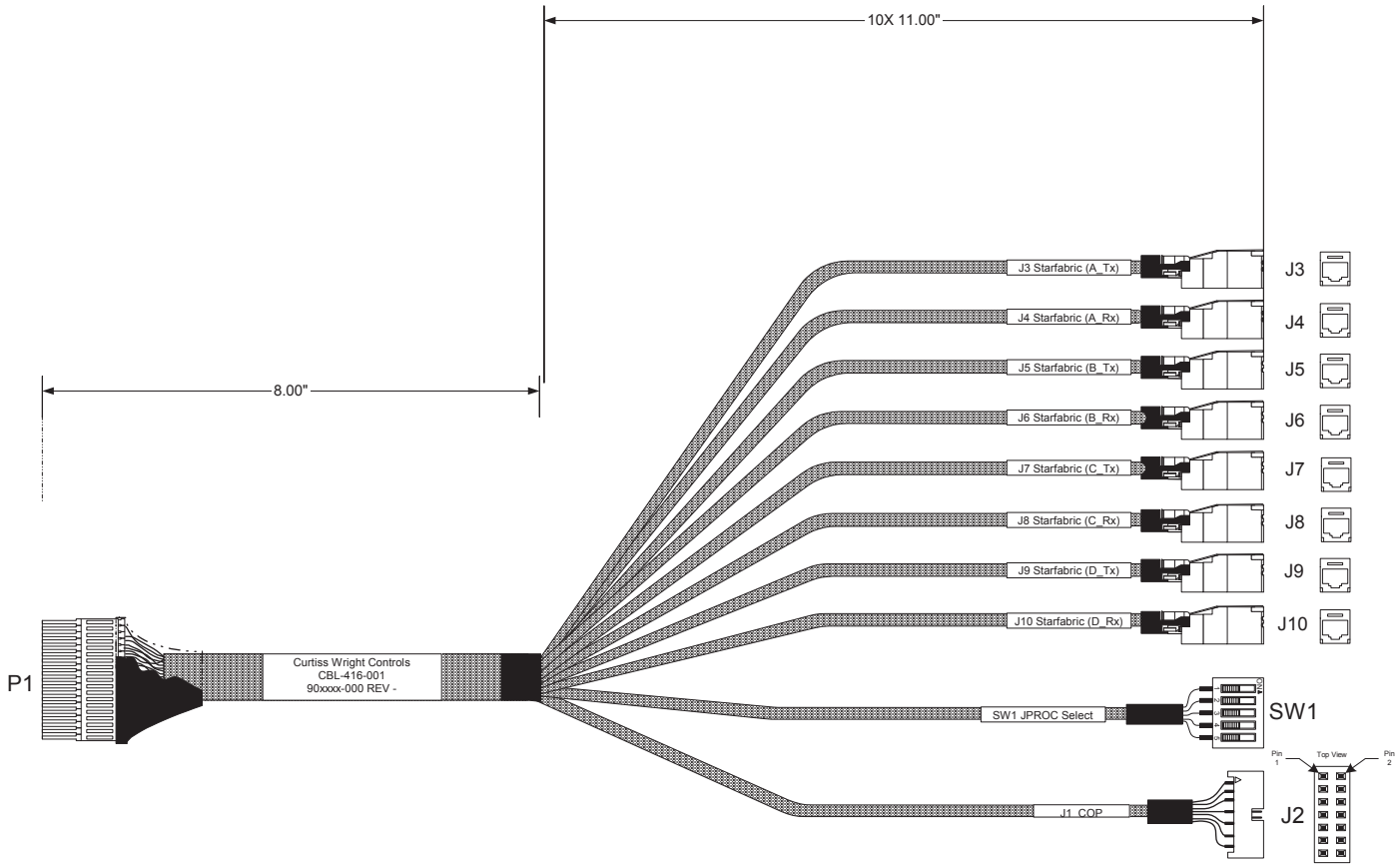


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CBL-416-001

Figure 2.3 shows a cable drawing of the CHAMP-AV IV P0 StarFabric Cable Assembly (CBL-416-001). This cable assembly is identical to the Standard P0 Cable Assembly (CBL-416-000) with the exception of the PMC user I/O signals. Instead of providing a single J1 connector, this cable provides eight RJ-45 connectors (J3 through J10) to support StarLink PMC I/O.

FIGURE 2.3: CBL-416-001 (CHAMP-AV IV P0 StarFabric Cable Assembly)

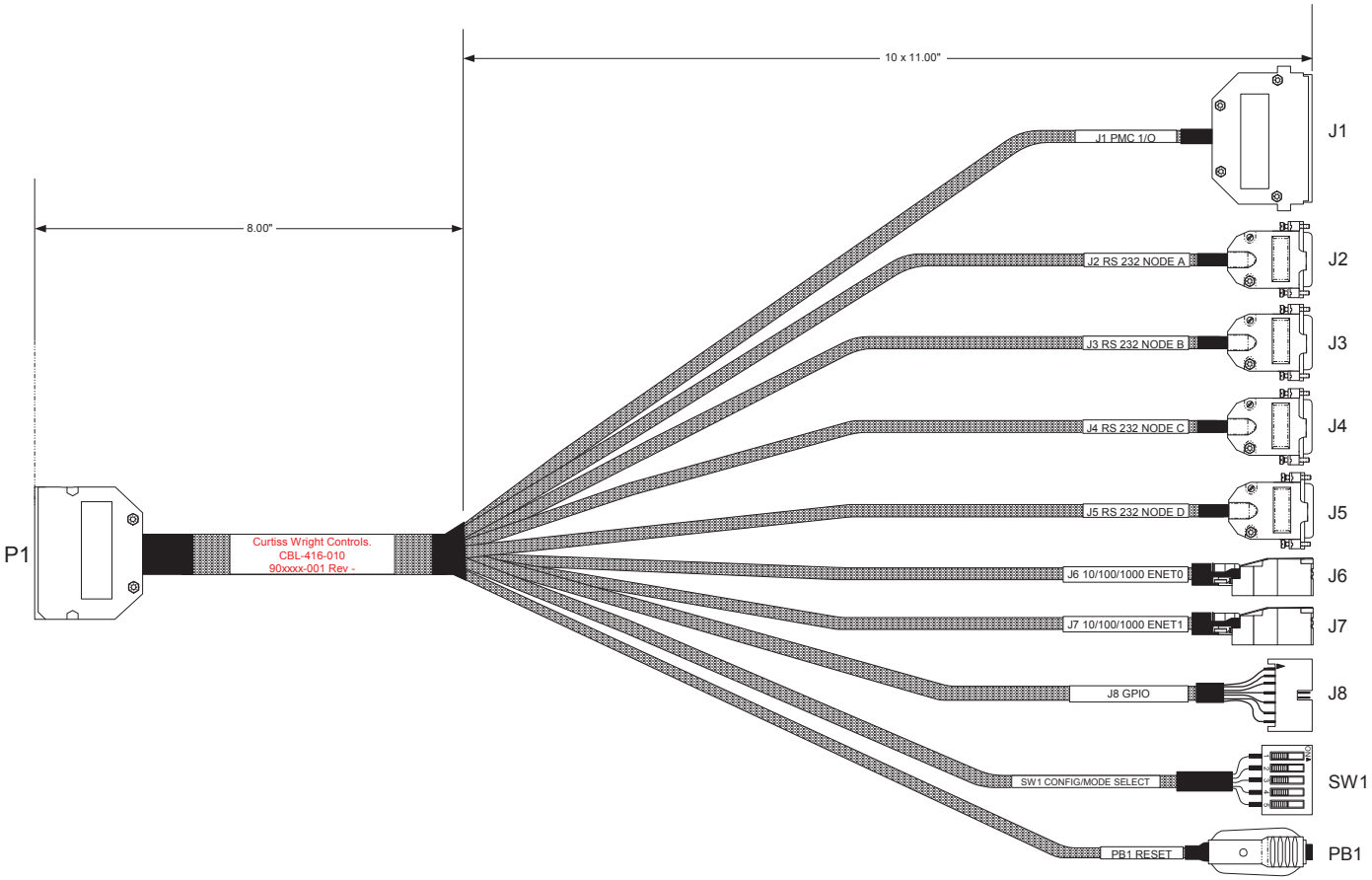


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CBL-416-010

Figure 2.4 shows a cable drawing of the CHAMP-AV IV Standard P2 Cable Assembly (CBL-416-010). The cable assembly provides access to the following CHAMP-AV IV signals: PMC user I/O signals (J1); RS-232 Node A Serial Port 0 signals (J2); RS-232 Node B Serial Port 0 signals (J3); RS-232 Node C Serial Port 0 signals (J4); RS-232 Node D Serial Port 0 signals (J5); 10/100/1000 Ethernet Port 0 (J6); 10/100/1000 Ethernet Port 1 (J7); GPIO signals (J8), Configuration Switches (SW1); and Pushbutton Reset Switch (PB1). SW1 is a 5-position DIP switch that allows the following signals to be grounded in the "ON" position (listed from left to right): BCFG0, BCFG1, and F_ALTPROM. Note that F_ALTPROM is a Reserved signal and the switch should be set to the Off (default) condition. SW1 positions 4 and 5 are not used.

FIGURE 2.4: CBL-416-010 (CHAMP-AV IV Standard P2 Cable Assembly)

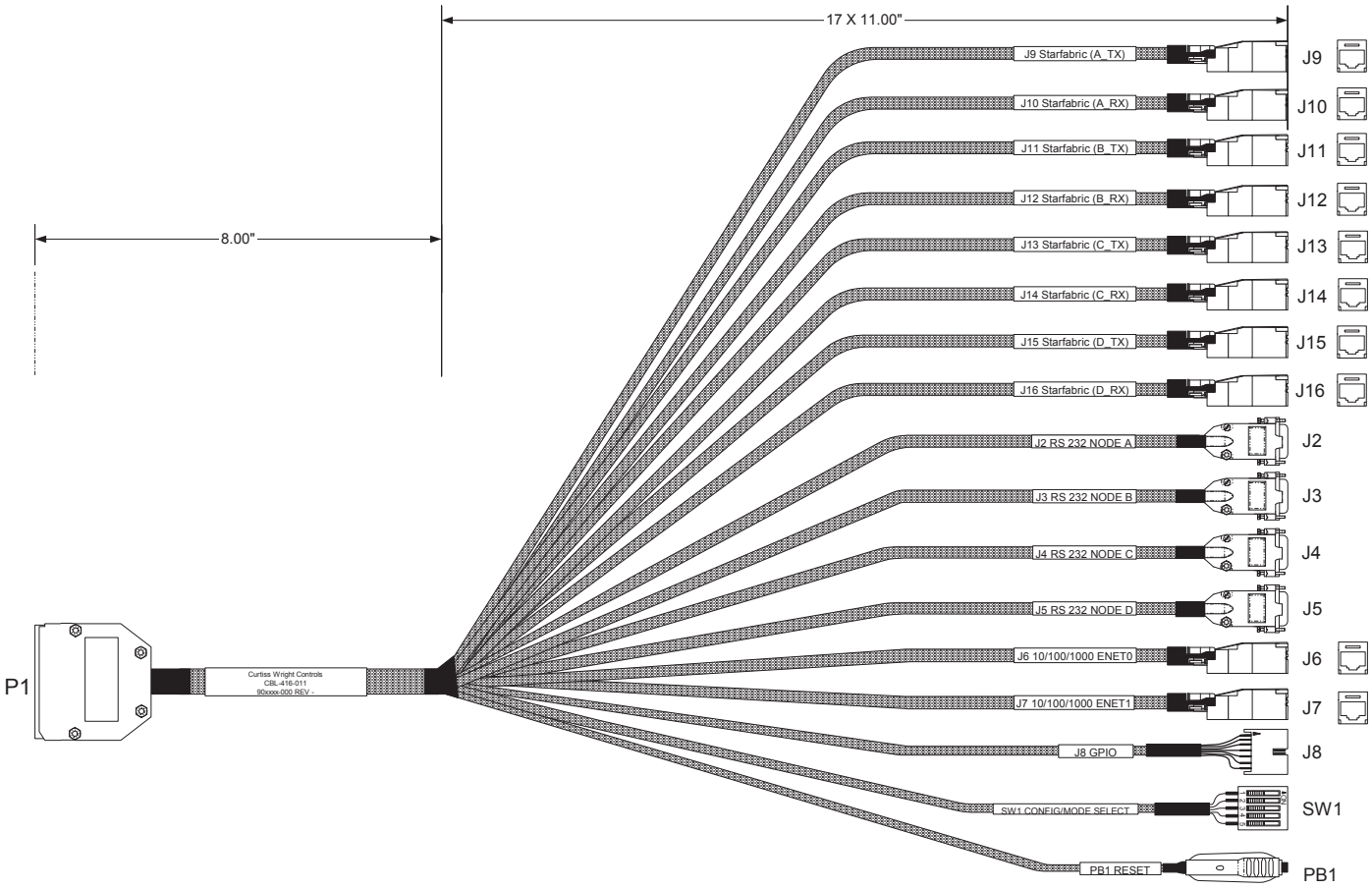


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CBL-416-011

Figure 2.5 shows a cable drawing of the CHAMP-AV IV P2 StarFabric Cable Assembly (CBL-416-011). This cable assembly is identical to the Standard P2 Cable Assembly (CBL-416-010) with the exception of the PMC user I/O signals. Instead of providing a single J1 connector, this cable provides eight RJ-45 connectors (J9 through J16) to support StarLink PMC I/O.

FIGURE 2.5: CBL-416-011 (CHAMP-AV IV P2 StarFabric Cable Assembly)



Cross Reference

Alternatively, the CHAMP-AV IV may be used in conjunction with a IXT-AV4 rear transition module. Refer to Chapter 3 of this manual for detailed information describing the cables and the StarLink rear transition module.

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