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Squall II Module Interface

The Squall II Module interface provides I/O expansion and flexibility by allowing customers to add a wide range of I/O interfaces to Cyclone's PCI and VME Single Board Computer products. Designers can mix and match standard and custom modules to meet unique I/O requirements. The Squall II Module interface, an open domain specification, is an enhanced set of the i960 microprocessor's data, address and control signals. The interface uses 32 bit data transfers to communicate with the processor or the shared memory. DMA controllers in the Squall II Modules can access the shared packet memory. The specification is published by Cyclone and is available on our web site. Intel also publishes the specification and uses the Squall II Module interface on their Cyclone Evaluation Platform.

Cyclone's VME products support two Squall II Modules while the PCI products support one standard and one extended Squall IIE Module. Squall IIE Module is electrically identical to a second Squall II Module but with an extended physical format. The standard Squall II Module provides 12 square inches of space for circuitry. The Squall IIE Modules provide 26 square inches for user I/O circuitry. Right angle extenders are offered for both the Squall II and Squall IIE Module interfaces to facilitate debugging of new Squall II and IIE Modules.

Standard Squall II modules are available for ATM, Fiber Optics, 100 Mbit Ethernet, Multi Megabit Serial, SCSI-2 (Fast and Wide) and others. If custom or unique I/O is required, customers can build their own modules or contact Cyclone for customized modules and boards. Cyclone has designed more than 50 Squall Modules and derivations of our standard products to fit our customer's specific needs. Please contact Cyclone with your specific requirements.

SQ01 Ethernet AUI Interface

An Ethernet AUI Interface is provided by an 82596CA LAN Coprocessor provides. A DB-15 (AUI) connector on the module's front panel permits cabling to an Ethernet Transceiver.

SQ02 VSB Master Interface with Ethernet AUI Interface

A VME Subsystem Bus (VSB) Master Interface controls a VSB P2 backplane bus while an Ethernet AUI Interface DB-15 connector is on the front panel.

SQ04 Dual 10BaseT Ethernet

The SQ04 provides dual Ethernet 10BaseT interfaces using two 82596CA LAN Coprocessors. Modular connectors allow direct connection to two 10BaseT Ethernet networks.

SQ05 10BaseT Ethernet (Big Endian)

The SQ05 provides a 10BaseT Ethernet interface using the 82596CA LAN Coprocessor. A modular connector allows direct connection to a 10BaseT network. This Squall Module uses the Big Endian byte ordering convention.

SQ06 10/100 BaseT4 Ethernet

The SQ06 provides a 10/100BaseT4 Ethernet interface based on the 82557 LAN Coprocessor. The module autosenses and adapts to the 10 or 100 Mbit network rate.

SQ08 Dual 10BaseT (Big Endian) Ethernet

The SQ08 provides dual Ethernet 10BaseT interfaces using two 82596CA LAN Coprocessors. Modular connectors allow direct connection to two 10BaseT Twisted Pair Ethernet networks. Big Endian byte ordering.

SQ09 10/100 BaseTX Ethernet

The SQ09 provides a 10/100BaseTX Ethernet interface based on the 82557 LAN Coprocessor. The module autosenses and adapts to the 10 or 100 Mbit network rate.

SQ10 SCSI-2 (Fast)

The SQ10 utilizes the SYM53C710 SCSI-2 Coprocessor to support 8-bit fast SCSI-2 accesses to up to seven SCSI-2 target devices.

SQ11 SCSI-2 (Fast and Wide)

The SQ11 utilizes the SYM53C720 SCSI-2 Coprocessor to support 16-bit fast SCSI-2 access to up to seven SCSI target devices. The SCSI-2 bus may be used to interface to a wide variety of devices including disk and tape drives.

SQ13 Ultra SCSI (Fast-20 Wide)

The SQ13 Ultra SCSI (Fast-20 Wide) uses the Symbios SYM53C770 to support Ultra SCSI transfer rates of 40 Mbytes/sec.

SQ20 Two RS-422 High Speed Serial Ports

The SQ20 supports synchronous and asynchronous communications up to 6 Mbit/sec with an aggregate of 8 Mbit/sec on all ports. Four DMA channels and FIFOs are provided to reduce the processors overhead in transferring data. The two serial ports are provided with EIA-422 drivers and receivers. All I/O signals exit via the front panel on two industry standard EIA-530-A, 26 position connectors.

SQ32 ATM/OC-3 Interface

The SQ32 provides an ATM User Network Interface (UNI) to 155 Mbit/s optical Area Networks. The SQ32 performs hardware segmentation and reassembly of ATM Adaptation Layer 5 (AAL 5) and the transmission convergence (TC) sublayer for 155.52 Mbit/s SONET STS-3c.

SQ33 ATM/T1 Interface

The SQ33 provides an ATM User Network Interface (UNI) to 1.5 Mbit/s T1 Area Networks. The SQ33 performs hardware segmentation and reassembly of ATM Adaptation Layer 5 (AAL 5) and the transmission convergence (TC) sublayer.

SQ40 20 Bit Differential Input with 8K FIFO

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The SQ40 provides a 20 bit input port host board. A buffer is utilized to allow data to be written to the SQ40 at different instantaneous data rates that is then read by the i960 processor. The FIFO data is clocked in by an external clock and is read by the host board. Interrupts are sourced from the FIFO status flags.

SQXX

Custom Squall II Modules developed upon request.

Squall II Module Interface Specification

The Squall II Module interface specification can be obtained through the following venues:

PCI911 Series Single Board Computer User's Manual
Part Number: 800-0911

965 Single Board Computer User's Manual
Part Number: 800-0965

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