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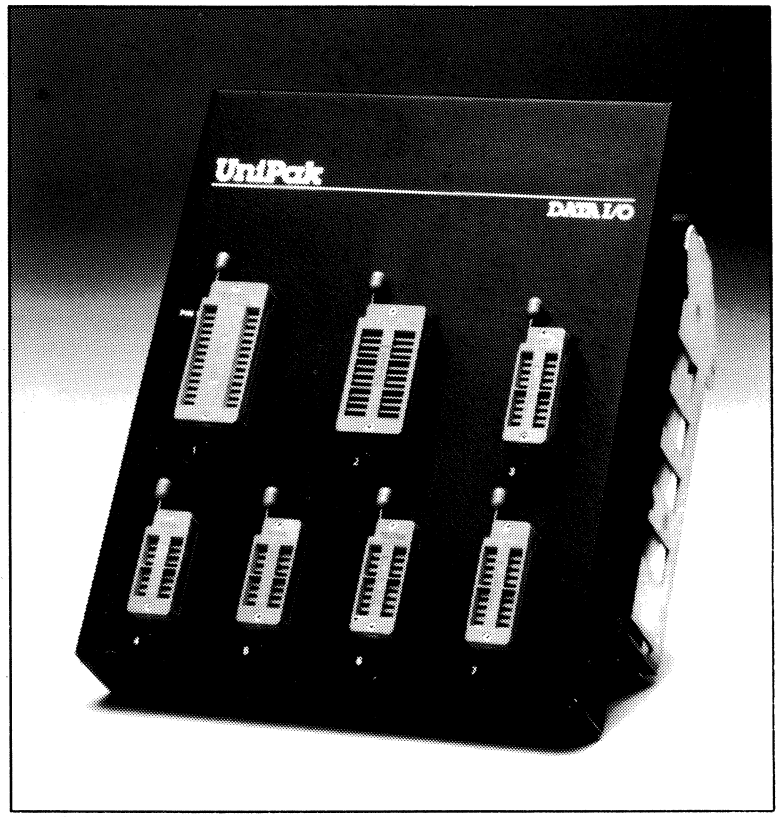
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UniPak™

981-0003-002 **JULY 86**

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NOTE

This configuration of the UniPak™ varies from previous configurations in that it uses some hexadecimal family codes. Some decimal family numbers have been changed. While this configuration will work with any Data I/O Universal Programmer (see section 1.1), to use hexadecimal families it may be necessary to update your programmer. Refer to section 1.2 of this manual for maintenance compatibility requirements. Model 1730s cannot handle hexadecimal families at this time. Some of the new larger devices will require that the programmer RAM be expanded. Consult your nearest Data I/O representative for update availability.

Applies to: Engineering Part No. 950-0099-011 and up
Text Part Number 090-0064-002

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008-1999	Motherboard
30-701-7997	Waveform Generator
30-702-0045	UniPak™ Memory
30-702-7995	Socket Assembly

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SECTION 1
INTRODUCTION

1.1 OVERVIEW

Data I/O's UniPak™ reliably programs over 400 popular MOS and bipolar devices. Values for programming variables, including pinouts, voltage levels and timing, are stored in firmware tables. When you choose the family and pinout codes for a particular device, the programmer uses information in these tables to assemble a specialized programming routine in scratch RAM. This method allows high-speed operation with minimum firmware overhead. The UniPak™ is designed to adapt to the programming requirements of many different devices. Pinout variations are handled by seven device sockets on the UniPak™ and, in some cases, by adapters which connect to socket 1 or 2. Specially designed electronic switches allow programming of both bipolar and MOS devices.

To maximize control speed during programming, the UniPak™ makes extensive use of addressable latches for control signals. For flexibility in waveform generation, digital-to-analog converters (DACs) control all major power supplies, with several rise and fall times selected by firmware.

1.2 PROGRAMMER COMPATIBILITY

Before using the -011 version of the UniPak™, read the information in this section to be sure your programmer does not require a modification. Either or both of two modifications to your System 17 or 19 may be required for compatibility with the -007 or later version of the UniPak™:

A. It may be necessary to make a small hardware modification to the System 17/19 Controller (702-1520).

B. A firmware update may be necessary.

29A Universal Programmers and 100A Production Programmers may need firmware updates.

1.2.1 Hardware Modification

System 19s with serial numbers below 1516 and System 17s with serial numbers below 219 will require small modifications for use with the UniPak™. The UniPak™ may cause error messages which are invalid if the modification is not made. No other programmer functions are affected, nor will attempting an operation harm the programmer, the UniPak™, or a device in the socket.

CAUTION

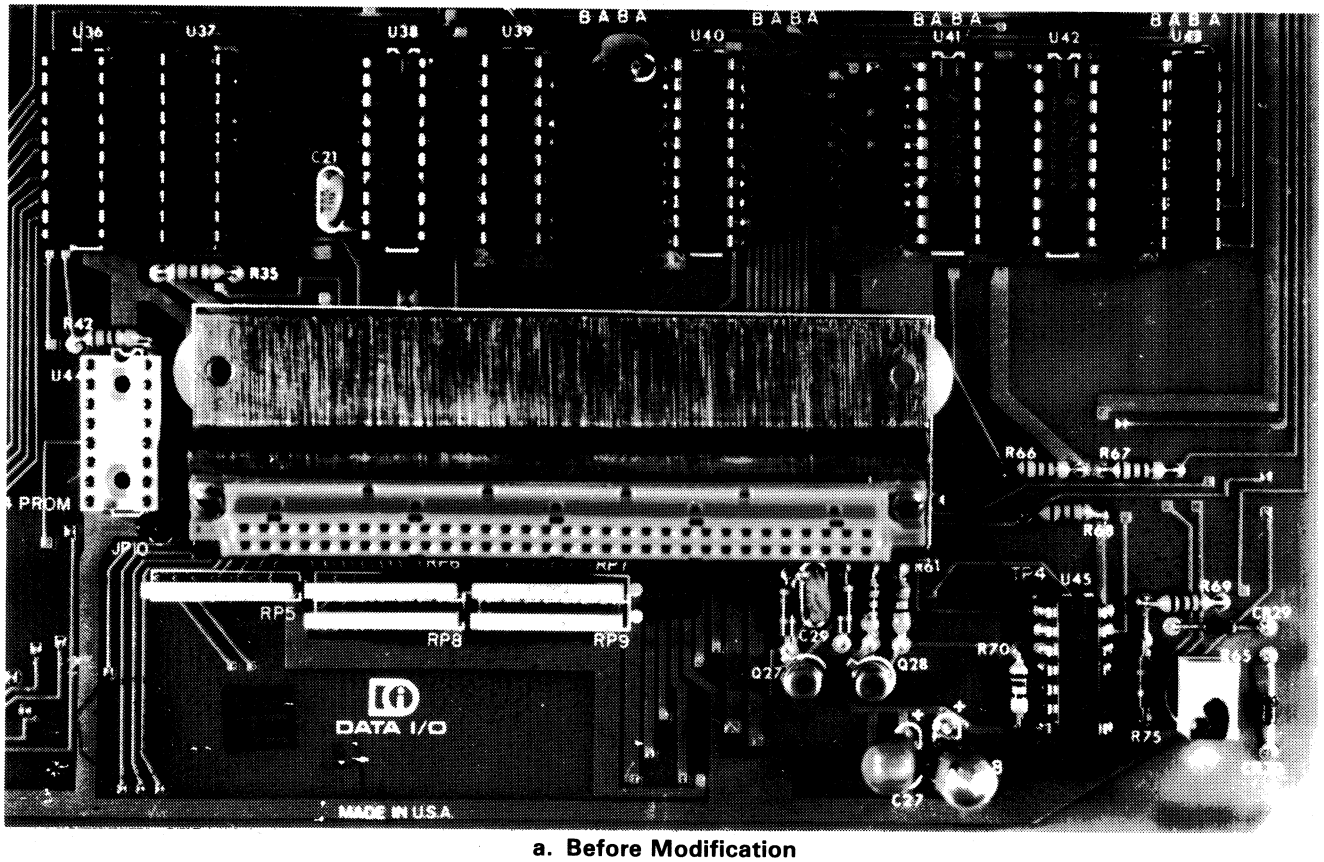
The following hardware modification to the System 17 and 19 should be performed by a qualified technician only. If the facilities are not available to perform the modification, contact your local Data I/O Service Center. A list of all Data I/O service centers is located in the back of this manual.

Modification Instructions

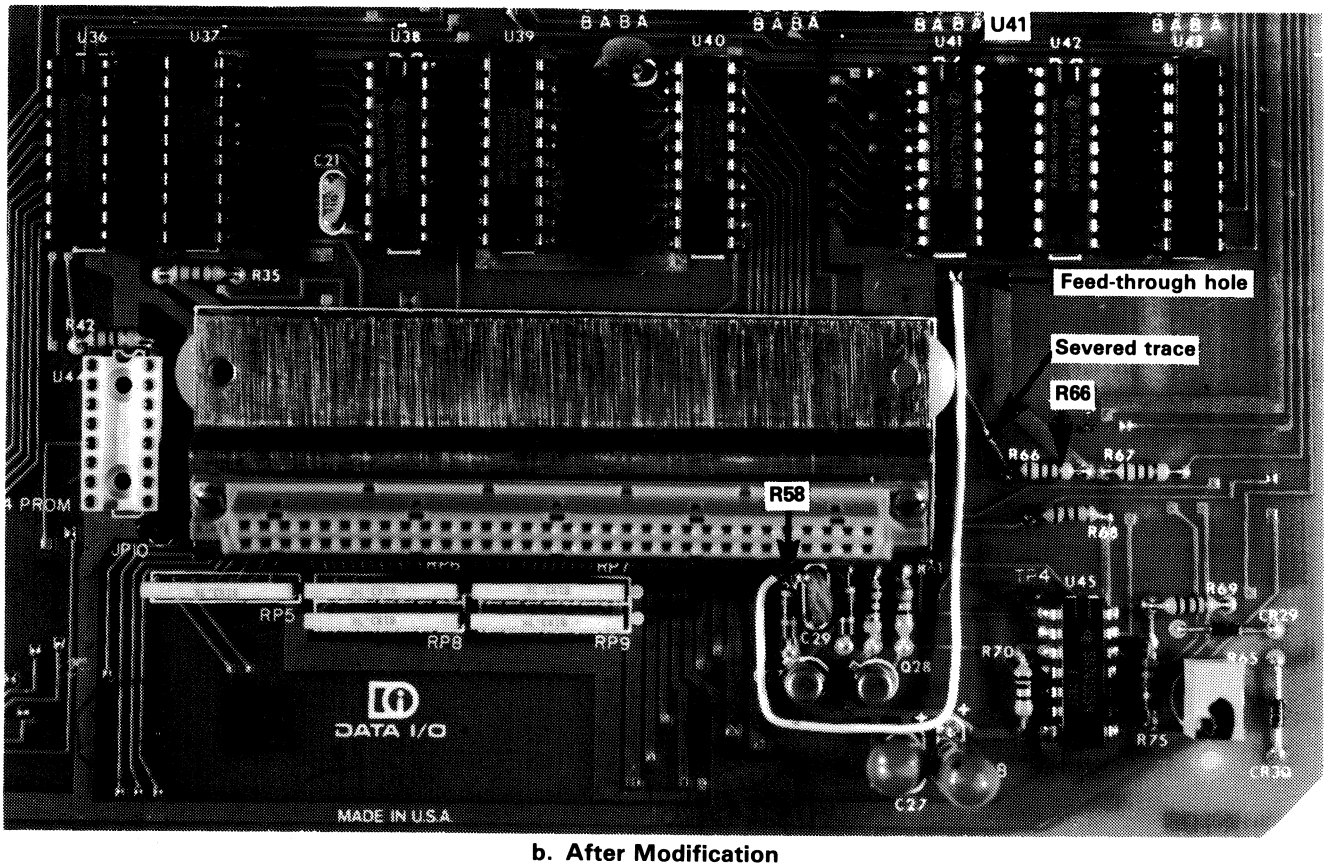
1. Unplug power cord.
2. Remove Programming Pak.
3. Remove protection shield.
   a. Pull the two snap-lock connectors and lift them gently.
   b. Lift the back edge of the plate first and pull it up slightly and turn it to the left until it is clear.
4. Remove top cover.
   a. Turn the programmer on its top.
   b. Remove the four cover screws.
   c. Turn the programmer upright and lift the cover off.
5. Remove display panel.
   a. Remove four screws located at the corner of the display panel.
   b. Remove the screw fastening the support bracket to the power supply assembly.
   c. Remove the screw fastening the support bracket to the front of the base.
   d. If there is a screw fastening the support bracket to the bottom plate, remove it.
6. Refer to figure 1-1. Sever the trace connecting R66 to U41 pin 1 just above R66 (left side).
7. Install an insulated wire from the top side of R58 (just *left* of C29, in front of the Programming Pak connector) to U41 pin 1. To connect to U41 pin 1, use the feed-through hole on the trace tying R66 to U41. (See figure 1.)
8. Reinstall the display panel top cover and protective shield by reversing the removal procedures.

Programmer Check

9. Install a Programming Pak.
   a. Check the programmer for proper initialization.
   b. Load a device with a known data pattern and perform a verify to confirm proper operation.



a. Before Modification



b. After Modification

Note: Your controller may appear slightly different. Be sure connections are made to the components designated in this bulletin.

Figure 1-1. Jumper-Wire Location on Programmer Controller, 702-1520

1.2.2 Software Update

Some programmers require a software update for compatibility with the -004 or later version of the UniPak™. Table 1-1 shows the revisions and software configuration-check numbers for each programmer configuration requiring a software update. If your System 17, 19, 29A or 100A is one of these revisions, contact a Data I/O sales representative to order the appropriate update kit.

To determine the revision level of a programmer, follow the procedure below to display the software configuration-check number and compare it to table 1-1.

- **System 19 and 29A, all configurations:** Key in Select Code B2-START.
- **System 29B, all configurations:** Key in Select Code B2-START. (All configurations are compatible.)
- **100A Production Programmer:** Key in Select Code 10.
- **System 1730:** Enter remote control and use the G command.
- **System 1731:** Enter remote control and use the CN command.

1.3 APPLICATIONS

Table A-1 lists all the devices that could be programmed with the UniPak™ when this manual was published. In many cases when a new device with industry-standard pinout is introduced within a manufacturer's family, the UniPak™ WILL NOT require a revision to program it. For some new applications, such as to accommodate a new device family, a firmware update of the UniPak™ may be required. The revision number is stamped after the part number (950-0099) along the underside of the top edge of the UniPak™ socket assembly.

1.4 SPECIFICATIONS

The UniPak™ receives its power from the programmer mainframe. Programming waveforms are generated from raw programmer supplies using regulators controlled by the programmer's microprocessor. The controlling firmware is located on a circuit card within the UniPak™.

The physical and environmental specifications are:

- Altitude: Sea level to 3 km (10,000 ft.)
- Dimensions: 20.9 x 17.0 x 10.5 cm (8.2 x 6.7 x 4.2 in.)
- Humidity (operating): 90% maximum (noncondensing)
- Humidity (storage): 95% maximum (noncondensing)
- Temperature (operating): 0 to 40°C (32 to 104°F)
- Temperature (storage): −40 to 55°C (−40 to 131°F)
- Weight: 1.38 kg (3 lb. 0.5 oz.)

Table 1-1. Programmers Requiring Updates

System	Revision	Software Configuration Check Number
990-1900	A	F9CF
	B	ØØAC
	C	Ø7CD
	D	ØB11
	E	FC6A
	F	B16C
990-1901	A	89CC
	B	CC89
	C	6BCD
990-1902	A	C56C
	B	8B82
	C	9141
	D	9ØØ2
	E	2Ø68
	F	29CE
	G	3868
	H	3599
990-1903	A	2C23
	B	6A9B
	C	3A33
990-1730	A	6D7B
	B	ADF5
	C	35EE
	D	418Ø
	E	44F8
990-1731	A	93AA
	B	3A3A
29A	A	1ECA
	B	2ØA4
29A w/computer remote control	A	BB41
	B	CØØB
100A	A	917F
	B	94Ø5
	C	9DEE
	D	9BED

1.5 FIELD APPLICATIONS SUPPORT

Data I/O has field applications engineers (FAE's) throughout the world. They can provide additional information about interfacing Data I/O products with other equipment and answer questions about equipment. FAE's are located within the United States at the addresses listed in the back of this manual. For international applications support, contact your nearest Data I/O representative.

1.6 WARRANTY

Data I/O equipment is warranted against defects in materials and workmanship. The warranty period of one year, unless specified otherwise, begins when you receive the equipment. The warranty card inside the back cover of this manual explains the length and conditions of the warranty. For warranty service, contact your nearest Data I/O service center.

1.7 SERVICE

Data I/O maintains service centers throughout the world, each staffed with factory-trained technicians to provide prompt, quality service. This includes not only repairs, but also calibration of all Data I/O products. A list of all Data I/O service centers is located in the back of this manual.

1.8 ORDERING

To order equipment, contact your Data I/O sales representative. Orders must contain the following information:

- Description of the equipment (see the latest Data I/O price list or contact your sales representative for equipment and part numbers)
- Quantity of each item ordered
- Shipping and billing address of firm, including ZIP code
- Name of person ordering equipment
- Purchase order number
- Desired method of shipment

SECTION 2
INSTALLATION

2.1 INSPECTION

Your UniPak™ was tested both electrically and mechanically before it was shipped, and was carefully packaged to prevent shipping damage. It should, therefore, arrive free of any defect, without marks or scratches, and in perfect operating condition. However, carefully inspect the instrument for any damage that may have occurred in transit. If you note any damage, file a claim with the carrier and notify Data I/O.

2.2 UniPak™ INSTALLATION

The UniPak™ may be installed and removed with the programmer's power on; this feature allows you to retain data in RAM during module changes. If the programmer power is turned on before the UniPak™ is installed, you will hear a beep until the UniPak™ is installed.

NOTE

Voltage transients can cause device damage. Thus, be sure that all sockets are empty when:

- *switching power on or off*
- *installing or removing the UniPak™*

To install the UniPak™, do the following:

1. Slide the UniPak™ into the opening in the programmer (figure 2-1).
2. Tilt the UniPak™ up and gently push it back to hook the flange of the UniPak™ over the back edge of the programmer opening (figure 2-1, a).
3. Lower the UniPak™ into position as shown in figure 2-1, b.
4. Press gently on the front edge of the UniPak™ to ensure a good connection (figure 2-1, c).

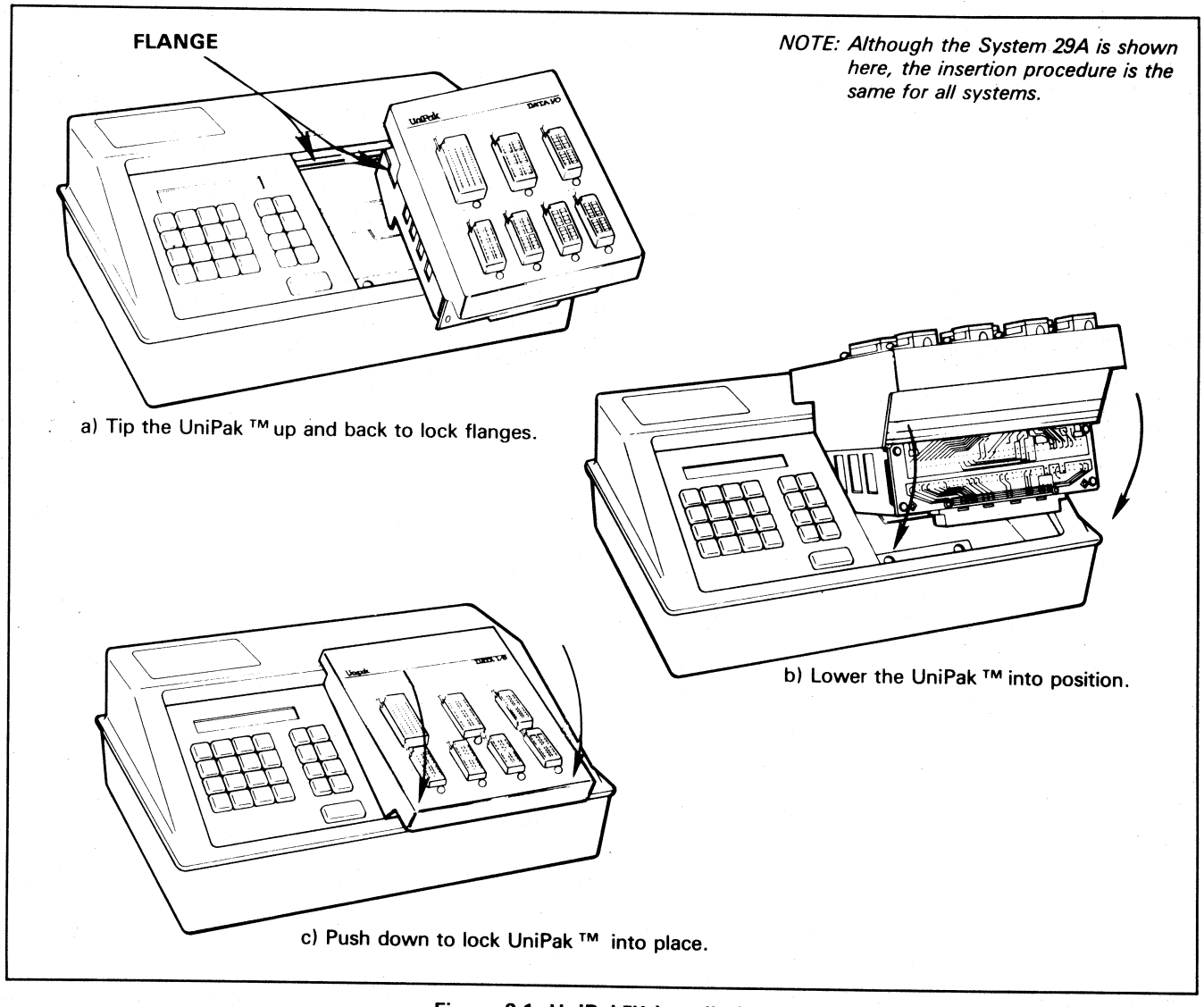


Figure 2-1. UniPak™ Installation

2.3 UniPak™ REMOVAL

1. Check to make sure the programmer is not in the process of an operation. If it is, wait until the operation is complete (the action symbol on the display disappears).
2. Check to make sure a device is not in a socket. If one is in a socket, remove it as described in section 3.7.
3. Tilt the UniPak™ up and gently remove it from the programmer.

2.4 REPACKING FOR SHIPMENT

If the UniPak™ is to be shipped to Data I/O for service or repair, attach a tag to it describing the work required and identifying the owner. In correspondence, identify the unit by part number, revision level, and name. If the original shipping container is to be used, place the UniPak™ in the container with the appropriate packing material and seal the container with strong tape. If another container is used, be sure that it is a heavy carton, wrapped with heavy paper or plastic; use appropriate packing material and seal well with strong tape. Mark the container "DELICATE INSTRUMENT" or "FRAGILE."

SECTION 3
OPERATION

3.1 OVERVIEW

The UniPak™ can be used in 29A, 29B, System 19, or 100A programmers of any configuration; see section 1.2 for firmware revision levels required. The typical programming operation with a 29A programmer and a UniPak™ is illustrated in figure 3-1. As can be seen from this figure, the UniPak™ can obtain data from three sources (a master device, a serial port, or the keyboard). Because the serial port and keyboard operations are unique for each type of programmer, you will be referred to your 29A, 29B, System 19, or 100A programmer manual for details on how to program using these mainframes.

When using a master device as the data source to program a blank device, you must first instruct the programmer to copy the device data into programmer RAM (shown as COPY in figure 3-1 and described in section 3.4). Then enter the family code and pinout code as described in section 3.5. The data in the device will have been copied to the RAM of your 29A when you press START, as shown in figure 3-1. You must then remove the master device and instruct the programmer to copy the information just stored in its RAM to a blank device. This completes the basic programming operation.

The procedures to perform basic operations with your UniPak™ are described in this section. You should follow these procedures to properly operate your UniPak™. Wherever possible, key sequences have been included for using your UniPak™ with a 29A Universal Programmer with Rev C firmware (read section 1.2 carefully to determine your programmer's firmware revision level). Refer to your programmer manual for key sequences for the System 19, 29B, and 100A programmers.

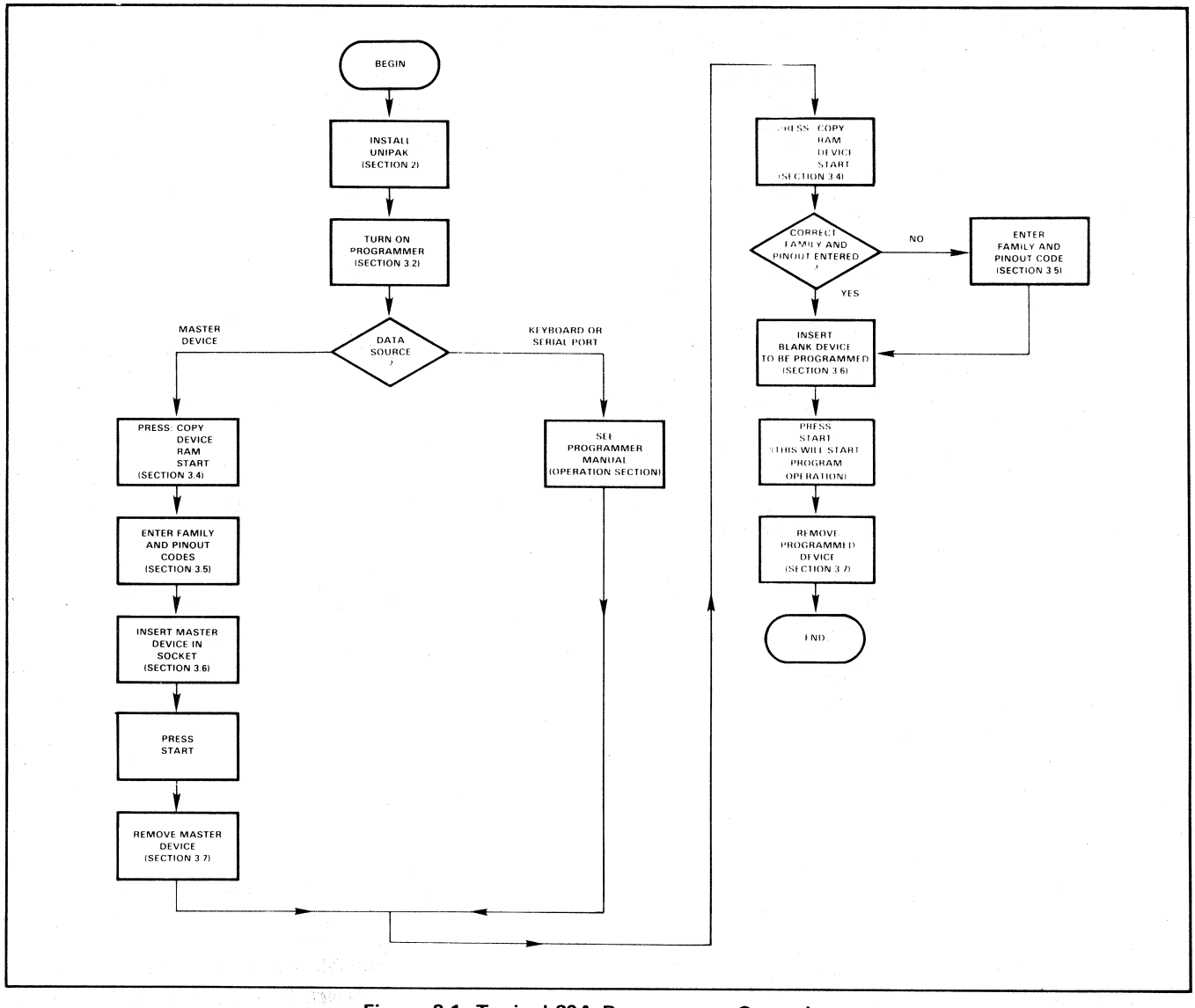


Figure 3-1. Typical 29A Programmer Operation

3.2 POWER UP

NOTE

If the UniPak™ is not installed in the programmer before power is turned on, you will hear a beep until the UniPak™ is installed.

When turned on, the programmer will perform an automatic self-test routine. When the self-test routine is complete, the programmer will signal its readiness.

To turn the programmer on, do the following:

1. Check to make sure a device is not in a socket. If a device is in a socket, lift up the lever (located on the upper left of the socket; see section 3.7), then gently lift the device out of the socket.
2. Plug the AC power cord into the power outlet.
3. Flip the power switch up to the "ON" position (see figure 3-2).

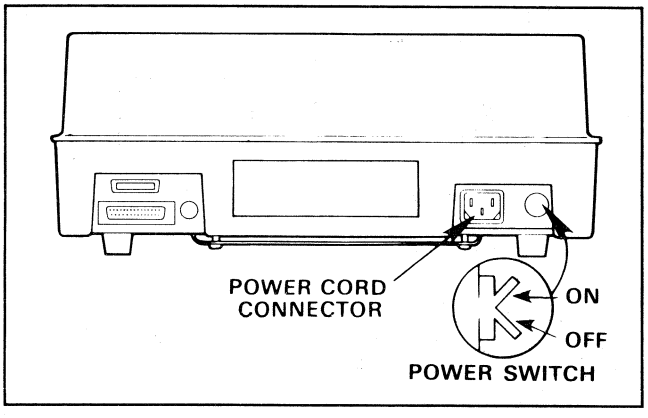


Figure 3-2. Programmer Power Switch Location

3.3 POWER DOWN

CAUTION

Do not turn the power off while the programmer is doing an operation or when a device is in a socket; voltage transients may damage the device.

To turn the programmer power off, do the following procedure:

1. Check to make sure that the programmer is not in the middle of an operation. If it is, wait until that operation is through.
2. Check to make sure a device is not in a socket. If a device is in a socket, remove it as described in section 3.7.
3. Flip the power switch down to the "OFF" position (figure 3-2).

3.4 BASIC OPERATION

All data transfer or verification operations take place between the programmer's internal RAM and the device or between the RAM and serial port in your programmer. Because the procedure to transfer data via a serial port varies from programmer to programmer, this manual describes only data transfer with the 29A. For other programmers, refer to your programmer operation manual.

The basic data transfer operations that can be performed with the UniPak™ and the 29A Universal Programmer are:

- Load RAM with data from a master device (described in section 3.4.1).
- Verify RAM data against the device data (described in section 3.4.2).
- Program a device with RAM data (described in section 3.4.3).

3.4.1 Load RAM With Data From Master Device

To load the 29A RAM with data from a master device, follow the steps listed below.

1. Press COPY; 29A displays COPY DATA FROM
2. Press DEVICE; 29A displays DEV ∧ ADDR/SIZE TO

NOTE

The device is the source of data.

3. Press RAM; 29A displays CO DEV > RAM ∧ ADDR

NOTE

The RAM is the destination of the data from the master device.

4. Press START; 29A displays FAM ∧ ØØ PIN ØØ
5. Enter the family code and pinout code (see section 3.5).
6. Insert the master device into the UniPak™ (see section 3.6).
7. Press START; 29A displays LOADING DEVICE
   LOAD DONE XXXX

NOTE

XXXX is the sumcheck of the device.

8. Remove the master device from the UniPak™ (see section 3.7).

During source destination operations (copy and verify), ADDR and SIZE appear in the 29A prompts. These correspond to starting address and block size, respectively. For more detail on these parameters, see your programmer operation manual. When reading a device, the UniPak™ applies a nominal V_{CC} level. To simulate loading on device outputs, each output is driven by a 1.6 mA current source.

3.4.2 Verify RAM Data Against Master Device Data

The two-pass verify consists of comparing the device data to RAM data and is performed at two V_{CC} levels; these levels, plus the output-sink currents and the output-level-sense voltages, vary according to each manufacturer's requirements.

To verify that data entered in the 29A RAM duplicates the master device data, follow these steps:

1. Press VERIFY; 29A displays VERIFY DATA FROM
2. Press DEVICE; 29A displays DEV ∧ ADDR/SIZE TO

NOTE
The device is the source of data.

3. Press RAM; 29A displays VE DEV > RAM ∧ ADDR

NOTE
The RAM is the destination of the data from the master device.

4. Press START; 29A displays FAM ∧ ØØ PIN ØØ
5. Enter the family code and pinout code (see section 3.5).
6. Insert the master device into the UniPak™ (see section 3.6).
7. Press START; 29A displays VERIFY DEVICE
   VE DEV DONE XXXX

NOTE
XXXX is the sumcheck of the device.

8. Remove the master device from the UniPak™ (see section 3-7).

3.4.3 Program Device With RAM Data

When programming a device, the system performs illegal-bit tests and blank checks at nominal V_{CC} and with nominal output loading.

To program a blank device with the data in the 29A RAM, follow these steps:

1. Press COPY; 29A displays COPY DATA FROM
2. Press RAM; 29A displays RAM ∧ ADDR/SIZE TO
3. Press DEVICE; 29A displays CO RAM > DEV ∧ ADDR
4. Press START; 29A displays FAM ∧ ØØ PIN ØØ
5. Enter the family code and pinout code (see section 3-5).
6. Insert the blank device into the UniPak™ (see section 3-6).
7. Press START; 29A displays TEST DEVICE
   PROGRAM DEVICE
   VERIFY DEVICE
   PRG DONE 01 XXXX
8. Remove the device from the UniPak™ (see section 3.7).

NOTE
XXXX represents the sumcheck of the device.

3.4.4 Extended Select Functions

In addition to the three basic source-destination functions (copy, verify and edit) and the select functions described in the Operation section of your programmer manual, the UniPak™ offers five extended select functions (CC, C3, CE, CF and EF). These functions are not required for normal operation of the UniPak™.

The extended select functions may be used from either the keyboard or from remote control.

Function CC displays the family and pinout codes of the last algorithm moved to RAM, usually the algorithm for the last device programmed or read.

To display the family and pinout codes of the last algorithm moved to RAM, follow the procedure below.

1. Press SELECT; 29A displays SELECT CODE ∧
2. Press C3 START; 29A displays FXX PYY OPTIONS
3. Press START; 29A displays "NAME OF FIRST OPTION"

To select different options, press the REVIEW key. To execute an option, press START (in terminal remote, the RETURN key is used for the START key, and the space bar is used for the REVIEW key). If the option has subheadings under it, once the START key has been pressed, the REVIEW key can select the desired subheading. The START key is then pressed to execute the subheading. Once an option has been completely executed, an asterisk will be displayed after the option name. Complete execution may require doing a number of subheadings. Pressing the START key a second time after an option is completely executed will exit the options file, and the 29A will display OPTIONS DONE **.

NOTE

For the 8751H, the option "PROG SECTY ONLY" will program the security fuse as soon as the option is selected and executed.

Functions CE and CF are used to set the reject count (the number of programming pulses applied to a fuse or cell before it is rejected); CE sets the reject count back to the commercial specification (this is the default value) and CF sets the single-pulse reject count. This feature was accomplished in older UniPak™ models by adding 50 to the family code.

To select the commercial (default) reject count (CE), follow the procedure below.

1. Press SELECT; 29A displays SELECT CODE ^
2. Press CE START; 29A displays SELECT CODE **

To select the single-pulse reject count (CF), take the following steps:

1. Press SELECT; 29A displays SELECT CODE ^
2. Press CF START; 29A displays SELECT CODE **

Function EF calls up a four-digit hexadecimal configuration number and a two-digit decimal version number that correspond to the revision level and version number of the UniPak™ firmware. This function can be useful to identify firmware revision levels when communicating with Data I/O regarding field bulletins and updates.

To display the UniPak™ firmware configuration and version number, do the following:

1. Press SELECT; 29A displays SELECT CODE ^
2. Press EF START; 29A displays XXXX YY **

NOTE

XXXX represents the UniPak™ firmware configuration number, and YY represents the version number.

3.5 FAMILY CODE AND PINOUT CODE SELECTION

Any device that can be programmed with the UniPak™ is specified by a unique combination of a two-digit family code and a two-digit pinout code (table A-1). Once the codes for a particular device are entered, the UniPak™ remains set up for any operation with that device until new codes are entered.

Your programmer manual will tell you where in the key sequence the family and pinout codes should be entered. If you enter invalid family and pinout codes, a beep will sound as you press either START or ENTER, or Err 30 (error 30) will be displayed and the operation will be aborted.

To select the family and pinout codes, procede as follows:

1. Locate the manufacturer name and part number stamped on the device.
2. Go to table A-1, column 1, and find the manufacturer's name.
3. Go to table A-1, column 3, entitled "PROM Part Number" and find the number corresponding to the number on the device.
4. Go to column 4 ("Family Code") and column 5 ("Pinout Code") to find the code numbers corresponding to the device number.
5. Enter the family and pinout codes you selected from table A-1.
6. Push "START."

NOTE

An LED (light-emitting diode) will light under one of the sockets.

Valid family and pinout codes must be in effect to use the System 19 DEVICE DATA key. When you press the DEVICE DATA key, either nominal, first-pass, or second-pass verify levels are applied to the device. The level applied depends on the System 19's position in executing the selected mode. If the KEYBD light is on, the nominal verify level is applied.

3.6 DEVICE INSERTION

Once you have chosen the appropriate family and pinout codes, the UniPak™ is ready to accept a device in the socket located above the lighted LED.

A good electrical connection between the device and the socket is essential. To ensure a good connection, do the following:

1. Check to make sure the programmer is not doing an operation. If it is, wait until the operation is complete.
2. Lift up the lever on the upper left side of the socket above the lighted LED (see figure 3-3). The lever will stay locked in the upright position.
3. Gently insert the device in the socket above the lighted LED. Make sure pin 1 of the device is aligned with pin 1 of the socket, as shown in figure 3-3.
4. Push the lever down to lock the device in the socket.

Once you have entered the family and pinout codes, the UniPak™ is ready for device-related operations. The key sequence to load, program, and verify is described in the Operation section of your programmer manual.

3.7 DEVICE REMOVAL

1. Check to make sure the programmer is not doing an operation. If it is, wait until the operation is complete.
2. Flip up the lever on the left side of the socket (see figure 3-3). The lever will lock in the upright position.
3. Lift the device out of the socket; the LED will remain illuminated.

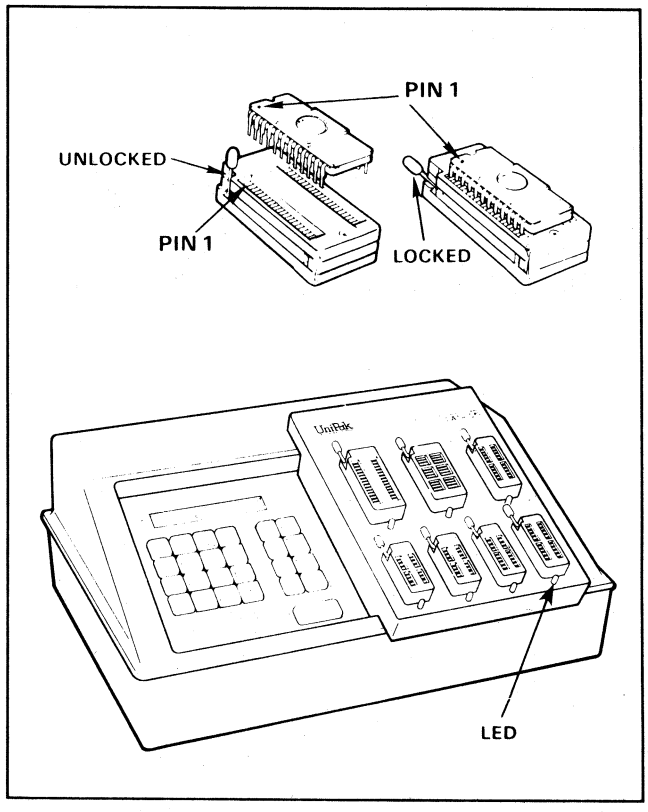


Figure 3-3. UniPak™ Sockets and Device Installation

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SECTION 4
MAINTENANCE/TROUBLESHOOTING/CALIBRATION

4.1 OVERVIEW

The support material in this section has been provided to help you keep your UniPak™ in good operating condition. General maintenance practices are discussed in section 4.2, while the basic troubleshooting steps are listed in section 4.3. For those UniPak™ users who prefer to do their own calibration, detailed procedures, including measurement charts and timing diagrams, are provided in section 4.4.

4.2 MAINTENANCE

Before the UniPak™ can be cleaned (section 4.2.2) and/or inspected (section 4.2.3), it must be disassembled as described below.

4.2.1 UniPak™ Disassembly

To disassemble the UniPak™, refer to figure 4-1 and follow the procedure outlined below.

1. Remove the UniPak™ from the programmer; see section 2.3 for details.
2. Place the UniPak™ face down on a flat surface.
3. Unscrew the captive fasteners (figure 4-1a) until they hang loosely; the screws will not separate from their standoffs.
4. Lift the card cage up slightly, then pull out (as shown in figure 4-1b) to unlock the flanges.

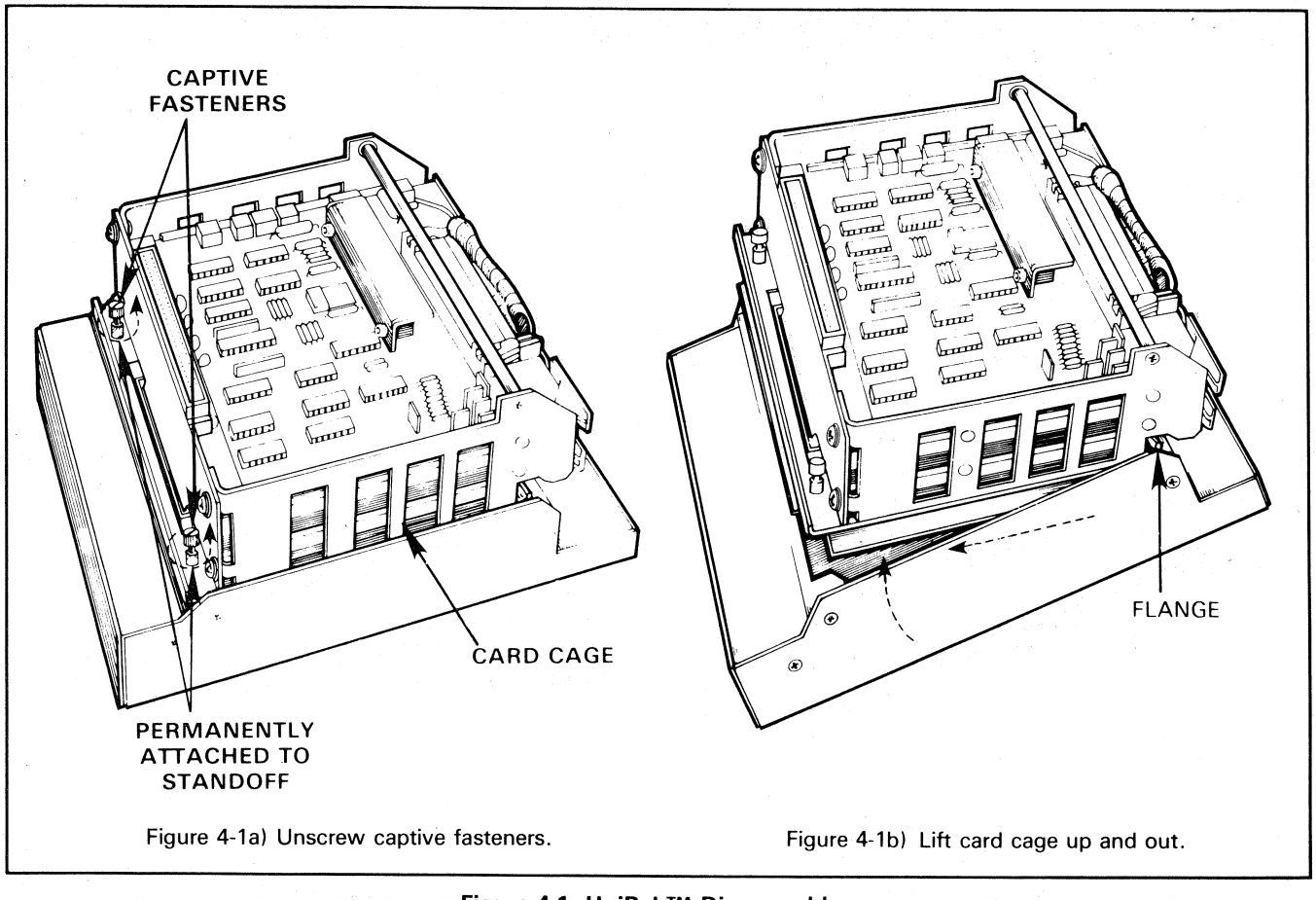


Figure 4-1a) Unscrew captive fasteners.

Figure 4-1b) Lift card cage up and out.

Figure 4-1. UniPak™ Disassembly

5. Lift the card cage up until you can see the socketboard interconnect cable and its connector (figure 4-2).
6. Flip the extraction tabs out on each side of the connector (figure 4-2).
7. Pull the cable out of the connector.
8. Flip the extraction tabs out on the top card (waveform generator card) and unplug the interconnect cable from its connector (figure 4-3).
9. Flip the extraction tabs out on the top card (waveform generator card).
10. Pull the waveform generator card out along the guides (figure 4-3).
11. Repeat steps 9, 10, and 11 for the extraction tabs on the address card.
12. Remove the two screws and the shield, and pull the memory card down to unplug it from the edge connector (as shown in figure 4-4).

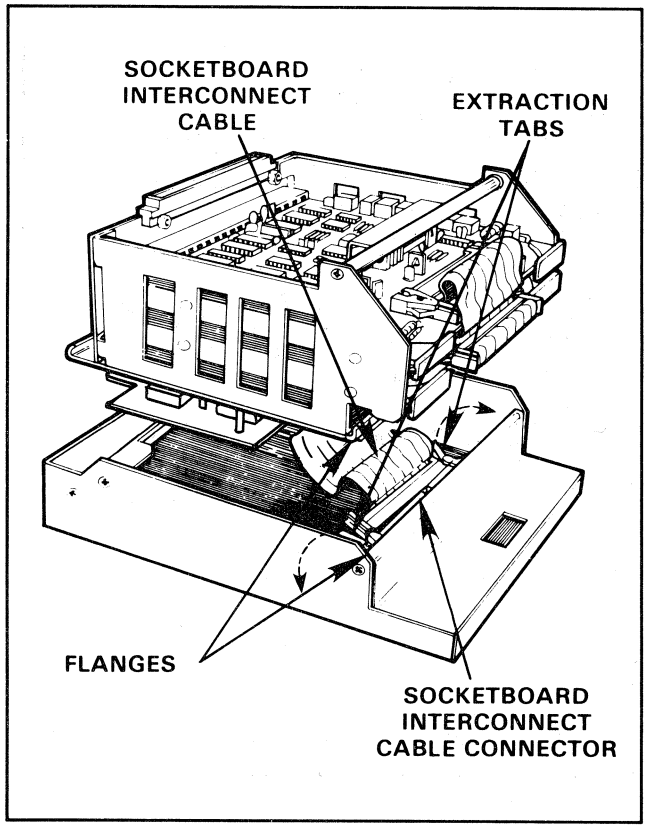


Figure 4-2. Socketboard Interconnect Cable Disconnect

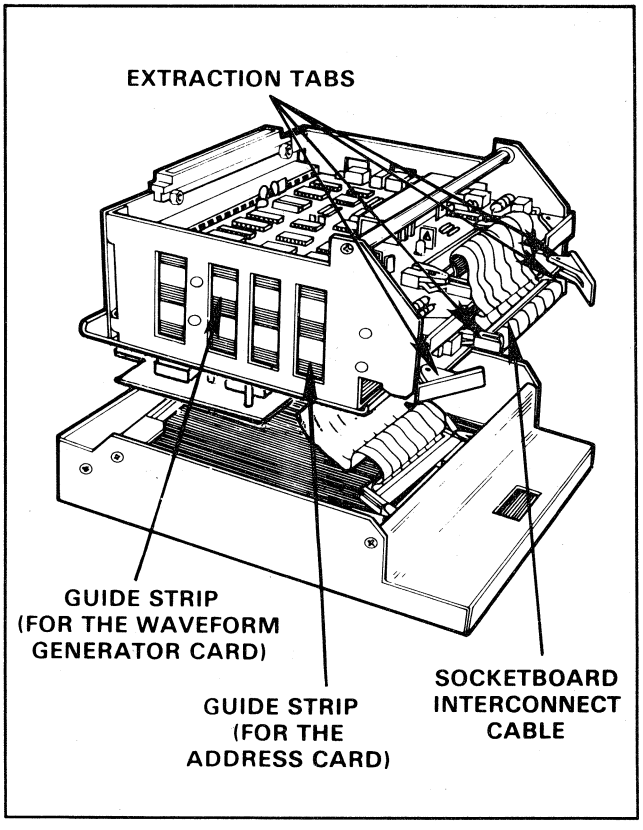


Figure 4-3. Circuit Board Removal

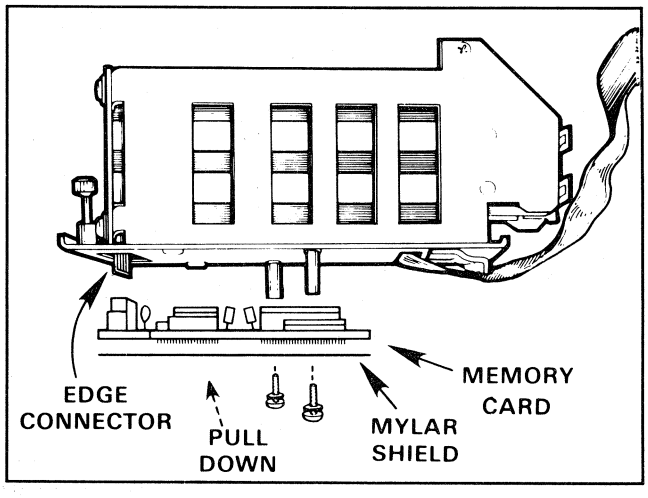


Figure 4-4. Memory Card Removal

4.2.2 Cleaning

Inspect the UniPak™ inside and out for accumulated dirt or dust. To clean the UniPak™, follow the procedure below.

1. Wipe any dust and/or dirt off the outside of the UniPak™ with a clean, damp cloth.

   NOTE
   Do not use abrasive cleaners or solvents. They will etch the paint.

2. Remove dust from the circuit boards with a blast of dry, compressed air or a clean, soft-bristled brush.

4.2.3 Inspection

You can help prevent malfunctions by periodically inspecting your UniPak™. Check cable connections, card seating, mounting of socketed components, etc., for shorts, opens or unstable continuity.

If you find heat-damaged components, be particularly careful to find and correct the cause of the overheating. This will prevent further damage.

4.2.4 UniPak™ Assembly

1. Plug the memory card onto its edge connector, as shown in figure 4-4.
2. Replace the shield, washers, and the two screws.
3. Flip the two extraction tabs down on the address card.
4. Using the flat surfaces of the extraction tabs, gently push the address card along the guides into its connector.
5. Make sure the extraction tabs on the interconnect cable connector are flipped open.
6. Firmly, but gently, push the socketboard interconnect cable into the connector. Notice that the extraction tabs will move back to their locked positions when the cable is locked into the connector.
7. Repeat steps 3 through 6 to replace the waveform generator card.
8. Plug the socketboard interconnect cable into its connector on the socketboard (figure 4-2).
9. Replace the card cage by tilting it up to lock the flanges, as shown in figure 4-1, then gently setting it down. Make sure the captive fasteners line up with the fastener holes on the UniPak™ frame.
10. Tighten the captive fasteners finger tight.

4.3 TROUBLESHOOTING

This section will help you interpret and isolate failures in the UniPak™. Use it in conjunction with section 5 (Circuit Description) and the schematics provided in the back of this manual.

Three major classes of failures can occur in a system comprised of a programmer and a UniPak™. The first is no system operation, the second is poor yields, and the third is UniPak™ failure.

After successfully troubleshooting the UniPak™, you must calibrate it according to the instructions in section 4.4. It is very important that the programmer be calibrated before the UniPak™ is calibrated.

4.3.1 No System Operation

You should perform the following steps if the system will not initialize with the UniPak™ installed. After completing each step, determine whether the problem still exists.

1. Check to be sure the UniPak™ is properly installed in your programmer.
2. Check the UniPak™ programmer mating connector (J1) for bent or broken pins. (Pin HH is intentionally shorter.)
3. Check the UniPak™ cards to be sure they are correctly installed in their connectors (section 4.2).
4. Check the ribbon cable to be sure it is properly inserted in the connectors (section 4.2).
5. Check the programmer power supplies for proper voltage output levels (see programmer manual).
6. If steps 1 through 5 fail to isolate the problem, contact your local Data I/O Service Center.

4.3.2 Poor Yields

If the yield rate begins to decrease, perform a complete calibration (see Section 4.4). Be sure that the programmer has been calibrated first.

After calibration, if the problem still exists, contact your local Data I/O Service Center.

4.3.3 UniPak™ Failure

Perform the following steps if a device will not program at all or if error messages are displayed. After completing each step, determine whether the problem still exists.

1. Check that the family and pinout codes are correct for the device, and that the device is being inserted in the correct socket.
2. If possible, try a known-good device to determine whether there is a hardware problem.
3. Check to be sure the UniPak™ is properly installed.
4. Check the UniPak™ programmer mating (J1) connector for bent or broken pins. (Pin HH is intentionally shorter.)
5. Check the UniPak™ cards to be sure they are correctly installed in their connectors (section 4.2).
6. Check to be sure the ribbon cable is correctly oriented and properly inserted in the connectors.
7. Perform a complete calibration, noting any measurements falling outside the indicated limits. Refer to the corresponding test number in table 4-1 for suspected boards and components, as well as the circuit description (section 5) and the schematics, to attempt to isolate the problem.
8. Perform waveform observations and note any discrepancies. Referring to the circuit description and the schematics may be helpful in isolating the problem.
9. If steps 1 through 8 fail to resolve the problem, contact your local Data I/O Service Center.

Table 4-1. Troubleshooting Chart

TEST NUMBER	SUSPECT BOARDS	SUSPECT COMPONENTS
1	701-1998	U26, U13, CR1
2	701-1998	Q1, Q2, U14
3	701-1998	U19, U13, Q3
4	701-7997	VR1, Q23, U6, U13
5	701-7997	Q17, U8, U4, U11
	702-7995	U2, CR12
6	701-7997	Q8, U1, U4, U10, Q2, Q7, Q14, Q24, Q1
7	701-7997	Q10, U3, U4, U9, Q1, Q13
	701-1998	U18, Q4-11, U16, U17
8	701-1998	U26, U13, CR1
9	701-1998	U26, U13, Q3
10	701-1998	U26
11	701-7997	U13, U14, U7, Q13, Q1, Q9
	701-1998	U18, Q4-11, U16, U17
12	701-7997	U12, U11, U4, U8, Q17
	702-7995	CR12, U2
13	701-7997	U12, U9, U4, U3, Q10
14	701-7997	U12, U10, U4, U1, Q8
15	701-1998	U25, U26, U13, CR1
16	701-1998	U25, U19, U13, CR1, Q3
17	701-7997, 701-1998	
18	702-7995	DS2, U1
19	701-7997	U1, CR8, U6, Q16, R39
20	701-7997	Q8, U1, U4, U10, Q2, Q7, Q14, Q24, Q1
21	701-7997	Q1, Q4, Q2, Q20
22	701-7997	Q1, Q18, Q21
23	701-7997	Q10, U3, U4, U9, Q1, Q13
	701-1998	U18, Q4-11, U16, U17
24	701-7997	Q6, Q12, CR7, Q22
25	701-7997	U11, U4, U8, Q17
26	701-7997	U10, U4, U1, Q8
27	701-7997	Q15, U4, Q21

TEST NUMBER	SUSPECT BOARDS	SUSPECT COMPONENTS
28	701-7997	VR2, U7
29	701-7997	U11, U4, U8, Q17
30	701-7997	U12, U10, U4, U1, Q8, CR1, CR2, U5, U2, U7, Q1, Q2, U6, Q18, CR11, Q19, Q21, Q15, Q5, U11
31	701-7997	U11, U4, U8, Q17
32-35	701-1998	RP1, RP2, U3-6
	702-7995	U9, U10, Q2
36	702-7995	DS1, U1
37	702-7995	U1, CR8
38	701-1998	U1, U2, U12, Q12-19
39	701-1998	U1, U2, U12, Q12-19
40	701-1998	U1, U2, U12, Q12-19
41	701-1998	U1, U2, U12, Q12-19
42	701-7997	Q10, U3, U4, U9, Q1, Q13
	701-1998	U18, Q4-11, U16, U17
43	701-7997	Q10, U3, U4, U9, Q1, Q13
	701-1998	U18, Q4-11, U16, U17
44	701-7997	Q10, U3, U4, U9, Q1, Q13
	701-1998	U18, Q4-11, U16, U17
45	701-7997	Q10, U3, U4, U9, Q1, Q13
	701-1998	U18, Q4-11, U16, U17
46	702-7995	DS3, U1
47	702-7995	U2, CR14
48	702-7995	DS4, U1
49	702-7995	U2, CR15
50	702-7995	DS5, U1
51	702-7995	U2, CR11
52	702-7995	DS6, U1
53	702-7995	U2, CR13
54	702-7995	DS7, U1
55	702-7995	U2, CR16
56	702-7995	Q1, RP1, U3, CR17

4.4 CALIBRATION

The need for calibration varies with the amount of use your UniPak™ receives. Generally, we suggest calibration whenever: 1) programming yields fall below the manufacturer's recommended minimums, or 2) troubleshooting has been completed, or 3) the user's company policy requires periodic calibration certification.

NOTE

If calibration or repair is required but you lack the facilities to accomplish it, contact the nearest Data I/O Service Center.

Because of differences in programmer mainframes, this manual does not attempt to cover all areas of programmer calibration. Instead, it lists the steps necessary to calibrate only the UniPak™.

Calibration of the UniPak™ consists of three parts:

1. Power Supply Calibration—measures the DC supply voltages of the programmer. All other voltages depend on these supplies; therefore, this part of the calibration procedure must be done first. Refer to your programmer manual.
2. DC Calibration—consists of measuring and adjusting critical DC voltage levels generated by the UniPak™.
3. Waveform Observation—enables observation of waveforms on an oscilloscope to ensure compliance with the device manufacturers' critical voltage and timing specifications.

The first part of the calibration procedure (power supply calibration) varies with the type of programmer you have. Therefore, this manual refers you to your programmer manual for details on power supply calibration. DC calibration is discussed in section 4.4.1.

The following equipment is necessary to calibrate the UniPak™:

- Data I/O calibration extender (part number 910-1521)
- Three and a half-digit digital voltmeter (DVM)
- Dual-trace oscilloscope (Tektronix 465 or equivalent)

Check the appropriate programmer manual for any additional equipment that you may need to calibrate the programmer.

To prepare your UniPak™ for calibration, follow the procedures outlined below:

1. Turn the programmer power off; see section 3.3 for details.
2. Remove the UniPak™ from the programmer; see section 2.3 for details.
3. Insert the calibration extender into the programmer the same way you insert the UniPak™ (section 2.2).
4. Unscrew the two thumb screws (captive fasteners) located on the underside of the top cover of the UniPak™ (figure 4-1); they connect the card cage to the socket assembly. Separate the two parts of the assembly.

CAUTION

Do not let the fasteners short to the motherboard.

5. Insert the 64-pin connector of the card cage into the mating connector on the calibration extender (figure 4-5, detail B).
6. Lean the top portion of the UniPak™ against its bottom portion at a 45-degree angle (see figure 4-5).

NOTE

Be sure the socket assembly flange locks into the card cage flange (see figure 4-5, detail A).

Do not allow the frame of the socket assembly to short to the memory board.

Be careful not to strain the cable or scratch the top of the programmer.

4.4.1 DC Calibration

The DC calibration procedure described in this section enables you to adjust critical DC voltage levels generated by the UniPak™. To follow this procedure, use the measurement chart at the end of this section. This measurement chart contains the information needed for all DC calibration tests. This information is included on the measurement chart in columns with the following headings:

- Step No.—tells which step to use for each test. Step numbers are set at the programmer keyboard and reflected in the display.
- Test No.—identifies individual tests.
- Test Description—identifies the functions being tested.
- Measurement Test Location—tells which socket pins, circuit boards, or test points to probe for measuring voltages.
- Measurement—specifies allowable measurement ranges. If a reading falls outside the range and you cannot adjust it to within the range, do not use the UniPak™ until the problem is corrected.
- Adjustment Location—tells which potentiometer to adjust if a measurement is out of range.
- Comments—gives special instructions for particular tests.

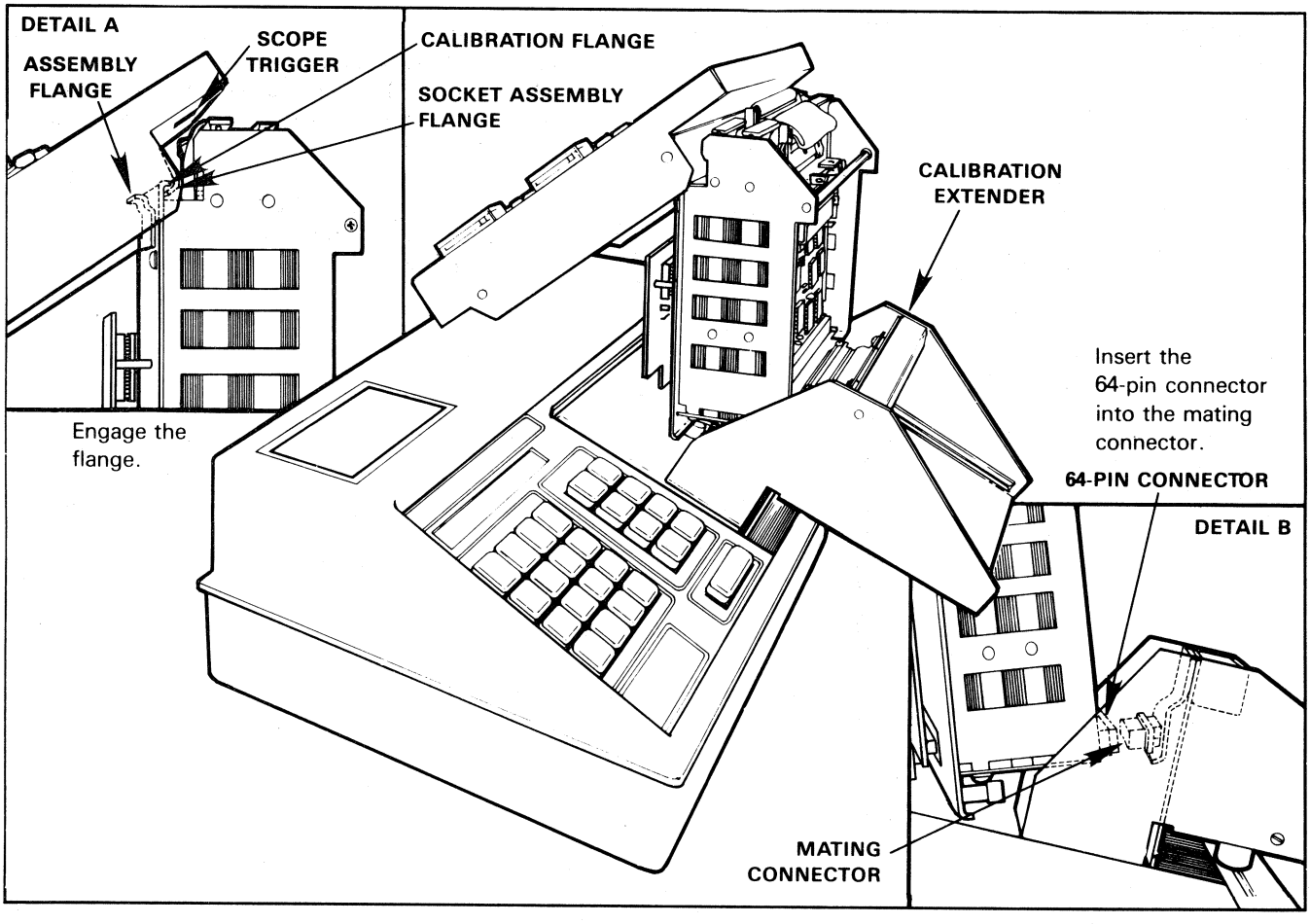
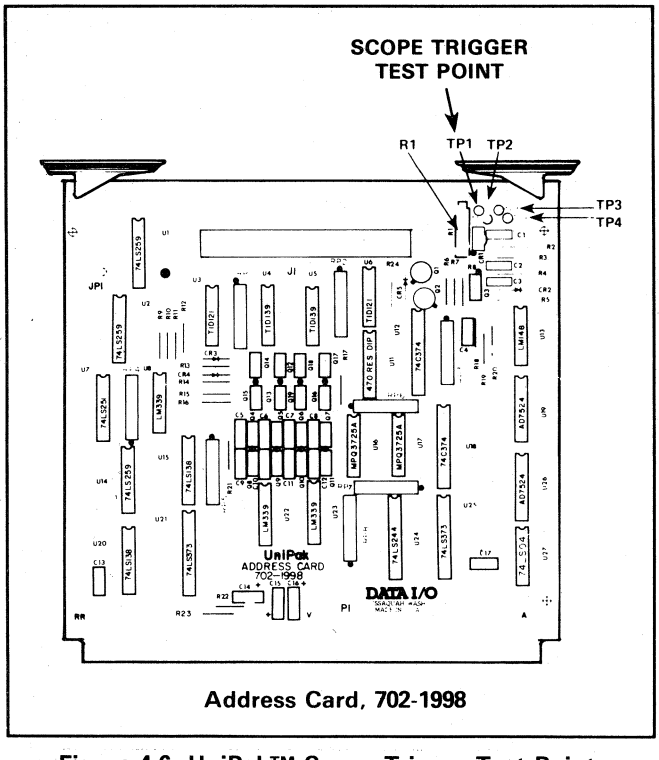


Figure 4-5. Calibration Setup

The DC calibration procedure is as follows:

CAUTION
Remove all devices from the sockets before entering the calibration mode (see section 3.8 for details). Waveform generation may damage any device in the UniPak™ sockets.

1. Turn the programmer power on (section 3.2).
2. Put the programmer into the calibration mode by following the key sequences in table 4-2.
3. Perform the general calibration steps (steps 1 through 20) on the measurement chart. For steps 4 and 5, refer to the figures at the end of the measurement chart to observe the bit switch rise waveform and the DAC step waveforms. Trigger your oscilloscope by connecting to the test point under the top edge of the socket assembly (see figure 4-6).



Address Card, 702-1998

Figure 4-6. UniPak™ Scope Trigger Test Point

Table 4-2. Key Sequence To Access the Calibration Mode

Programmer System	Key Sequence To Enter Calibration Mode	To Increment Step No.	To Decrement Step No.
19	Press SELECT Press C2 Press ENTER Enter Step Number* Press START	Press ENTER	Press REVIEW
29A/ 29B	Press SELECT Press C1 Press START Enter Step Number* Press START	Press START	Press REVIEW
100A	Press SELECT Press 12 Enter Step Number* Press START	Press START	Press BACK-SPACE

*Optional

For each general calibration step on the measurement chart do the following:

- Take measurement readings at the device sockets or test points indicated on the measurement chart; figure 4-7 shows the pin numbers for the sockets; figure 4-8 shows test points.
- Ground the digital voltmeter to socket 7, pin 10 on the front panel of UniPak™.

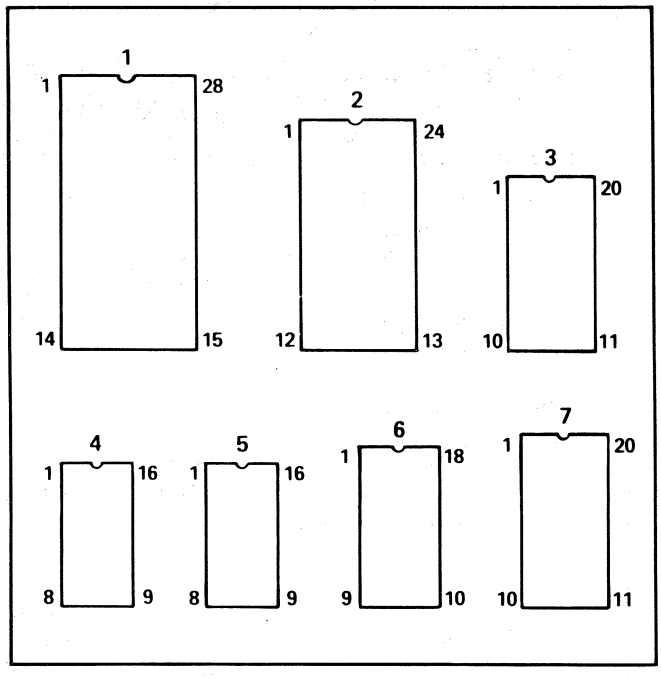
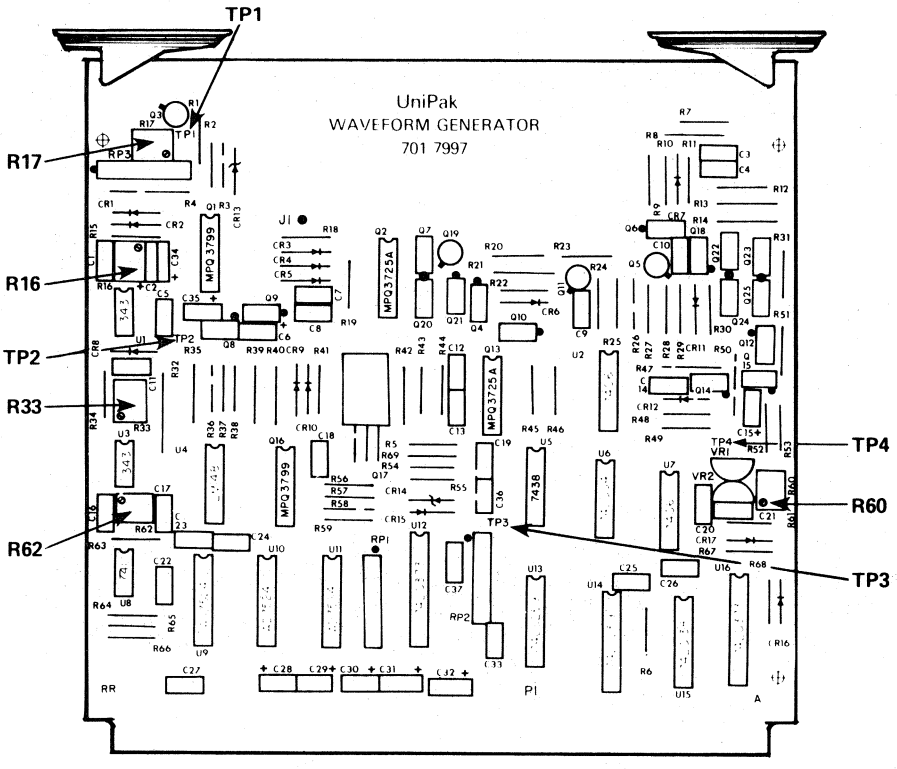
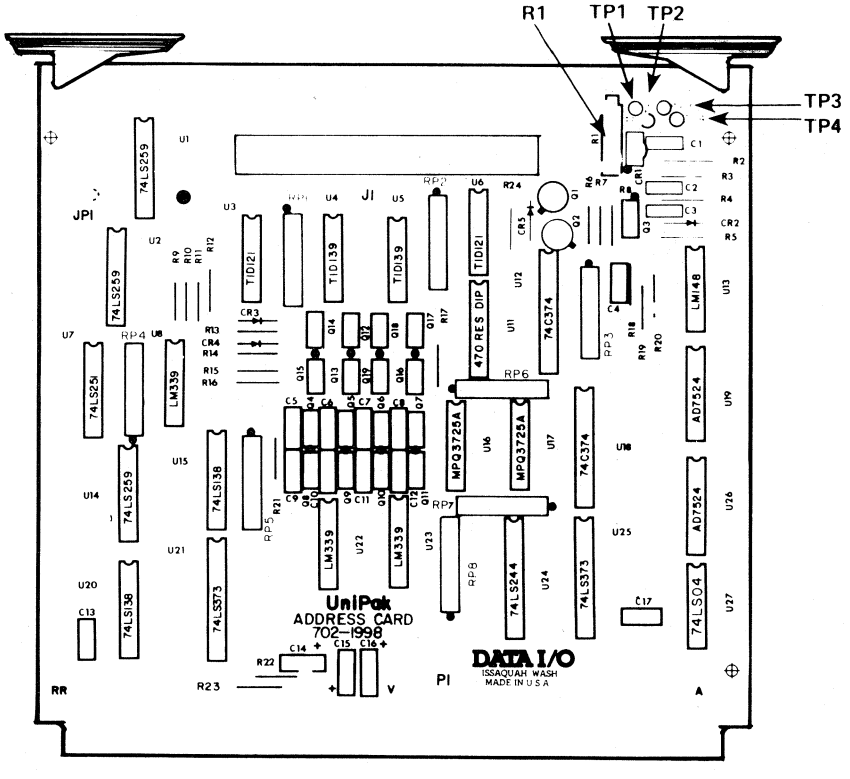


Figure 4-7. Pin Numbers of Device Sockets

- The adjustment pots on the waveform generator, memory board, and the address card enable you to make adjustments when your measurements do not match the measurement chart; figure 4-7 shows the location of these adjustment points.
- Access each new step by pressing the START (or ENTER) key. The new step number will appear in the display when the UniPak™ is ready for the next step. To go back to a previous test, press the REVIEW (or BACKSPACE) key.



a. Waveform Generator, 701-7997



b. Address Card, 702-1998

Figure 4-8. Adjustment Locations

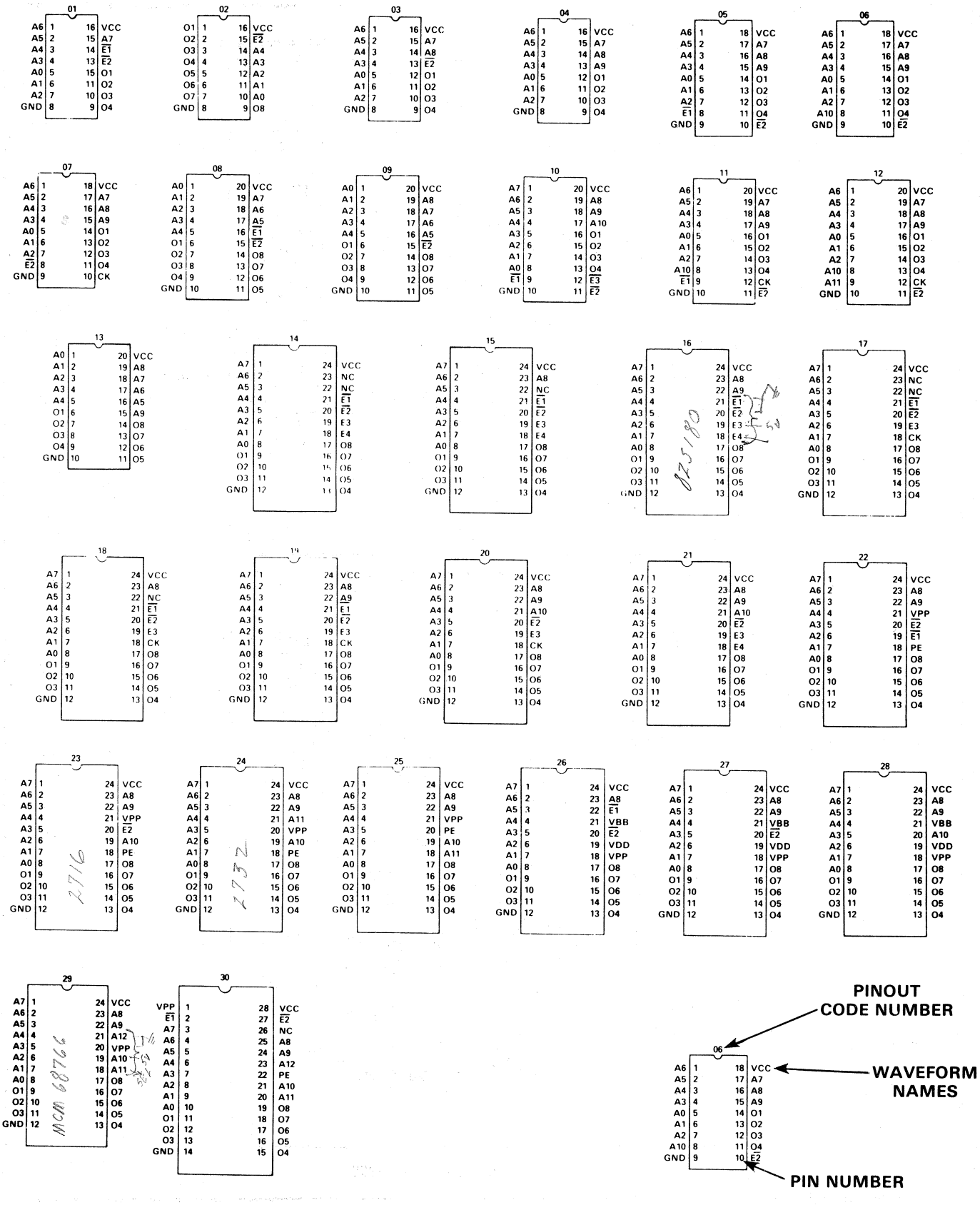


Figure 4-9. Pin Names by Pinout Code Numbers

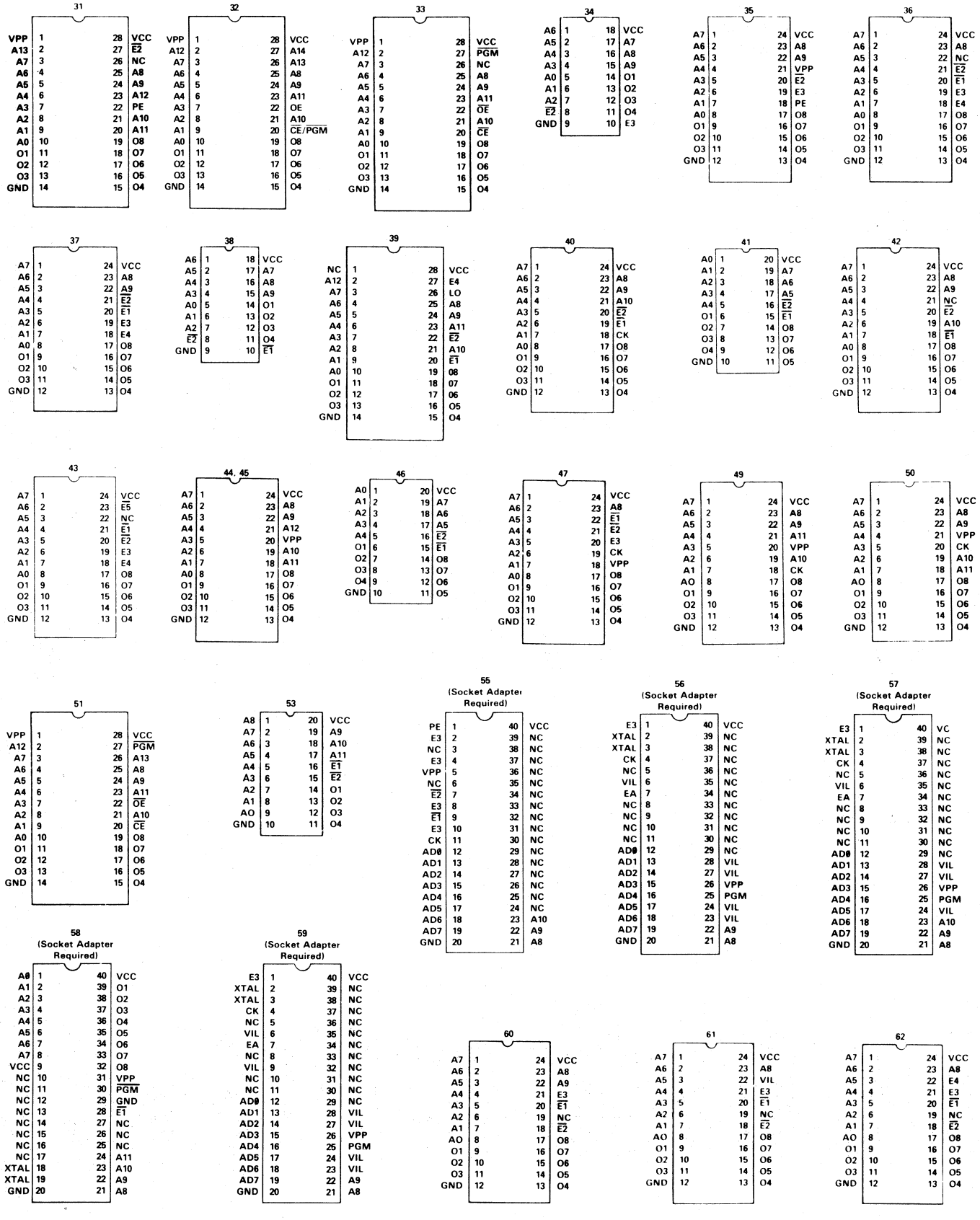


Figure 4-9. Pin Names by Pinout Code Numbers (Continued)

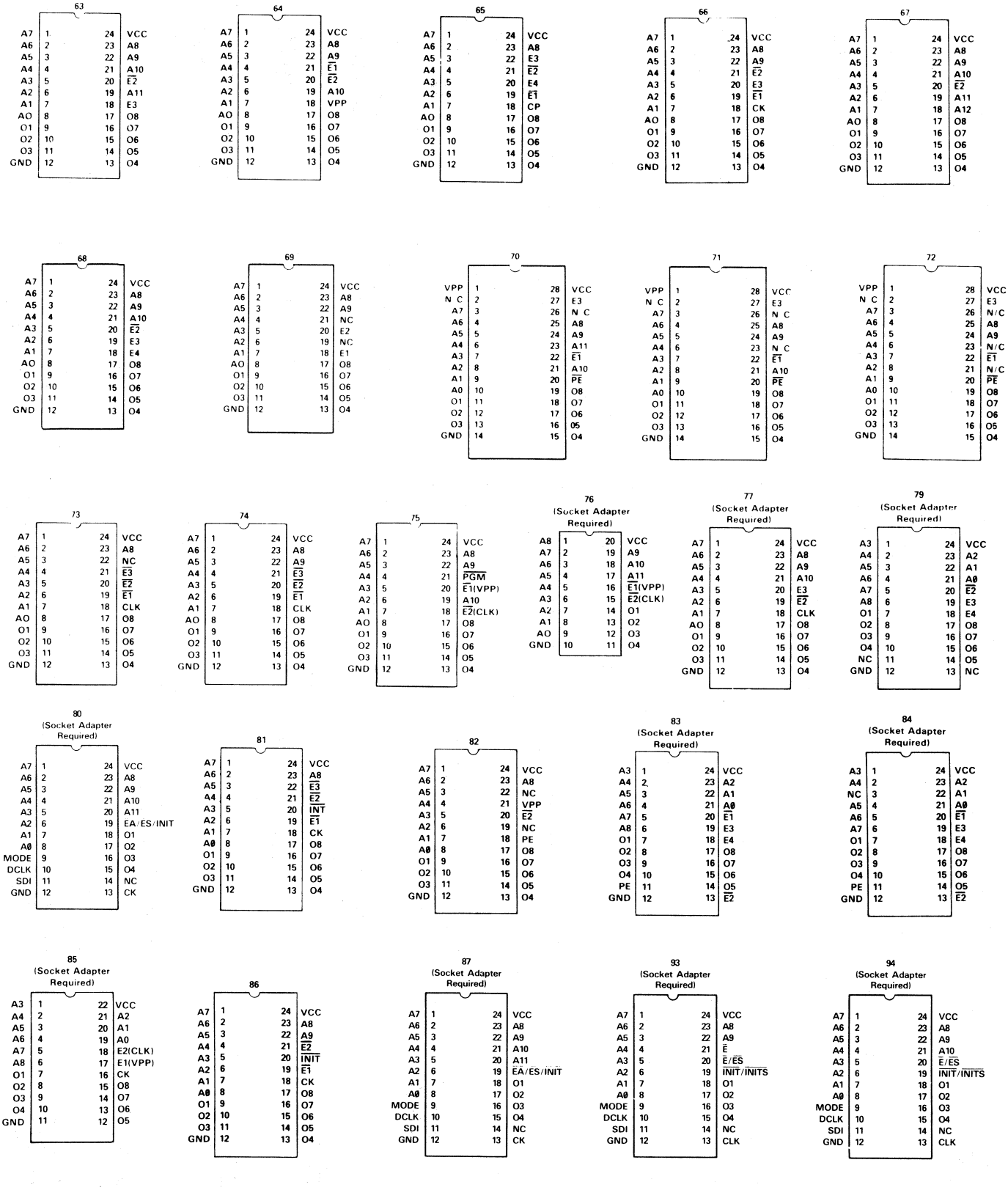


Figure 4-9. Pin Names by Pinout Code Numbers (Continued)

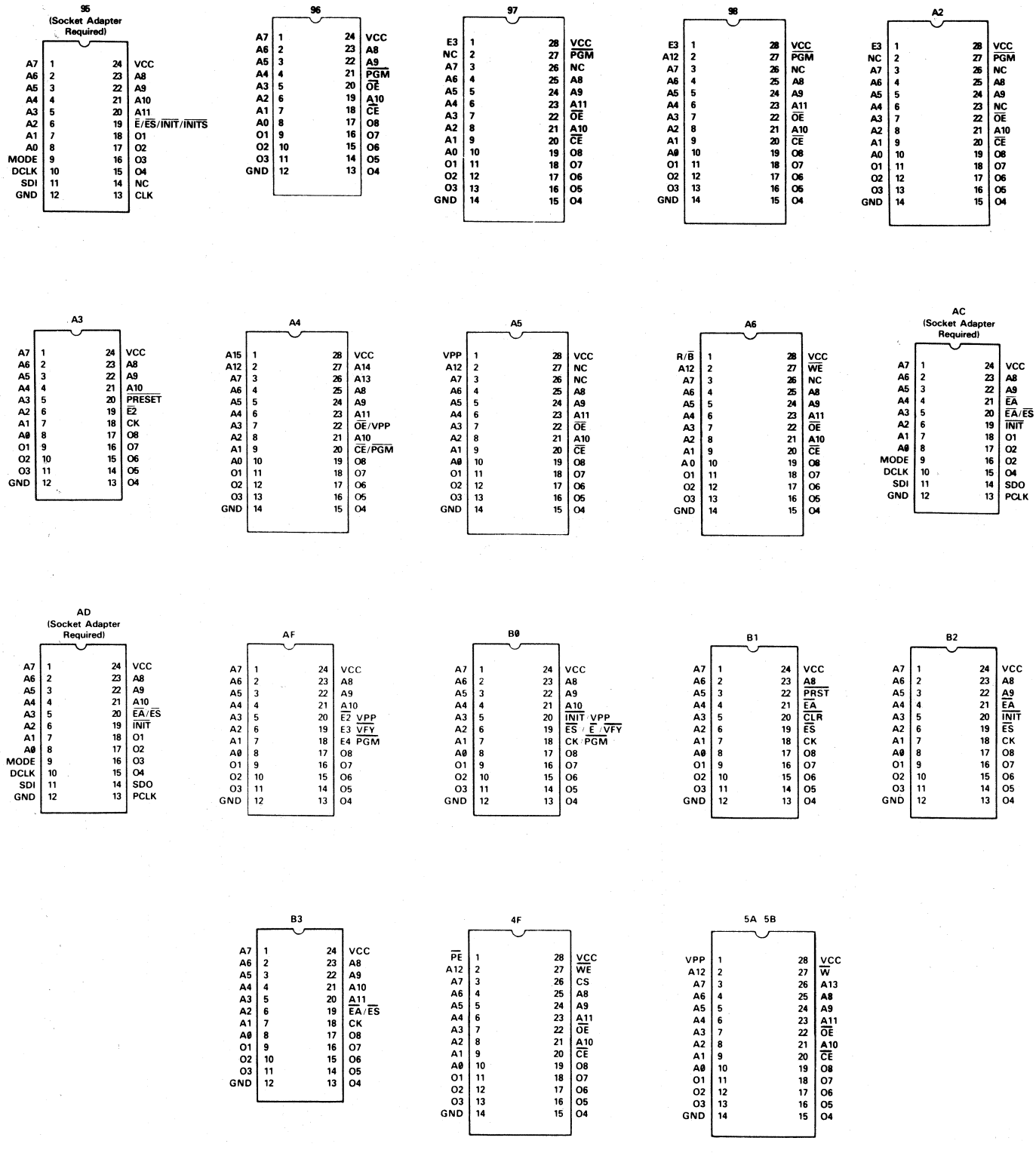


Figure 4-9. Pin Names by Pinout Code Numbers (Continued)

Table 4-3. Measurement Chart

REVISIONS

LTR	DESCRIPTION	P.E.	DATE
D	ECN 5022		2/84

UniPak™ Measurement Chart

STEP	TEST NO.	TEST DESCRIPTION	MEASUREMENT LOCATION	MEASUREMENT MIN	MEASUREMENT NOM	MEASUREMENT MAX	ADJUSTMENT LOCATION	COMMENTS Ground DMM to socket 7, pin 10
			The following tests are performed					
			with the UniPak™ on the calibration					
			extender:					
			Socket/Pin					
1	1	V reference supply	701-1998/TP4	10.20V	10.24V	10.28V	R1,701-1998	
	2	Load supply (high range)	701-1998/TP2	24.3V		25.7V		
	3	Load supply (low range)	701-1998/TP3	24.3V		25.7V		
	4	Supply reference	701-7997/TP4	4.98V	5.00V	5.02V	R60, 701-7997	
	5	V_{CC} supply	2 24	11.9V	12.0V	12.1V	R62,701-7997	
	6	CE supply	2 20	32.7V	33.0V	33.2V	R16,701-7997	
	7	Bit supply	2 9	25.7V	26.0V	26.2V	R33,701-7997	
2	8	V reference supply	701-1998/TP4	6.70V		6.90V		
	9	Load supply	701-1998/TP3	10.3V		11.7V		
3	10	V reference supply	701-1998/TP4	3.30V		3.50V		

Table 4-3. Measurement Chart (Continued)

REVISIONS

LTR	DESCRIPTION	P.E	DATE
D	ECN 5022		2/84

UniPak™ Measurement Chart

STEP	TEST NO.	TEST DESCRIPTION	MEASUREMENT LOCATION Socket/Pin	MEASUREMENT MIN	MEASUREMENT NOM	MEASUREMENT MAX	ADJUSTMENT LOCATION	COMMENTS
								Ground DMM to socket 7, pin 10
4	11	Bit switch rise waveform	2 14				R17, 701-7997	See waveform photograph (page 4-27)
5		DAC step waveforms						
	12	V_{CC}	2 24					See waveform photograph (page 4-28)
	13	Bit supply	701-7997/TP2					See waveform photograph (page 4-29)
	14	CE supply	701-7997/TP1					See waveform photograph (page 4-28)
	15	V_{REF} supply	701-1998/TP4					See waveform photograph (page 4-29)
	16	Load supply	701-1998/TP3					See waveform photograph (page 4-26)
			The following tests are performed					
			with the UniPak™ installed in its					
			normal operating position.					
6	17	All voltages off	all all	-0.1V		0.4V		
7	18	Socket 2 LED						Confirm that socket 2 LED is on
	19	V_{CC} supply load	2 24	11.8V		12.1V		Place a 20-ohm, 2W resistor between pins 24 and 12, socket 2

Table 4-3. Measurement Chart (Continued)

REVISIONS

LTR	DESCRIPTION	P.E	DATE
D	ECN 5022		2/84

UniPak^{tm} Measurement Chart

STEP	TEST NO.	TEST DESCRIPTION	MEASUREMENT LOCATION Socket/Pin	MEASUREMENT MIN	MEASUREMENT NOM	MEASUREMENT MAX	ADJUSTMENT LOCATION	COMMENTS Ground DMM to socket 7, pin 10
8	20	CE supply load	2 20	32.3V		33.2V		Place a 100-ohm, 10W resistor
								between pins 20 and 12,socket 2.
	21	Pin 18 voltage switch	2 18	32.7V		33.2V		
	22	Pin 21 voltage switch	2 21	32.7V		33.2V		
9	23	Bit supply load	2 11	25.2V		26.0V		Place a 100-ohm, 5W resistor
								between pins 11 and 12,socket 2.
	24	Pin 19 voltage switch	2 19	24.9V		25.5V		
10	25	V_{CC} voltage linearity	2 24	3.90V		4.10V		
	26	CE supply linearity	2 18	23.0V		23.5V		Place a 2.2K-ohm, 1/2W resistor
								between pins 12 and 18,
								socket 2.
	27	-5V supply	2 21	-5.2V		-4.8V		
	28	12V supply	2 20	11.4V		12.6V		
11	29	V_{CC} voltage linearity	2 24	4.90V		5.10V		

Table 4-3. Measurement Chart (Continued)

REVISIONS

LTR	DESCRIPTION	P.E	DATE
D	ECN 5022		2/84

UniPak™ Measurement Chart

STEP	TEST NO.	TEST DESCRIPTION	MEASUREMENT LOCATION Socket/Pin	MEASUREMENT MIN	MEASUREMENT NOM	MEASUREMENT MAX	ADJUSTMENT LOCATION	COMMENTS
								Ground DMM to socket 7, pin 10
	30	CE supply linearity	2 21	11.4V		12.0V		
12	31	V_{CC} voltage linearity	2 24	5.90V		6.10V		
	32	I source and pulldowns	2 9,11,14,16	2.0V		2.6V		
	33	I source and pulldowns	2 10,13,15,17	0.0V		1.0V		
13	34	I source and pulldowns	2 10,13,15,17	2.0V		2.6V		
	35	I source and pulldowns	2 9, 11, 14. 16	0.0V		1.0V		
14	36	Socket 1 LED						Confirm that socket 1 LED is on.
	37	V_{CC} voltage supply	1 28	4.90V		5.10V		
	38	Odd address and data	1 2,3,5,7,9,11,13,16,18,	3.0V		6.0V		
		high	20,22,24,26					
	39	Even address and data	1 1,4,6,8,10,12,15,17,19,21,	-0.1V		0.4V		
		low	23,25,27					
15	40	Odd address and data	1 2,3,5,7,9,11,13,16,18,	-0.1V		0.4V		
		low	20,22,24,26					
	41	Even address and data	1 1,4,6,8,10,12,15,17,19,21,	3.0V		6.0V		
		high	23,25,27					
16	42	Odd data lines high	1 11,13,16,18	25.5V		26.5V		
	43	Even data lines pullups	1 12,15,17,19	4.5V		5.5V		

Table 4-3. Measurement Chart (Continued)

REVISIONS

LTR	DESCRIPTION	P.E	DATE
D	ECN 5022		2/84

UniPak^{tm} Measurement Chart

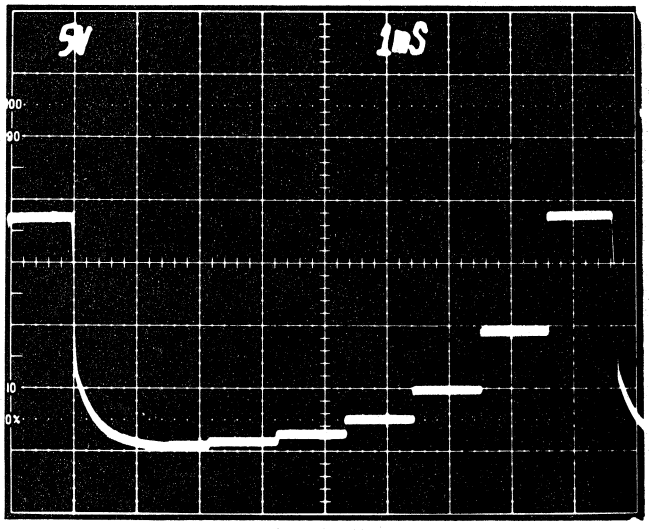
STEP	TEST NO.	TEST DESCRIPTION	MEASUREMENT LOCATION Socket/Pin	MEASUREMENT MIN	MEASUREMENT NOM	MEASUREMENT MAX	ADJUSTMENT LOCATION	COMMENTS Ground DMM to socket 7, pin 10
17	44	Odd data lines pullups	1 11,13,16,18	4.5V		5.5V		
	45	Even data lines high	1 12,15,17,19	25.5V		26.5V		
18	46	Socket 3 LED						Confirm that socket 3 LED is on.
	47	V_{CC} voltage supply	3 20	4.90V		5.10V		
19	48	Socket 4 LED						Confirm that socket 4 LED is on.
	49	V_{CC} voltage supply	4 16	4.90V		5.10V		
20	50	Socket 5 LED						Confirm that socket 5 LED is on.
	51	V_{CC} voltage supply	5 16	4.90V		5.10V		
21	52	Socket 6 LED						Confirm that socket 6 LED is on.
	53	V_{CC} voltage supply	6 18	4.90V		5.10V		
22	54	Socket 7 LED						Confirm that socket 7 LED is on.
	55	V_{CC} voltage supply	7 20	4.90V		5.10V		
23	56	V_{CC} pullup 1 on	1 28	4.0V		5.2V		
		V_{CC} pullup 2 on	2 24	4.0V		5.2V		
		V_{CC} pullup 3 on	3 20	4.0V		5.2V		
		V_{CC} pullup 4 on	4 16	4.0V		5.2V		
		V_{CC} pullup 5 on	5 16	4.0V		5.2V		
		V_{CC} pullup 6 on	6 18	4.0V		5.2V		
		V_{CC} pullup 7 on	7 20	4.0V		5.2V		

Measurement Chart

Measurement Chart

PROGRAM ELECTRONICS _____

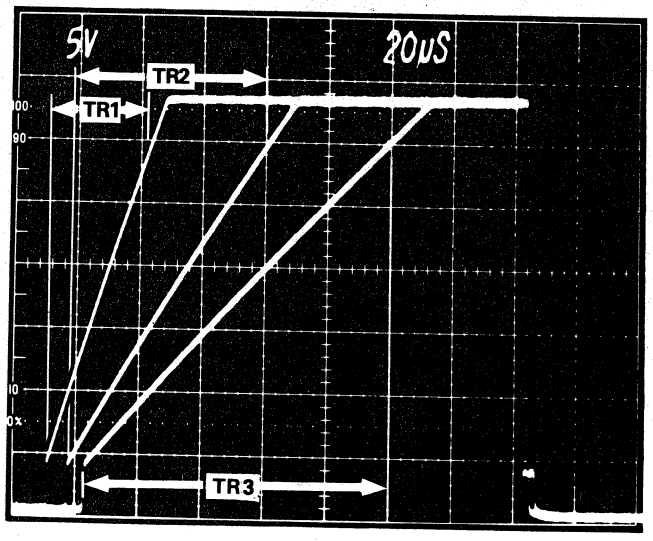
DAC Step Waveform



DAC Load Supply

DATE	REV	REVISION RECORD	DR	CK
3/84				

Bit Switch Rise-Time Waveform



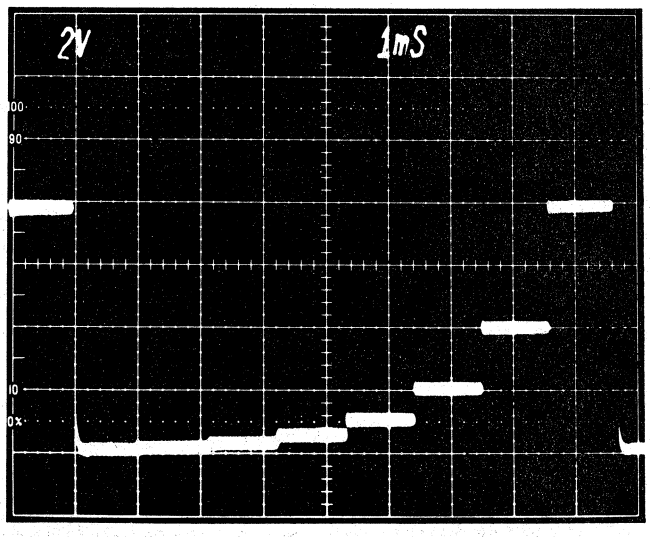
	VARIABLE	MIN	NOM	MAX	UNIT	COMMENTS
PROGRAM	TR1	26	33	37	µs	Adjust R17, 701-7997.
	TR2	62	66	70	µs	
	TR3	90	100	110	µs	

NOTE: All TR's are measured from 10% to 90%.

Measurement Chart

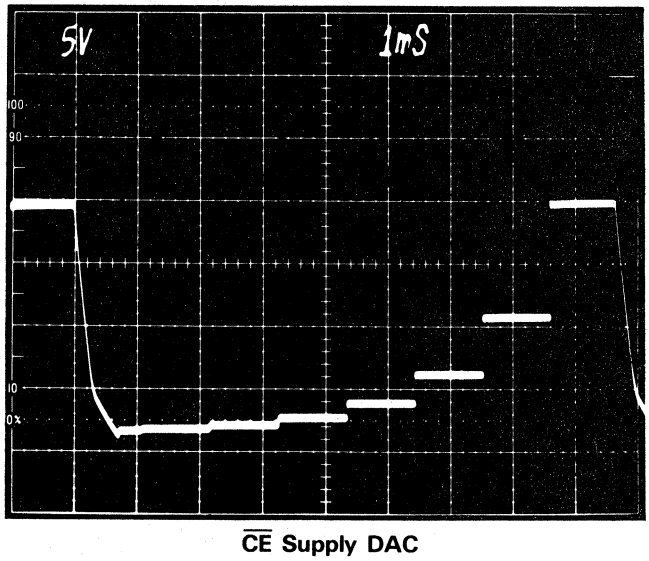
PROGRAM ELECTRONICS _____

DAC Step Waveform



V_{CC} DAC

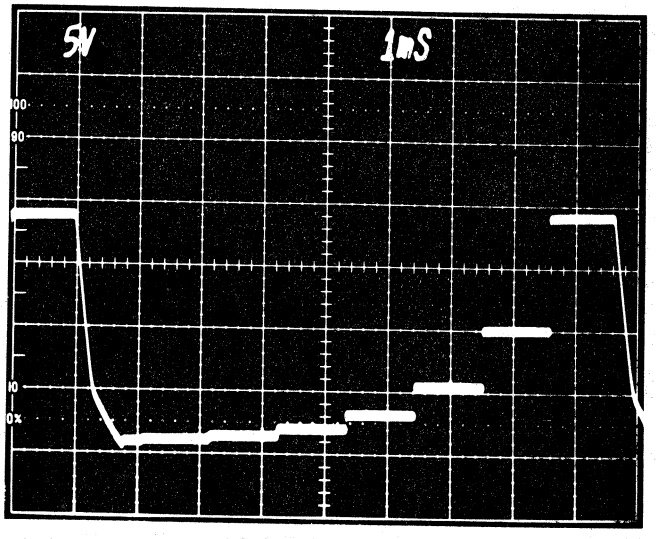
DAC Step Waveform



$\overline{CE}$ Supply DAC

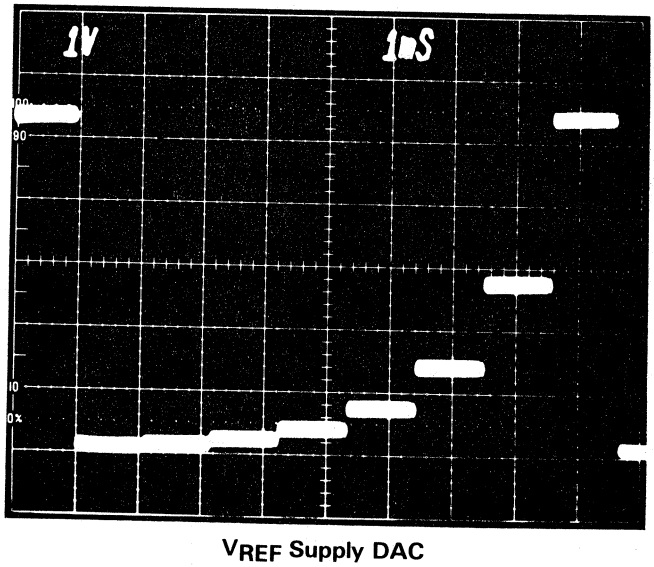
DATE	REV	REVISION RECORD	DR	CK
3/84				

DAC Step Waveform



Bit Supply DAC

DAC Step Waveform



V_{REF} Supply DAC

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SECTION 5
CIRCUIT DESCRIPTION

5.1 OVERVIEW

This section defines the functions of UniPak™ principal hardware components. Each circuit-card assembly is depicted by a block diagram accompanied by a written description.

5.2 GENERAL ARCHITECTURE

5.2.1 The Link Between the UniPak™ and the Programmer

The UniPak™ is controlled by the programmer's extended processor bus (J6), through the UniPak™'s mating connector. Pin functions of the extended processor bus are shown in table 5-1.

The control software for the UniPak™ is located in EPROM on the memory card (702-0045).

5.2.2 The Buses

The programmer's address bus, data bus, R/$\overline{W}$ line and $\overline{V \bullet \emptyset_2}$ line access the software on the memory card and control the gates and registers on the waveform generator (701-7997) and address and data driver cards (701-1998). The UniPak™'s device bus gathers the programming waveforms produced by these cards and transmits them to the socket card (702-7995). Figure 5-1 shows the relationships between the buses.

Table 5-1. Pin Functions, Programmer's Extended Processor Bus (at J1-J3)

Pin	Function	Pin	Function
1	A_0	A	A_5
2	A_1	B	A_6
3	A_2	C	A_7
4	A_3	D	A_8
5	A_4	E	A_9
6	A_{10}	F	A_{11}
7	A_{12}	H	A_{13}
8	A_{14}	J	A_{15}
9	DO_1	K	DI_1
10	DO_2	L	DI_2
11	DO_3	M	DI_3
12	DO_4	N	DI_4
13	DO_5	P	DI_5
14	DO_6	R	DI_6
15	DO_7	S	DI_7
16	DO_8	T	DI_8
17	Ver. A	U	Ver. B
18	Start	V	Clk. Inh.
19	W/L	W	"26"
20	VOL/VOH	X	"36"
21	+5V	Y	−9V
22	+ Prog.	Z	+24V
23	GND	AA	−5V
24	Sense	BB	Operate
25	+48V	CC	Unreg. H.V.
26	GND	DD	Gnd.
27	C1	EE	C4
28	C2	FF	C5
29	C3	HH	C6
30	IRQ	JJ	Gate Enable
31	R/W	KK	Extend
32	V•Ø2	LL	+18V Raw
33	Interlock	MM	PP
34	+10V Raw	NN	RR
35	Write	PP	Read
36	Reset	RR	Fwd.

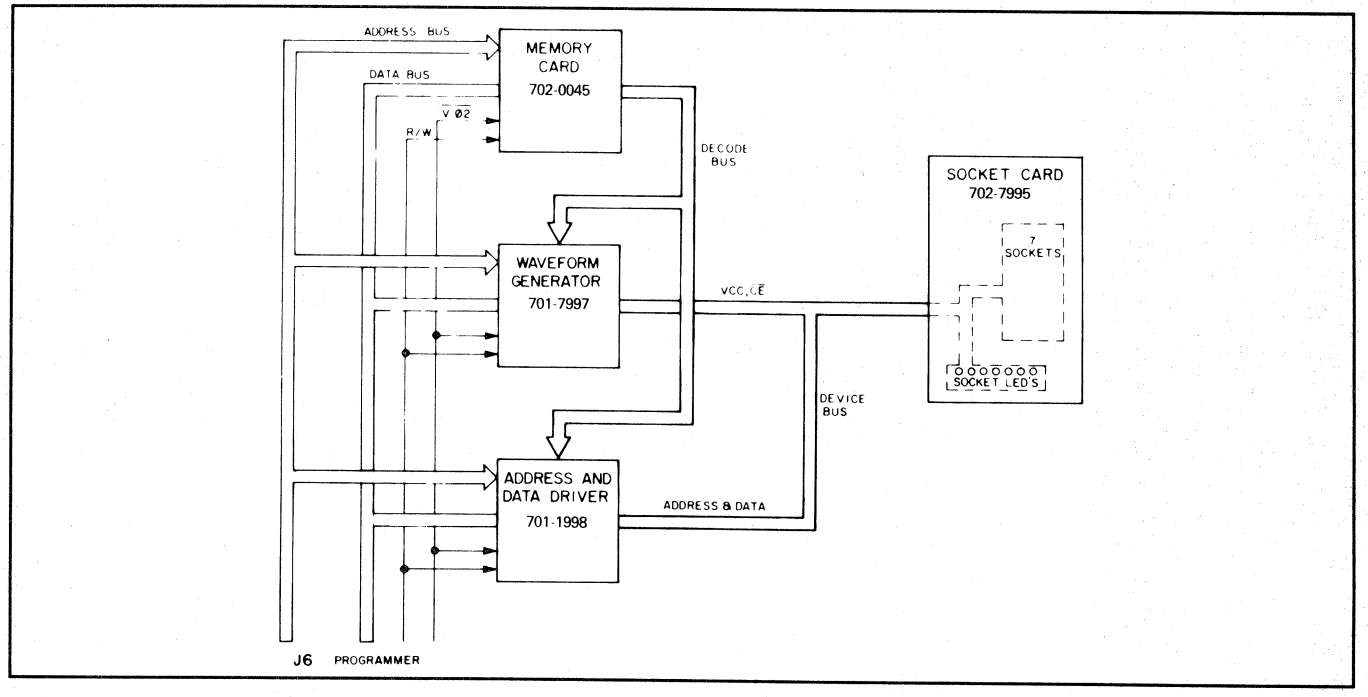


Figure 5-1. Block Diagram, UniPak™ Electronics

5.3 COMPONENT LAYOUT

Figure 5-2 shows the component layout of the UniPak™. The principal components are described in paragraphs 5.3.1 through 5.3.5.

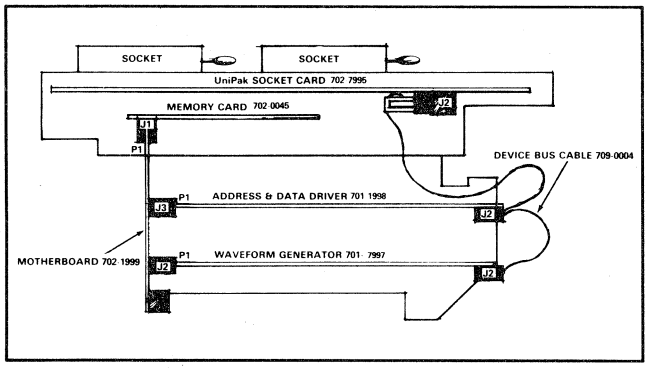


Figure 5-2. Principal Components of the UniPak™

5.3.1 Motherboard

The motherboard accepts the signals and power supplies from the J6 of the programmer and transmits them to two identical 72-pin edge connectors and a 50-pin edge connector (see figure 5-3 and schematic 008-1999).

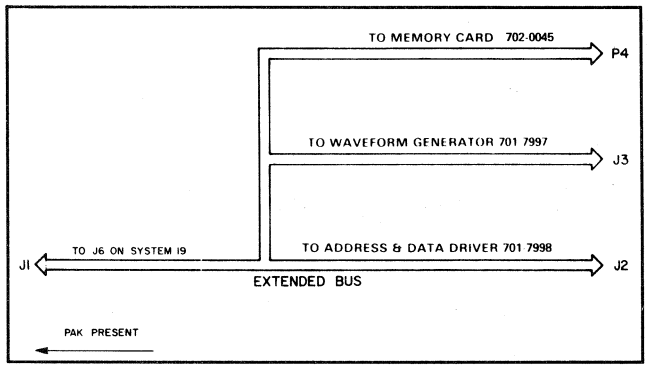


Figure 5-3. Block Diagram, UniPak™Motherboard

5.3.2 Waveform Generator

The waveform generator provides all signals, including addresses and data, required for programming devices. These signals are generated by the blocks shown in figure 5-4.

Three major supplies are the V_{CC} supply, the CE supply and the bit supply, which are used to generate the respective signals. Each supply is software-controlled via a D/A converter. All DACs obtain their reference voltage from the DAC reference.

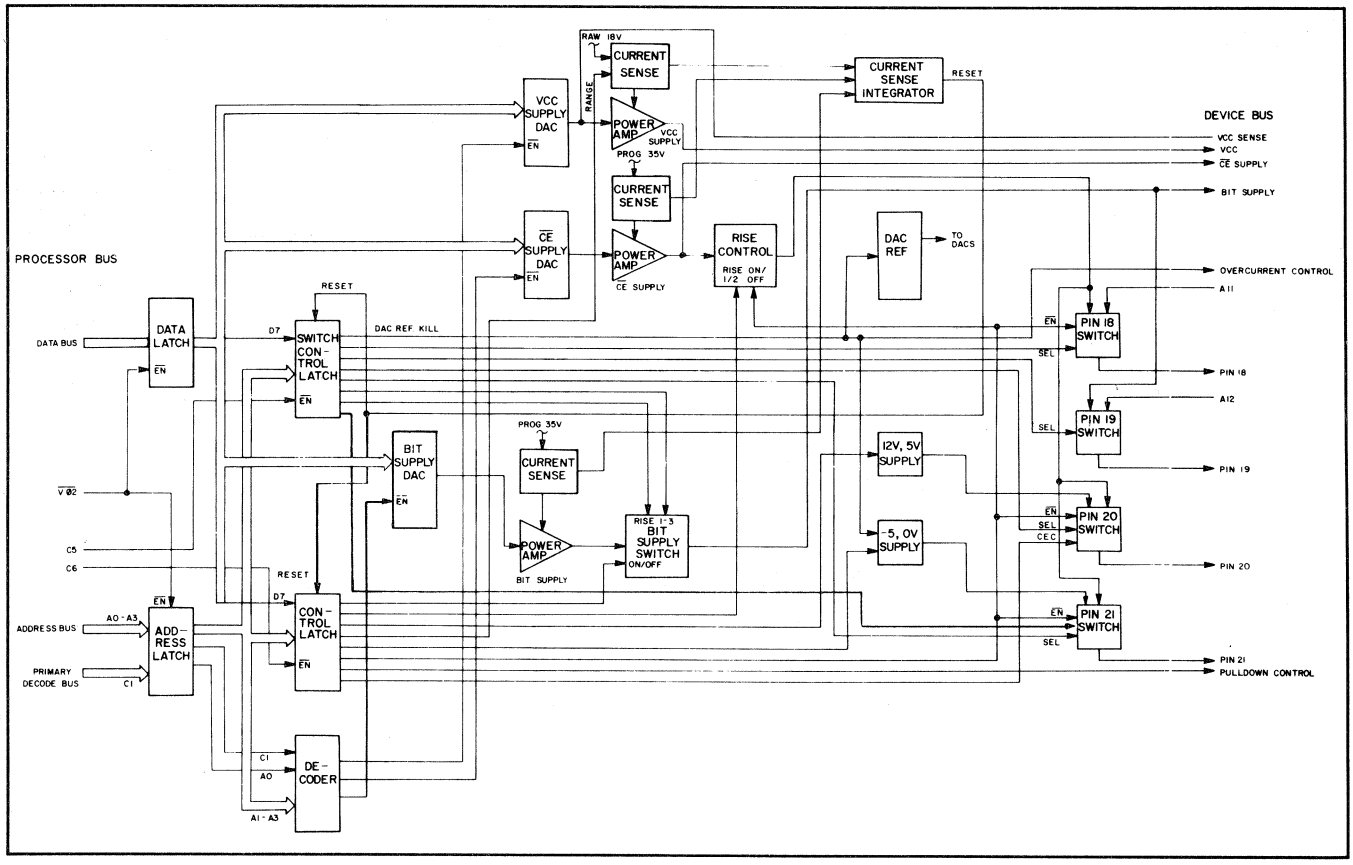


Figure 5-4. Block Diagram, Waveform Generator

The V_{CC} waveforms are generated by writing appropriate DAC values from the firmware. The rise and fall times are fixed by the slewing rate of the op amp. Two overcurrent detectors are included, one for low currents and one for high currents (above 1 amp). If a detector is activated, the control latch is reset; the DAC-reference kill output then causes the DAC reference to go to zero, in turn causing all supplies to return to zero.

The V_{CC} supply senses the V_{CC} voltage at the PROM socket via the V_{CC}-sense line. This remote sensing compensates for all cable drops between the supply and the socket.

The $\overline{CE}$ waveforms are generated by using the $\overline{CE}$ supply in conjunction with one of the pin switches. The voltage level is selected by writing the appropriate value to the $\overline{CE}$ DAC. One of two rise times is selected by the control latch and rise-time control circuitry. Either the pin 18, 20 or 21 switch can be enabled by the switch-control latch to output the high-level $\overline{CS}$ voltage. Switches that are not enabled can output TTL levels.

Each pin switch consists of an emitter follower with the collector tied to the $\overline{CE}$ supply. A current source is provided for the base of each switch to charge the common rise-time capacitor. When the base is released, a linear ramp is generated which is truncated at the $\overline{CE}$-supply level. An NPN-transistor pulldown is included in the switch to provide a 20V/μs-controlled fall time. Logic circuitry prevents the pulldown and pullup circuits from being active simultaneously.

The pin 21 switch uses the same principles as the pin 18 and pin 20 switches. However, a power amplifier output (-5V/0 supply) provides the ground reference for the switch. For certain programming algorithms this amplifier output is brought to -5V.

The pin 20 switch includes a pullup that is connected to the +12/+5V supply, thus allowing the switch in the TTL mode to switch from 0 to 12V as well as from 0 to 5V. The +12/+5V supply consists of a monolithic regulator and a 5.1V zener diode controlled by the switch-control latch.

Signals to be applied to the data lines of a device are generated with the bit-supply signals and controlled by the bit-supply switch. The bit supply is nearly identical to the $\overline{CE}$ supply, but has one less diode in the feedback path, compensating for one less drop in the switch paths. The bit-supply switch consists of an emitter follower, a current source, and three rise-time control capacitors. The collector of the emitter follower is connected to the bit supply; the base is connected to the current source and timing capacitor. The control latch can select the timing capacitor and also control the base of the switch. When the base is released, the output ramps linearly to the bit-supply level. The output on the bit-supply switch is sent to the address and data driver card and to the pin 19 switch. Unlike the pin 18, 20 and 21 switches, the pin 19 switch consists of a simple PNP-saturating switch controlled by the switch-control latch.

The current-sense integrator smoothes the transient overcurrent pulses occurring from charging supply capacitors. When an overcurrent condition from the V_{CC}, $\overline{CE}$, bit or 0/-5V supply exists for sufficient time, the control latch is reset, in turn causing the DAC reference and the supplies to go to zero. The state of the overcurrent-control line can be read by the address and data driver card and used by the programmer to detect shorted devices. Table 5-2 lists the functions of the device-bus pins. The data latch buffers the data bus and holds data to satisfy the long DAC data-hold requirement. The address latch buffers the lower-order address lines and the primary decode bus. These buffered lines are then sent to the decoder and the address latches. The decoder provides decode signals to the DACs for the V_{CC}, $\overline{CE}$ and bit supplies. The switch-control latch and the control latch receive their clocks from a decoder on the address and data driver card.

Table 5-2. Pin Functions, Device Bus (at J1)

1	PA_8	26	PA_7
2	PA_9	27	PA_6
3	PA_{10}	28	PA_5
4	PA_{11}	29	PA_4
5	PA_{12}	30	PA_3
6	PA_{13}	31	PA_2
7	PA_{14}	32	PA_1
8	PA_{15}	33	PA_0
9	GND	34	VCC
10	VCC Sense	35	GND
11	CE Supply	36	GND
12	Bit Switch	37	Bit Supply
13	Pin 20	38	Pin 18
14	Pin 21	39	Pin 19
15	Scope Trigger	40	PD_1
16	-9	41	PD_2
17	+24	42	PD_3
18	Overcurrent	43	PD_4
19	Pull Down Control	44	S1
20	VCC Pullup	45	S2
21	VREF	46	S3
22	PD_8	47	Spare
23	PD_7	48	Spare
24	PD_6	49	+5
25	PD_5	50	GND

5.3.3 Address and Data Driver

The address and data driver, diagrammed in figure 5-5, provides the device address, device data, data loads and supply measurement capability of the UniPak™.

The address drivers consist of addressable latches driving the device address bus. The addressable latches receive data from the most-significant-bit line of the data bus.

The data switch register drives PNP data switches which direct the output of the bit switch to the appropriate device-data line. The PNP switches are driven by current sources to provide a constant-base drive at all bit-switch voltages.

The data sink register drives the NPN data sinks directly. These data sinks are used to shunt to ground large programming currents. Device data is read via the data comparators and strobed to the processor bus via the data gate. The comparators receive their reference voltage from the V_{REF} amplifier, which is controlled by the V_{REF} DAC. Loading the device data bus is controlled by the load DAC, the load amplifier and the high/low-range load switch. A voltage is developed by the load amp and applied to either the high-range or low-range resistor banks. The diode clamps limit the voltage applied by the load resistors to the data bus to approximately 5V.

The supply comparators read the V_{CC}-sense line, the $\overline{CE}$ supply and the bit-switch line. The comparator gate/multiplexer strobes the data from the supply comparators and the overcurrent-read line to the most-significant-bit line of the data bus.

The socket-select latch provides a control line for the high-/low-range switch and control lines for the socket card.

The data latch buffers the data bus and holds data to satisfy the DAC requirements.

The address latch buffers low-order addresses for the secondary decoder. The decoder provides the appropriate signals for the DACs and registers as well as the latches on this card and on the waveform generator. The $\overline{V \bullet \emptyset_2}$ signal controls the timing of the various clock signals developed by the decoder.

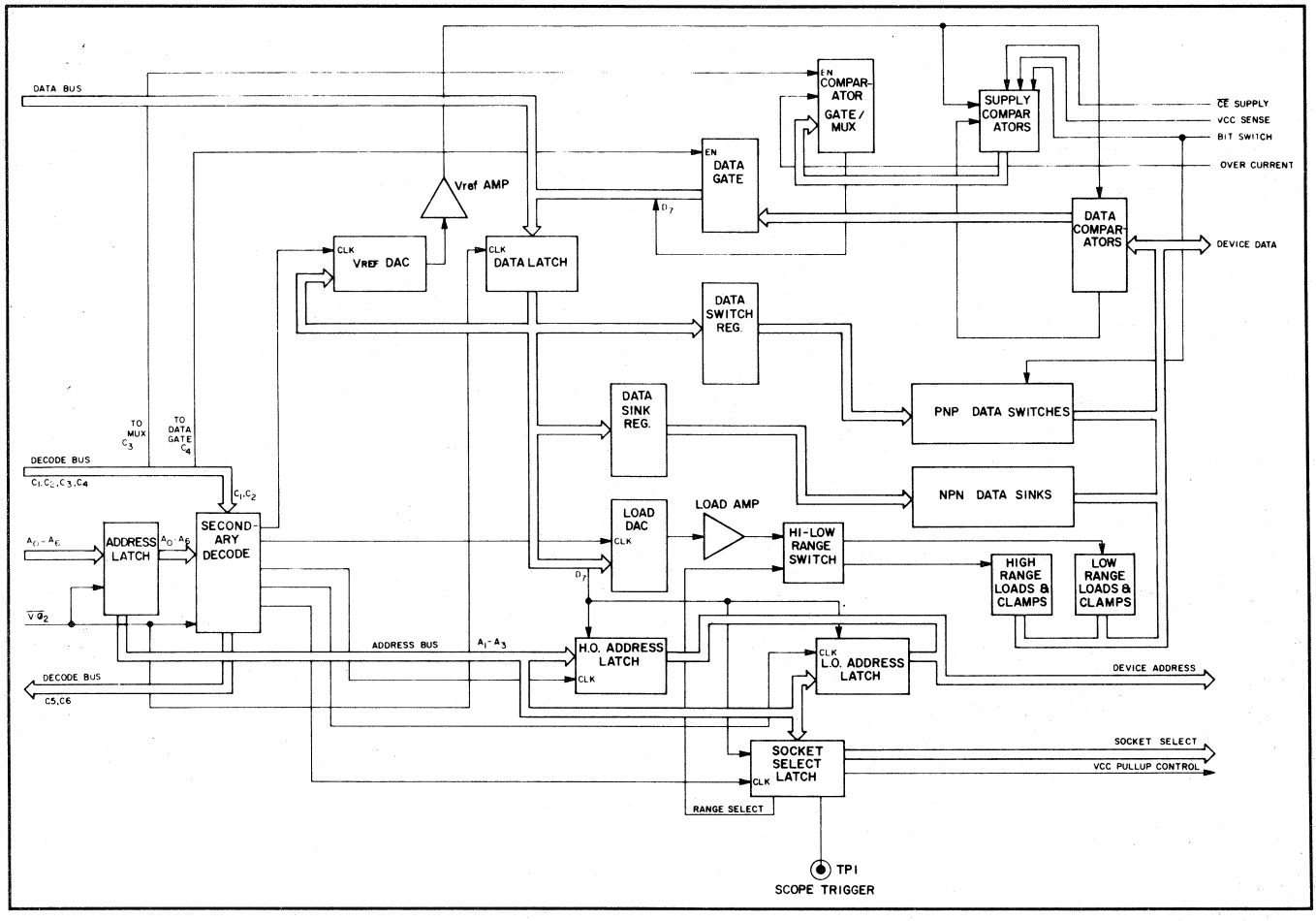


Figure 5-5. Block Diagram, Address and Data Driver Card

5.3.4 UniPak™ Socket Card

The UniPak™ socket card distributes to the device sockets the signals developed on the address and data driver card and the waveform generator. Refer to the block diagram, figure 5-6. The device address lines connect directly to the device sockets; larger devices connect to more device addresses than smaller devices; diode-overvoltage protection on these lines prevents damage to the drivers on the address and data driver card.

The device-data bus connects directly to all sockets. Four-bit devices are connected to PD_1-PD_4. The data pulldowns consist of 1K-ohm resistors and a diode network. Data-spike clamps consist of diode networks and capacitor-resistor networks. The diode networks are used to clip overshoot on the data-line programming pulses. The capacitor network is charged by the bit supply so that the network does not absorb energy from the actual data-line programming pulses.

Pins 18, 19, 20 and 21 of the 24-pin device socket receive signals directly from the waveform generator via the corresponding pin switches. A spike-suppression network similar to that used on the data lines is provided where the $\overline{CE}$ supply charges the RC network. V_{CC} is applied to all sockets through seven diodes. Remote sensing of the voltage at the selected socket is provided by the analog switch of the V_{CC}-sense multiplexer. When V_{CC} is brought to zero, the device's V_{CC} lines can be pulled up by the V_{CC} pullups. The V_{CC} sense-multiplexer and a comparator on the address and data driver card are then used to read the V_{CC} voltage. If a device is properly inserted in a socket, the V_{CC} voltage will be above 2V. If it is in backwards, it will be below 1V, and if no device is in the socket, the voltage will approach 4V.

The LED decoder is used to light the LEDs below the selected socket.

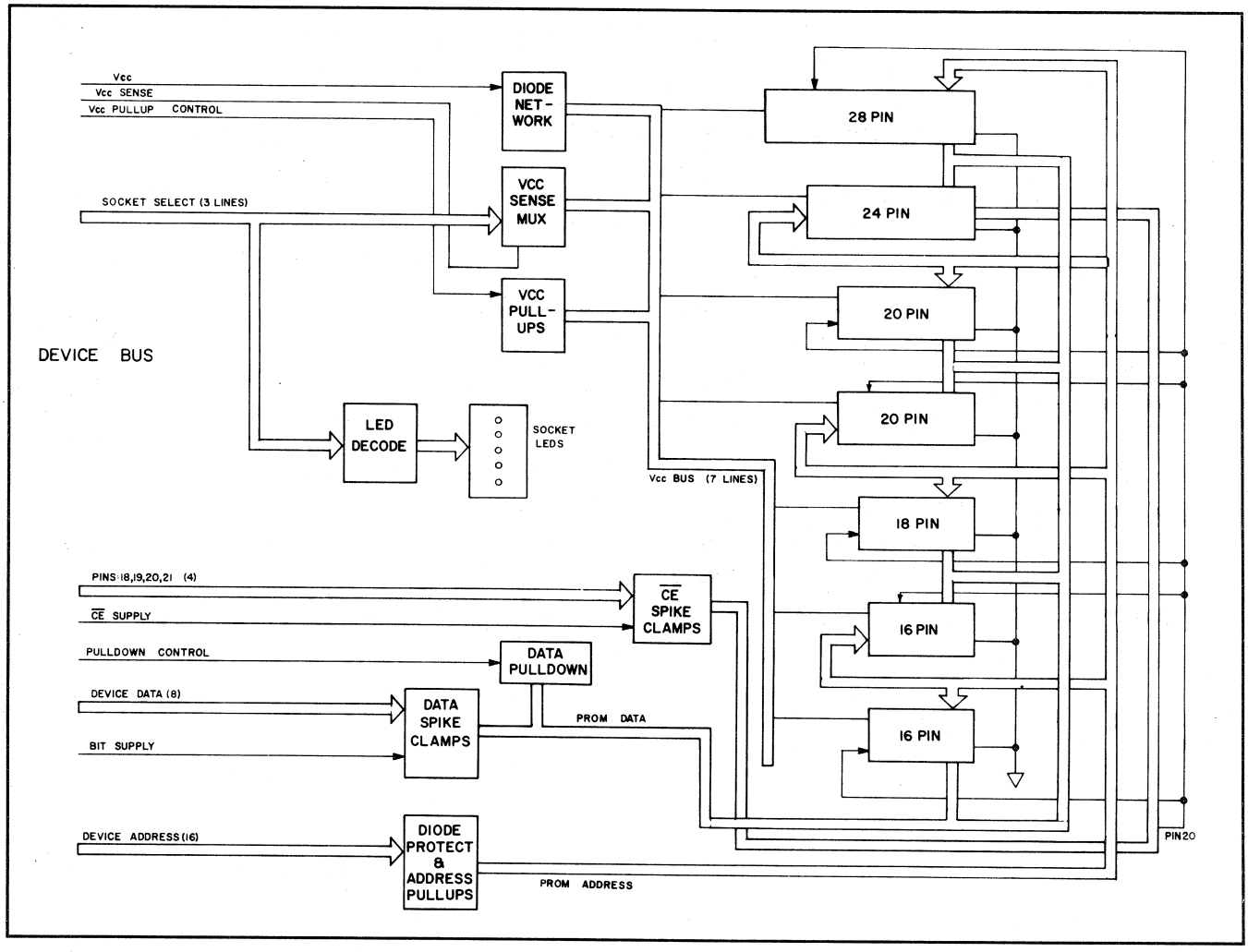


Figure 5-6. Block Diagram, UniPak™ Socket Card

5.3.5 UniPak™ Memory Card

The UniPak™ memory card is shown in block diagram form in figure 5-7. PROMs which store the UniPak™ software are contained on the memory card. These PROMs connect to the address bus directly and to the data bus through data buffers.

Two PROMs and a latch comprise the primary decoder. The PROMs connect to the 12 high-order address lines and the R/$\overline{W}$ line. Outputs from the primary-decoder latch connect to the secondary decoder and also to secondary decoders on the address and data driver card and the waveform generator. A 1-of-8 decoder, timed with $\overline{V \bullet \emptyset_2}$, provides the secondary decoding for the software PROMs. Two additional lines from this decoder connect to the address card to provide the decode signals for the data gate and comparator gate/multiplexer. Additional outputs from the primary decoder enable the data buffer during all software-read operations and lower the data-gate-enable line during any access of the UniPak™

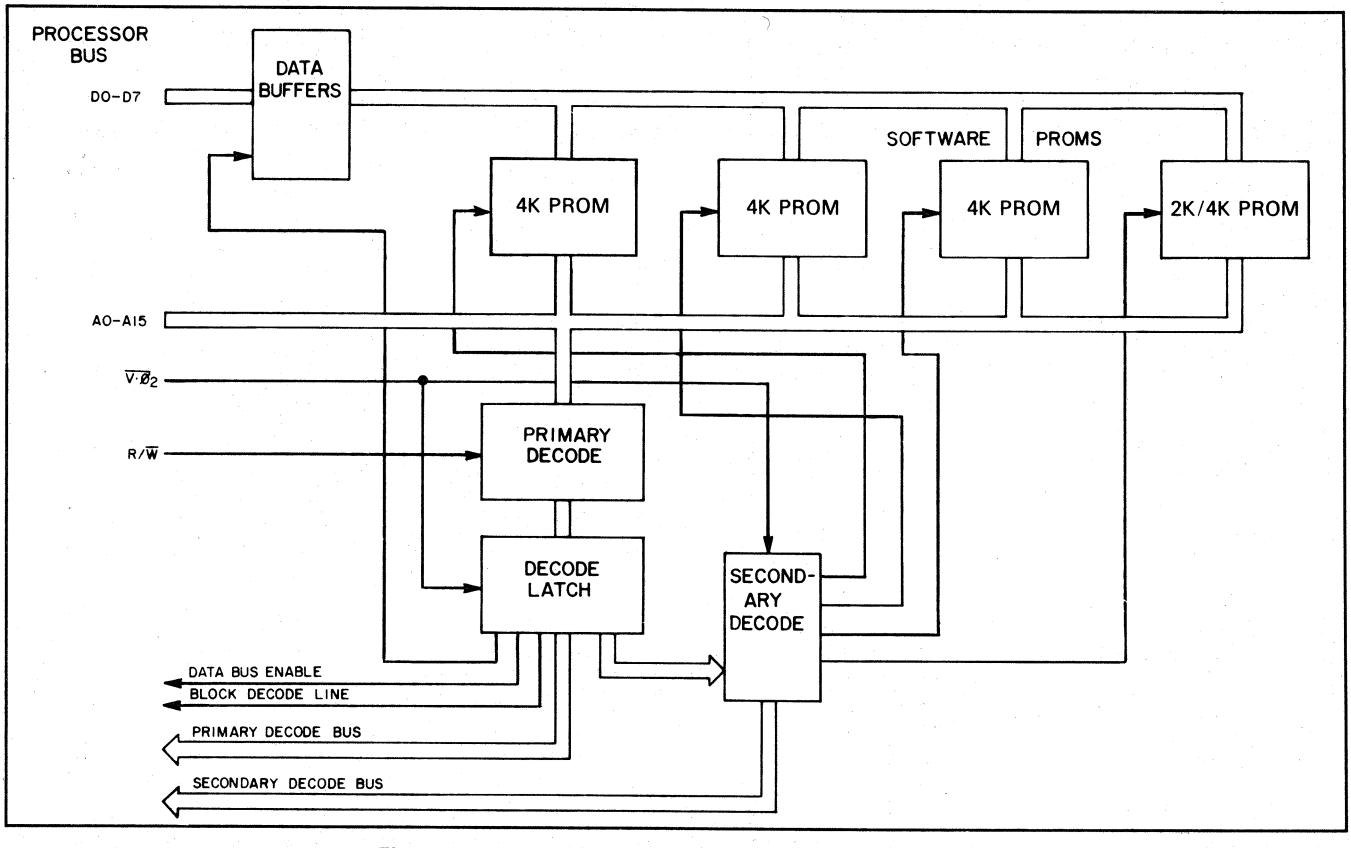


Figure 5-7. Block Diagram, UniPak™ Memory Card

APPENDIX A
ERROR CODES

CODE	NAME	DESCRIPTION
21	Illegal-Bit Error	The device cannot be programmed due to already programmed locations of incorrect polarity.
23	First-Pass Verify Error	The device data was incorrect on the first pass of the automatic verify sequence during device programming.
24	Second-Pass Verify Error	The device data was incorrect on the second pass of the automatic verify sequence during device programming.
27	Insufficient RAM	Due to the value of the Begin RAM Address, there is insufficient RAM to program the device, or the total allotment of RAM resident is less than the word limit of the device.
30	No Programming Algorithm	Valid family and pinout codes are not selected, or family code selection is not followed by pinout code selection.
31	Excessive Current Drain	The operation aborted due to excessive current drain by a device.
32	Backward Device	The operation aborted due to V_{CC} level test indicating a backward device.
35	Faulty Chip Select	The operation aborted due to data being present while a device is disabled.
37	Socketing Error	Operation aborted due to a low V_{CC} level indication on sockets presumed to be empty. A device may be in the wrong socket, or two or more devices may be socketed simultaneously.
38	Illegal Operation During Calibration	An illegal or invalid operation was attempted during calibration.
39	Failure to Lock Security Fuse	The security bit did not program and the device is not locked.
70	Faulty Bit Supply	The operation aborted due to a faulty bit supply. Do not use UniPak™ until repaired.
71	Faulty CS Supply	The operation aborted due to a faulty CS supply. Do not use UniPak™ until repaired.
72	Faulty V_{CC} Supply	The operation aborted due to a faulty V_{CC}. Do not use UniPak™ until repaired.
BØ	Byte Erase Error	The device does not have a byte erase mode. Block limits must be removed and a chip erase performed. The entire chip may then be reprogrammed.
B1	Chip Erase Error	The device does not have a chip erase mode.

*In the case of an error condition, be sure that the family and pinout codes are correct for the PROM installed; refer to the UniPak Device List to cross check family and pinout codes.

UNIPAK™ DEVICE LIST

This document comes in two parts. The first is a list of the UniPak device family and pinout codes. An explanation of each of the column headings is given below. The second is a flow chart of the KEPROM™ algorithm.

CAUTION

Be sure you enter the proper family and pinout codes for the device you want to program. If you enter an incorrect family and pinout code, you may damage your device. Be aware that although you may enter an independently valid family code and an independently valid pinout code, when combined, produce an invalid (illegal) combination. The correct combination for your device is published in this table. All family/pinout combinations not contained in this table are considered "illegal." Data I/O assumes no responsibility or liability for results produced by entry of "illegal" family/pinout combinations.

Key to Headings and Footnotes:

Device Part Number: The number assigned by the device manufacturer.

Family/Pinout Code: A 2-digit hexadecimal number that designates the programming algorithm (family) followed by a 2-digit hexadecimal number used to differentiate device types based on pin assignment and array size (pinout).

Software Version: A number in this column specifies the earliest version of the UniPak that will program the device to the manufacturer's latest specifications.

Adapter: The model number of the adapter required to program the designated device.

Approval Status: The following is an explanation of the symbols used in this column:

A Written approval obtained.

O Device is obsolete and no longer in production. No approval can be obtained. Algorithm has been used and approved in previous Data I/O equipment.

S This algorithm is in the process of submittal for manufacturer approval. The algorithm has been tested by Data I/O or the manufacturer, but no representation as to yield level is made or implied.

* Devices marked with this symbol following the approval status symbol has extra programmable locations beyond the main array. Data is entered sequentially in RAM above the main array data. Consult the manufacturer's specifications for specific information.

\# Devices marked with this symbol following the approval status symbol have security bits. Use SELECT CODE C3 to set the programming flow for the security bits.

\+ Devices marked with this symbol following the approval status symbol are KEPROMs. Refer to the KEPROM flow chart.

† Devices marked with this symbol following the approval status sybol cannot be programmed using the System 19 or the Model 100A.

\## Devices marked with this symbol following the approval status symbol can only be programmed by the 29B with V04 or later.

** Devices marked with this symbol following the approval status symbol have extra programmable locations beyond the main array. Data is entered sequentially in RAM above the main array data. Consult the manufacturer's specifications for specific information. To program asynchronous to synchronous, put 01 at second RAM location above the main array. Enter 00 to not program asynchronous to synchronous.

KEPROM™ is a trademark of the Intel Corporation.

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
		Advanced Micro Devices			
2708	21	27	A	None	A
27128	AF	51	005	None	A
27128A	C1	51	V08	None	S
2716	19	23	A	None	A
2716B	C2	23	V11	None	S
27256	C1	32	V08	None	S
2732	19	24	A	None	A
2732A	27	24	005	None	A
2732B	C2	24	V11	None	S
27512	DD	A4	V08	None	S†
2764 8KX8	AF	33	005	None	A
2764A	C1	33	V08	None	S
27LS18	16	02	K	None	S
27LS184	16	06	E	None	S
27LS185	16	06	E	None	S
27LS19	16	02	K	None	A
27PS181	16	37	K	None	A
27PS184	16	06	A	None	A
27PS185	16	06	K	None	A
27PS191	16	68	K	None	A
27PS281	16	37	003	None	S
27PS291	16	68	003	None	A
27PS41	16	53	005	351A-065	S
27PS43	16	63	004	None	A
27S08	15	02	A	None	O
27S09	15	02	A	None	O
27S10	15	01	A	None	O
27S11	15	01	A	None	O
27S12	16	03	A	None	A
27S13	16	03	A	None	A
27S15	16	79	005	351A-068	A
27S18	16	02	A	None	A
27S180	16	37	A	None	A
27S181	16	37	A	None	A
27S184	16	06	E	None	A
27S185	16	06	E	None	A
27S19	16	02	A	None	A
27S190	16	68	H	None	A
27S191	16	68	H	None	A
27S20	16	01	A	None	A
27S21	16	01	A	None	A
27S25	16	65	003	None	S
27S26	16	85	005	351A-067	A
27S27	16	85	005	351A-067	A
27S28	16	09	E	None	A
27S280	16	37	003	None	A
27S281	16	37	003	None	A
27S29	16	09	E	None	A

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Advanced Micro Devices (Continued)		
27S290	16	68	003	None	A
27S291	16	68	003	None	A
27S30	16	36	A	None	A
27S31	16	36	A	None	A
27S32	16	38	E	None	A
27S33	16	38	E	None	A
27S35	16	66	004	None	S
27S37	16	66	004	None	S
27S40	16	53	004	351A-065	A
27S41	16	53	004	351A-065	A
27S43	16	63	004	None	A
27S45	16	77	005	351A-066	A**
27S47	16	77	005	351A-066	A**
27S49	16	67	003	None	S
27S65	16	93	V08	351A-073	S*
27S75	16	94	V08	351A-073	S*
27S85	16	95	V08	351A-073	S*
2817A	BF	A2	V11	None	S
2864B	CA	A6	V11	None	S
29750A	16	02	A	None	O
29751A	16	02	A	None	O
29760A	16	01	A	None	O
29761A	16	01	A	None	O
29770	16	03	A	None	O
29771	16	03	A	None	O
29774	16	85	005	351A-067	S
29775	16	85	005	351A-067	O
8751H	54	58	V10	351A-071	S#
9864	C9	A6	V08	None	S
AM9708	21	27	A	None	A
AM9716	19	23	A	None	A
AM9732	19	24	A	None	A
AM9761	54	6A	V10	351A-071	S#
AM9764	AF	33	005	None	A
			ATMEL		
27256	93	32	V11	None	S
27C128	93	51	V11	None	S
27C256	93	32	V11	None	S
27C512	4B	A4	V11	None	S†
27C513	5B	5E	V11	None	S†
27C515	5B	CA	V11	None	S†
27C64	93	33	V11	None	S
27HC256	93	32	V11	None	S

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			ATMEL (Continued)		
27HC64	93	33	V11	None	S
27HC641	90	67	V11	None	S
27HC641L	90	67	V11	None	S
27HC642	90	67	V11	None	S
27HC642L	90	67	V11	None	S
28C04	C4	82	V11	None	S
28C16	C4	96	V11	None	S
28C17	C4	A2	V11	None	S
28C64	C4	98	V11	None	S
28HC16	C4	96	V11	None	S
28HC17	C4	A2	V11	None	S
			Electronic Arrays		
2708	21	27	A	None	O
2716	19	23	A	None	O
			Eurotechnique		
ET2716	19	23	005	None	A
ET2732	19	24	005	None	A
ET2764	35	33	V08	None	A
ETC2716	19	23	005	None	A
ETC2732	27	24	V11	None	S
			Exel Microelectronics Inc.		
2816A	B7	23	V08	None	S
2864A	C3	98	V10	None	S
2865A	C3	98	V11	None	S
46C15	CD	21	V11	None	S
46C16	CD	21	V10	None	S
			Fairchild		
2708	21	27	A	None	O
93417	01	01	A	None	A
93427	01	01	A	None	A
93436	01	03	A	None	A
93438	01	15	A	None	A
93446	01	03	A	None	A
93448	01	15	A	None	A
93450	01	16	A	None	A
93451	01	16	A	None	A
93452	01	05	A	None	A
93453	01	05	A	None	A
93510	01	21	004	None	A
93511	01	21	004	None	A
93L450	01	16	A	None	S
93L451	01	16	A	None	S

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Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Fujitsu		
27128	45	51	005	None	S
27128A	93	51	V11	None	S
27256	93	32	V11	None	S
2732	19	24	E	None	A
2732A	27	24	F	None	A
2764	45	33	005	None	S
27C128	45	51	005	None	S
27C256	45	32	V08	None	S
27C256A	93	32	V11	None	S
27C256H	93	32	V11	None	S
27C32A	27	24	A	None	S
27C512	4B	A4	V11	None	S†
27C64	45	33	005	None	S
28C64	C3	98	V11	None	S
28C65	C3	98	V11	None	S
8516	19	23	E	None	A
8518	21	27	E	None	A
8532	19	24	E	None	A
8742	50	57	005	351A-070	S
8749H	50	57	005	351A-070	S
			General Instruments		
27256	93	32	V11	None	S
27C128	93	51	V11	None	S
27C256	93	32	V11	None	S
27C512	4B	A4	V11	None	S†
27C513	5B	5E	V11	None	S†
27C515	5B	CA	V11	None	S†
27C64	93	33	V11	None	S
27HC64	93	33	V11	None	S
27HC641	90	67	V11	None	S
28C04	C4	82	V11	None	S
28C16	C4	96	V11	None	S
28C17	C4	A2	V11	None	S
28C64	C4	98	V11	None	S
28CP64	C4	98	V11	None	S
28HC16	C4	96	V11	None	S
28HC17	C4	A2	V11	None	S
5716	83	23	003	None	A
5816	37	23	003	None	S
			Harris		
6641	40	47	F	None	A@
7602	06	02	V10	None	S
7603	06	02	V10	None	S
7608	05	16	A	None	O

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Harris (Continued)		
7610	06	01	V10	None	S
7611	06	01	V10	None	S
7616	05	42	A	None	O
76160	05	21	A	None	O
76161	06	21	V10	None	S
76165	06	53	V10	351A-065	S
7620	06	03	V10	None	S
7621	06	03	V10	None	S
7629	05	43	A	None	O
76320	05	63	H	None	O
76321	06	63	V10	None	S
7640	06	15	V10	None	S
7641	06	15	V10	None	S
7642	06	05	V10	None	S
7642P	05	38	H	None	O
7643	06	05	V10	None	S
7643P	05	38	H	None	O
7644	05	04	A	None	O
7647R	05	79	V08	351A-068	S
7648	05	09	A	None	O
7649	06	09	V10	None	S
76641	06	67	V10	None	S
7680	05	16	A	None	O
7680RP	05	16	H	None	O
7681	06	16	V10	None	S
7681RP	05	16	H	None	O
7684	05	06	A	None	O
7684P	05	06	H	None	O
7685	06	06	V10	None	S
7685P	05	06	H	None	O
7686	05	10	A	None	A
7687	05	10	001	None	O
			Hitachi		
27256	93	32	V10	None	S
27512	4B	A4	V11	None	S†
27C256	93	32	V11	None	S
27C64	79	33	V10	None	A
462532	19	25	F	None	A
462716	19	23	F	None	A
462732	19	24	F	None	A
462732P	19	24	A	None	S
48016	33	23	V09	None	S
4827128	79	51	004	None	A
4827128P	79	51	V10	None	S
482732A	27	24	A	None	A
482764	79	33	004	None	A
58064	D7	98	V10	None	S

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Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Hughes		
3004-1	58	62	004	None	A
3004-2	58	61	004	None	A
3008	58	60	004	None	A
3104-1	58	62	004	None	S
3104-2	58	61	004	None	S
3108	58	60	004	None	S
			Intel		
2704	21	26	A	None	O
2708	21	27	A	None	O
27128	79	51	004	None	A
27128A	93	51	005	None	A
2716	19	23	A	None	A
27256	93	32	005	None	A
2732	19	24	A	None	A
2732A	27	24	A	None	A
2732B	93	24	V10	None	S
27512	4B	A4	V09	None	S†
27513	5B	5E	V10	None	S†
2758	19	22	A	None	O
2764	79	33	004	None	A
2764A	93	33	005	None	A
27C256	93	32	V10	None	A
27C64	93	33	V09	None	S
2815	85	23	005	None	S
2816	37	23	H	None	S
2816A	A5	96	V08	None	S
2817A	BF	A2	V08	None	S
2864A	CC	98	V11	None	S
8704	21	26	A	None	O
8708	21	27	A	None	O
8741	56	59	005	351A-070	S
8741A	56	59	005	351A-070	S
8742	50	57	005	351A-070	S
8744	53	58	005	351A-071	S
8748	52	56	005	351A-070	S
8748H	50	56	005	351A-070	S
8749H	50	57	005	351A-070	S
8751	53	58	005	351A-071	S
8751H	D5	58	V08	351A-071	S#
8755A	47	55	005	351A-072	S
87C256	5C	C8	V11	None	S
87C64	93	3A	V10	None	S
P27128A	5C	51	V11	None	S
P27256	5C	32	V11	None	S
P2732A	4D	24	V11	None	S
P27512	5E	A4	V11	None	S†
P2764A	5C	33	V11	None	S

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Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Intersil		
6716	59	64	004	None	A
			Mitsubishi		
2708	21	27	A	None	S
27128	79	51	004	None	S
2716	19	23	A	None	A
2732	19	24	A	None	A
2764	79	33	004	None	S
8748	52	56	V08	351A-070	S
			Monolithic Memories		
5300	11	01	D	None	A
	E5	01	V09	None	S
5301	11	01	D	None	A
	E5	01	V09	None	S
5305	11	03	D	None	A
	E5	03	V09	None	S
5306	11	03	D	None	A
	E5	03	V09	None	S
5308	11	08	D	None	A
	D1	08	V08	None	A
5309	11	08	D	None	A
	D1	08	V08	None	A
5330	29	02	A	None	A
	E7	02	V09	None	S
5331	29	02	A	None	A
	E7	02	V09	None	S
5335	11	14	D	None	A
	D1	14	V08	None	A
5336	11	14	D	None	A
	D1	14	V08	None	A
5340	11	15	D	None	A
	D1	15	V08	None	A
5340JS	11	15	D	None	S
	D1	15	V08	None	A
5341	11	15	D	None	A
	D1	15	V08	None	A
5341JS	11	15	D	None	S
	D1	15	V08	None	A
5348	11	09	D	None	A
	D1	09	V08	None	A
5349	11	09	D	None	A
	D1	09	V08	None	A

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Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
		Monolithic Memories (Continued)			
5352	11	05	D	None	A
	D1	05	V08	None	A
5353	11	05	D	None	A
	D1	05	V08	None	A
5380	11	16	D	None	A
	D1	16	V08	None	A
5380JS	11	16	D	None	S
	D1	16	V08	None	A
5381	11	16	D	None	A
	D1	16	V08	None	A
5381JS	11	16	D	None	S
	D1	16	V08	None	A
5388	11	06	D	None	A
	D1	06	V08	None	A
5389	11	06	D	None	A
	D1	06	V08	None	A
53D1641	B2	80	V08	351A-073	S
53DA1643	AA	87	V08	351A-073	S*
53DA441	AA	AC	V08	351A-073	S*
53DA442	AA	AC	V08	351A-073	S*
53DA841	AA	AD	V08	351A-073	S*
53LS140	18	01	004	None	S
53LS141	18	01	004	None	S
53LS1681	18	21	V11	None	S
53LS240	18	03	004	None	S
53LS241	18	03	004	None	S
53LS441	18	05	004	None	S
53PL1681	18	21	V08	None	S
53PS1681	18	21	005	None	S
53RA1681	18	A3	V08	None	S*
53RA441	18	07	004	None	S
53RA481	EC	65	V09	None	S
53RS1681	18	A3	V08	None	S*
53RS881	18	86	005	None	A*
53S080	18	02	004	None	O
53S081	18	02	004	None	O
53S140	18	01	004	None	S
53S141	18	01	004	None	S
53S1641	18	53	004	351A-065	A
53S1681	18	21	004	None	S
53S1681J	18	21	004	None	S
53S240	18	03	004	None	S
53S241	18	03	004	None	S
53S280	18	08	004	None	S
53S281	18	08	004	None	S
53S285	18	14	V11	None	S
53S3281	18	63	004	None	A
53S440	18	05	004	None	S

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Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
		Monolithic Memories (Continued)			
53S441	18	05	004	None	S
53S480	18	09	004	None	S
53S481	18	09	004	None	S
53S485	18	15	V11	None	S
53S6481	18	67	V10	None	S
53S840	18	06	004	None	S
53S841	18	06	004	None	S
53S880	18	16	V11	None	S
53S881	18	16	003	None	S
6300	11	01	D	None	A
	E5	01	V09	None	S
6301	11	01	D	None	A
	E5	01	V09	None	S
6305	11	03	D	None	A
	E5	03	V09	None	S
6306	11	03	D	None	A
	E5	03	V09	None	S
6308	11	08	D	None	A
	D1	08	V08	None	A
6309	11	08	D	None	A
	D1	08	V08	None	A
6330	29	02	A	None	A
	E7	02	V09	None	S
6331	29	02	A	None	A
	E7	02	V09	None	S
6335	11	14	D	None	A
	D1	14	V08	None	A
6336	11	14	D	None	A
	D1	14	V08	None	A
6340	11	15	D	None	A
	D1	15	V08	None	A
6340JS	11	15	D	None	S
	D1	15	V08	None	A
6341	11	15	D	None	A
	D1	15	V08	None	A
6341JS	11	15	D	None	S
	D1	15	V08	None	A
6348	11	09	D	None	A
	D1	09	V08	None	A
6349	11	09	D	None	A
	D1	09	V08	None	A
6352	11	05	D	None	A
	D1	05	V08	None	A
6353	11	05	D	None	A
	D1	05	V08	None	A
6380	11	16	D	None	A
	D1	16	V08	None	A
6380JS	11	16	D	None	S
	D1	16	V08	None	A

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Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
		Monolithic Memories (Continued)			
6381	11	16	D	None	A
	D1	16	V08	None	A
6381JS	11	16	D	None	S
	D1	16	V08	None	A
6388	11	06	D	None	A
	D1	06	V08	None	A
6389	11	06	D	None	A
	D1	06	V08	None	A
63D1641	B2	80	V08	351A-073	S
63D1642	B2	80	V08	351A-073	A
63DA1643	AA	87	V08	351A-073	S*
63DA441	AA	AC	V08	351A-073	S*
63DA442	AA	AC	V08	351A-073	S*
63DA841	AA	AD	V08	351A-073	S*
63LS140	18	01	004	None	S
63LS141	18	01	004	None	S
63LS1681	18	21	V11	None	S
63LS240	18	03	004	None	S
63LS241	18	03	004	None	S
63LS441	18	05	004	None	S
63PL1681	18	21	V08	None	S
63PS1681	18	21	005	None	S
63RA1681	18	A3	V08	None	S*
63RA441	18	07	004	None	S
63RA481	EC	65	V09	None	S
63RS1681	18	A3	V08	None	S*
63RS881	18	86	005	None	A*
63S080	18	02	004	None	A
63S081	18	02	004	None	A
63S140	18	01	004	None	S
63S141	18	01	004	None	S
63S1641	18	53	004	351A-065	A
63S1681	18	21	004	None	S
63S1681J	18	21	004	None	S
63S240	18	03	004	None	S
63S241	18	03	004	None	S
63S280	18	08	004	None	S
63S281	18	08	004	None	S
63S285	18	14	V11	None	S
63S3281	18	63	004	None	A
63S440	18	05	004	None	S
63S441	18	05	004	None	S
63S480	18	09	004	None	S
63S481	18	09	004	None	S
63S485	18	15	V11	None	S
63S6481	18	67	V10	None	S
63S840	18	06	004	None	S
63S841	18	06	004	None	S

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Monolithic Memories (Continued)		
63S880	18	16	V11	None	S
63S881	18	16	004	None	S
			Monolithic Memories (PLE)		
PLE5P8AC	18	02	V09	None	S
PLE5P8AM	18	02	V09	None	S
PLE5P8C	18	02	V09	None	S
PLE5P8M	18	02	V09	None	S
PLE8P4C	18	01	V09	None	S
PLE8P4M	18	01	V09	None	S
PLE8P8C	18	08	V09	None	S
PLE8P8M	18	08	V09	None	S
PLE9P4C	18	03	V09	None	S
PLE9P4M	18	03	V09	None	S
PLE9P8C	18	09	V09	None	S
PLE9P8M	18	09	V09	None	S
PLE9R8C	EC	65	V09	None	S
PLE9R8M	EC	65	V09	None	S
PLE10P4C	18	05	V09	None	S
PLE10P4M	18	05	V09	None	S
PLE10P8C	18	16	V09	None	S
PLE10P8M	18	16	V09	None	S
PLE10R8C	18	86	V09	None	S
PLE10R8M	18	86	V09	None	S
PLE11P4C	18	06	V09	None	S
PLE11P4M	18	06	V09	None	S
PLE11P8C	18	21	V09	None	S
PLE11P8M	18	21	V09	None	S
PLE11RA8C	18	A3	V09	None	S
PLE11RA8M	18	A3	V09	None	S
PLE11RS8C	18	A3	V09	None	S
PLE11RS8M	18	A3	V09	None	S
PLE12P4C	18	53	V09	351A-065	S
PLE12P4M	18	53	V09	351A-065	S
PLE12P8C	18	63	V09	None	S
PLE12P8M	18	63	V09	None	S
			Mostek		
2716	19	23	A	None	O
			Motorola		
67256C	49	32	V11	None	S
67259	49	32	V11	None	S
6836E16	2D	5A	V09	None	S
68732-0	25	44	A	None	O
68732-1	25	45	A	None	O
68769	25	29	V11	None	S
76161	05	21	A	None	S

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Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Motorola (Continued)		
76165	05	53	003	351A-065	S
7620	05	03	A	None	O
7621	05	03	A	None	S
7640	05	15	A	None	O
7641	05	15	A	None	S
7642	05	05	A	None	O
7643	05	05	A	None	S
7649	05	09	A	None	S
7680	05	16	A	None	O
7681	05	16	A	None	S
7684	05	06	A	None	O
7685	05	06	A	None	S
MCM2532	19	25	B	None	S
MCM2708P	21	27	A	None	O
MCM2716	19	23	B	None	S
MCM2808	81	72	003	None	S
MCM2816	43	23	003	None	S
MCM2817	81	71	003	None	S
MCM2832	81	70	003	None	S
MCM68708	21	27	A	None	A
MCM68764	25	29	V11	None	S
MCM68766	25	29	V11	None	S
TMS2716	23	28	A	None	O
			National Semiconductor		
2532	19	25	A	None	A
2708	21	27	A	None	A
2716	19	23	A	None	A
2732	19	24	A	None	A
2758A	19	22	A	None	A
2758B	19	35	A	None	A
27C128	5D	51	V10	None	S
27C16	19	23	E	None	A
27C16H	BD	23	V08	None	S
27C256	5D	32	V10	None	S
27C32	19	24	A	None	A
27C32B	5D	24	V11	None	S
27C32H	BD	24	V08	None	S
27C512	4C	A4	V10	None	S†
27C58A	19	22	A	None	S
27C58B	19	35	A	None	S
27C64	5D	33	V10	None	S
27CP128	5D	BB	V10	None	S
27CP256	4C	1E	V11	None	S
27CP64	5D	1D	V11	None	S

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
		National Semiconductor (Continued)			
2816	37	23	003	None	A
2864	C7	A5	V09	None	S
54LS471	08	08	A	None	O
54S188	08	02	A	None	O
54S287	08	01	A	None	O
54S288	08	02	A	None	O
54S387	08	01	A	None	O
54S471	08	08	K	None	O
54S472	08	09	A	None	O
54S473	08	09	A	None	O
54S474	08	15	A	None	A
54S475	08	15	A	None	A
54S570	08	03	A	None	O
54S571	08	03	A	None	O
54S572	08	05	A	None	O
54S573	08	05	A	None	O
54S574	08	34	A	None	O
74LS471	08	08	A	None	S
74S188	08	02	A	None	A
74S287	08	01	A	None	A
74S288	08	02	A	None	A
74S387	08	01	A	None	A
74S471	08	08	K	None	A
74S472	08	09	A	None	A
74S473	08	09	A	None	A
74S474	08	15	A	None	A
74S475	08	15	A	None	A
74S570	08	03	A	None	A
74S571	08	03	A	None	A
74S572	08	05	A	None	A
74S573	08	05	A	None	A
74S574	08	34	A	None	O
77LS181	08	16	A	None	A
77S180	08	16	A	None	A
77S181	08	16	A	None	A
77S184	08	06	A	None	A
77S185	08	06	A	None	A
77S190	08	21	A	None	A
77S191	08	21	A	None	A
77S195	08	53	004	351A-065	S
77S280	08	16	003	None	A
77S281	08	16	003	None	A
77S290	08	21	003	None	A
77S291	08	21	003	None	A
77S295	08	15	A	None	A
77S296	08	15	A	None	A
77S321	08	63	005	None	A
77SR181	08	66	V08	None	S*
77SR193	08	77	V11	365A-066	S

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			National Semiconductor (Continued)		
77SR25	08	65	V08	None	S*
77SR27	08	85	V09	351A-067	S
77SR474	08	81	V08	None	S*
77SR476	08	81	V10	None	S*
77X288	08	02	V10	None	S
87LS181	08	16	A	None	S
87S180	08	16	A	None	A
87S181	08	16	A	None	A
87S184	08	06	A	None	A
87S185	08	06	A	None	A
87S190	08	21	A	None	A
87S191	08	21	A	None	A
87S195	08	53	004	351A-065	S
87S280	08	16	003	None	A
87S281	08	16	003	None	A
87S290	08	21	003	None	A
87S291	08	21	003	None	A
87S295	08	15	A	None	A
87S296	08	15	A	None	A
87S321	08	63	005	None	A
87SR181	08	66	V08	None	S*
87SR193	08	77	V11	351A-066	S
87SR25	08	65	V08	None	S*
87SR27	08	85	V09	351A-067	S
87SR474	08	81	V08	None	S*
87SR476	08	81	V10	None	S*
87X288	08	02	V10	None	S
9716	B3	23	005	None	A
9816A	C3	96	V10	None	S
9817	BF	A2	V10	None	S
9817A	BF	A2	V10	None	S
98C64	9F	A7	V10	None	S
			Nippon Electric Company, Ltd.		
27128	79	51	004	None	S
2716	19	23	F	None	A
27256AD	48	32	V11	None	S
27256D	45	32	V11	None	S
2732	19	24	F	None	A
2732A	27	24	A	None	S
2764	79	33	004	None	A
27C64D	79	33	V11	None	S
27C256D	45	32	V11	None	A
8741AD	56	59	005	351A-070	S
8748	52	56	005	351A-070	S
8748AD	52	56	005	351A-070	S
8748H	50	56	005	351A-070	S
8749H	50	57	005	351A-070	S
8755A	47	55	005	351A-072	S

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Oki		
2708	21	27	A	None	A
27128	79	51	004	None	S
2716	19	23	A	None	A
2758	19	22	A	None	A
2764	79	33	004	None	S
8755A	47	55	005	351A-072	S
			Raytheon		
29600	11	08	D	None	A
29601	11	08	D	None	A
29602	11	08	D	None	A
29603	11	08	D	None	A
29610	11	03	D	None	A
29611	11	03	D	None	A
29612	11	03	D	None	A
29613	11	03	D	None	A
29620	11	09	D	None	A
29621	11	09	D	None	A
29622	11	09	D	None	A
29623	11	09	D	None	A
29624	11	15	D	None	A
29625	11	15	D	None	A
29626	11	15	D	None	A
29627	11	15	D	None	A
29630	11	16	D	None	A
29630SM	11	16	003	None	S
29631	11	16	D	None	A
29631SM	11	16	003	None	A
29632	11	16	D	None	A
29632SM	11	16	003	None	S
29633	11	16	D	None	A
29633SM	11	16	003	None	S
29634	11	16	D	None	A
29635	11	16	D	None	A
29636	11	16	D	None	A
29637	11	16	D	None	A
29640	11	53	004	351A-065	S
29641	11	53	004	351A-065	S
29642	11	53	004	351A-065	S
29643	11	53	004	351A-065	S
29650	11	06	D	None	A
29651	11	06	D	None	A
29652	11	06	D	None	A
29653	11	06	D	None	A
29660	11	01	D	None	A
29661	11	01	D	None	A
29662	11	01	D	None	A
29663	11	01	D	None	A

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Raytheon (Continued)		
29671	11	63	H	None	A
29673	11	63	H	None	A
29680	11	21	D	None	A
29680SM	11	21	003	None	S
29681	11	21	D	None	A
29681SM	11	21	003	None	S
29682	11	21	D	None	A
29682SM	11	21	003	None	S
29683	11	21	D	None	A
29683SM	11	21	003	None	S
29VP816	7A	68	V10	None	S
29VP832	7A	63	V10	None	S
29VP864	7A	67	V10	None	S
29VS816	7A	68	V10	None	S
29VS832	7A	63	V10	None	S
29VS864	7A	67	V10	None	S
39VP816	7A	68	V10	None	S
39VP832	7A	63	V10	None	S
39VP864	7A	67	V10	None	S
39VS816	7A	68	V10	None	S
39VS832	7A	63	V10	None	S
39VS864	7A	67	V10	None	S
			Ricoh		
27C256	93	32	V11	None	S
27C32	27	24	V11	None	S
27C64	79	33	V11	None	S
687C64	D9	29	V11	None	S
RD5H32	27	24	F	None	A
			Rockwell		
87C64	79	33	V10	None	S
			Samsung		
2816A	B7	23	V11	None	S
2864A	C3	98	V11	None	S
2865A	39	A6	V11	None	S
2817A	BF	A2	V11	None	S
2865AH	C9	A6	V11	None	S
			Seeq		
27128	79	51	005	None	S
2764	79	33	005	None	A
27C256	93	32	V08	None	S
2816A	B7	23	V08	None	S

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Seeq (Continued)		
2816AH	DF	23	V09	None	S
2817A	BF	A2	V08	None	S
2817AH	BF	A2	V08	None	S
2864H	C9	A6	V11	None	S
5133	79	33	005	None	A
5133H	79	33	005	None	A
5143	79	51	005	None	S
5213	A5	96	V08	None	S
5213H	B9	96	V08	None	S
52B13	A5	96	V08	None	S
52B13H	B9	96	V08	None	S
52B23	AB	97	V08	None	S
52B23H	F1	97	V09	None	S
52B33	AB	98	V08	None	S
52B33H	F1	98	V09	None	S
5516A	B7	23	V08	None	S
5516AH	DF	23	V09	None	S
5517A	BF	A2	V08	None	S
5517AH	BF	A2	V08	None	S
			SGS Technology		
2532	19	25	003	None	A
2716	19	23	003	None	A
27256	93	32	V11	None	S
2764	79	33	V08	None	S
2764A	93	33	V11	None	S
			Sharp		
LH764J	1D	33	V11	None	S
			Signetics		
2708	21	27	A	None	O
27C64	93	33	V08	None	S
27C256	93	32	V11	None	S
27C64A	93	33	V11	None	S
82123	10	02	V09	None	S
82LS135	10	08	A	None	S
82LS137	10	05	A	None	S
82LS180	10	16	A	None	A
82LS181	10	16	003	None	S
82PS180	10	16	V09	None	A
82PS181	10	16	003	None	S
82S114	AE	84	V10	351A-068	S
82S115	AE	83	V10	351A-068	S
82S123	10	02	A	None	A
82S126	10	01	A	None	A
82S129	10	01	A	None	A

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Signetics (Continued)		
82S130	10	03	A	None	A
82S131	10	03	V09	None	S
82S135	10	08	A	None	S
82S136	10	05	A	None	A
82S137	10	05	A	None	A
82S137A	0F	05	V11	None	S
82S140	10	15	A	None	A
82S141	10	15	A	None	A
82S146	10	09	A	None	A
82S147	10	09	A	None	A
82S180	10	16	A	None	A
82S181	10	16	A	None	A
82S182	10	16	A	None	A
82S183	10	16	A	None	A
82S184	10	06	A	None	A
82S185	10	06	A	None	A
82S190	10	21	A	None	A
82S191	10	21	A	None	A
82S195	10	53	004	351A-065	A
82S23	10	02	A	None	A
82S2708	10	16	A	None	A
82S321	10	63	004	None	A
			SMOS		
27128	79	51	V11	None	S
27C256	93	32	V11	None	S
27C64	79	33	V11	None	S
			Synertek		
2716	19	23	A	None	O
			Texas Instruments		
14S10	03	01	A	None	O
14SA10	03	01	A	None	O
18S030	04	02	A	None	A
18S22	04	08	A	None	O
18S42	04	09	A	None	O
18S46	04	15	A	None	O
18SA030	04	02	A	None	A
18SA22	04	08	A	None	O
18SA42	04	09	A	None	O
18SA46	04	15	A	None	O
24S10	13	01	A	None	A
24S166	13	53	005	351A-065	O

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
			Texas Instruments (Continued)		
24S41	13	38	A	None	A
24S81	13	06	A	None	A
24SA10	13	01	A	None	A
24SA166	13	53	005	351A-065	O
24SA41	13	38	A	None	A
24SA81	13	06	A	None	A
2508	19	22	A	None	A
2516	BD	23	V08	None	A
2532	BD	25	V08	None	A
2564	BD	30	V08	None	A
25L32	BD	25	V08	None	S
2708	21	27	A	None	A
27128	79	51	V08	None	A
27128A	93	51	V11	None	S
27256	93	32	V11	None	S
2732	BD	24	V08	None	A
2732A	63	24	V08	None	A
2764	79	33	V08	None	A
27C128	93	51	V11	None	A
27C256	93	32	V11	None	A
27L08	21	27	A	None	A
28L166	13	21	G	None	A
28L22	13	46	G	None	A
28L42	13	09	G	None	A
28L45	13	15	G	None	S
28L85	13	16	G	None	S
28L86	13	16	A	None	A
28LA22	13	46	G	None	A
28P166	13	21	G	None	S
28P42	13	09	G	None	S
28P45	13	15	G	None	S
28P85	13	16	G	None	S
28S166	13	21	G	None	A
28S2708	13	16	A	None	A
28S42	13	09	A	None	A
28S45	13	15	G	None	S
28S46	13	15	G	None	A
28S85	13	16	G	None	S
28S86	13	16	A	None	A
28SA166	13	21	G	None	S
28SA42	13	09	G	None	A
28SA46	13	15	G	None	A
28SA86	13	16	A	None	A
54LS2708	13	16	A	None	O
54LS478	13	16	A	None	O
54S188	04	02	A	None	O
54S2708	13	16	V09	None	O
54S287	03	01	A	None	O

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
		Texas Instruments (Continued)			
54S288	04	02	A	None	O
54S387	03	01	A	None	O
54S454	13	06	A	None	O
54S455	13	06	A	None	O
54S470	04	08	A	None	O
54S471	04	08	A	None	O
54S472	04	09	A	None	O
54S473	04	09	A	None	O
54S474	04	15	A	None	O
54S475	04	15	A	None	O
54S476	13	38	A	None	O
54S477	13	38	A	None	O
54S478	13	16	A	None	O
54S479	13	16	A	None	O
74188	04	02	A	None	O
74LS478	13	16	A	None	O
74S188	04	02	A	None	O
74S2708	13	16	A	None	O
74S287	03	01	A	None	O
74S288	04	02	A	None	O
74S387	03	01	A	None	O
74S454	13	06	A	None	O
74S455	13	06	A	None	O
74S470	04	08	A	None	O
74S471	04	08	A	None	O
74S472	04	09	A	None	O
74S473	04	09	A	None	O
74S474	04	15	A	None	O
74S475	04	15	A	None	O
74S476	13	38	A	None	O
74S477	13	38	A	None	O
74S478	13	16	A	None	O
74S479	13	16	A	None	O
TMS2716	23	28	A	None	O
		Thomson CSF			
27C64	93	33	V11	None	S
71190	92	21	004	None	A
71191	92	21	004	None	A
		Toshiba			
24128AP	5C	51	V11	None	S
24128P	79	51	V11	None	S
24256AP	5C	32	V11	None	S
24256P	45	32	V11	None	S

UNIPAK™ DEVICE LIST

Device Part Number	Family/Pinout Codes		Software Version	Adapter	Approval Status
		Toshiba (Continued)			
2464AP	5C	33	V11	None	S
2464P	79	33	V11	None	S
27128	79	51	004	None	S
27128AD	5C	51	V11	None	S
27256AD	5C	32	V11	None	S
2732	19	24	A	None	S
27512D	5E	A4	V11	None	S†
2764	79	33	004	None	S
2764AD	5C	33	V11	None	S
321	21	26	A	None	S
322	21	27	A	None	S
323	19	23	A	None	S
8755AC	47	55	005	351A-072	S
TC57256	45	32	V10	None	S
TMM27256	45	32	V10	None	S
		VLSI Technology, Inc.			
27C128	5D	51	V11	None	S
27C256	5D	32	V11	None	S
28H64	C9	A6	V10	None	S
VT27C512	4C	A4	V11	None	S†
VT27C64	5D	33	V11	None	S
		Waferscale Integration, Inc.			
WS27C128	3B	51	V11	None	S
WS27C256	3B	32	V11	None	S
WS27C64	3B	33	V11	None	S
WS57C128	3C	51	V11	None	S
WS57C256	3C	32	V11	None	S
WS57C49	3C	67	V11	None	S
WS57C64F	3C	33	V11	None	S
		Xicor			
2804A	B7	82	V08	None	S
2816A	B7	23	V08	None	S
2864A	C3	98	V11	None	S

APPENDIX B
TIMING DIAGRAMS

The timing diagrams are no longer a part of the UniPak manual; however, they are available for purchase from the Data I/O Corporate Service Department.

APPENDIX C
SCHEMATICS

008-1998	Address Card
008-1999	Motherboard
30-701-7997	Waveform Generator
30-702-0045	UniPak™ Memory
30-702-7995	Socket Assembly

REVISIONS				
ZONE	LTR	DESCRIPTION	PE	CM	DATE
	A	RELEASE PER ECN 4564			5/82
	B	ECN 4728			1/83

VPUP 20 · +15V · +5V · -9V · R7 2.2K · R8 2.2K · +5V · Q1 2N2907 · CR17 IN4148 · R9 300 ½W · VCC PULLUPS · RP1 330

SOCKET SELECT S1 44 · S2 45 · S3 46

U2 6108 (OUT, EN, +VCC, VCC, GND; A0, A1, A2; S1 S2 S3 S4 S5 S6 S7 S8) · R4 R5 R6 3.3K (3 PLACES) · +5V

VCC SENSE MUX · CR15 CR11 CR13 CR16 CR14 CR12 CR8 IN4003 (7 PLACES)

VCC SENSE · VCC 34

U1 74LS138 (G2A G2B GND G1 VCC; A B C; Y0 Y1 Y2 Y3 Y4 Y5 Y6) · L.E.D. DECODE · SOCKET LED'S DS4 · R15 R16 R17 R18 R14 R12 R11 470 (7PLACES) · DS5 DS6 DS7 DS8 DS3 DS2 DS1

CE SUPPLY 11 · CR10 IN4148 · CR9 IN4148

PIN 18 38 · PIN 19 39 · PIN 20 13 · PIN 21 14 · J1

CR5 CR6 CR7 1N5819 (3 PLACES) CE · RI0 10K · C7 .1/50V · +5V

SPIKE CLAMPS

O.C. 18 · BIT SPLY. 37 · BIT SW 12 · VREF 21 · VCC SENSE 10 · SP 47 · SP 48 · SCOPE TRIGGER 15 · GND 9 35 36 50 · +5V 49 · +24 VOLTS 17 J2

C1, C4 .1/50V · R3' 1K · C2 .1/50V · CR3 IN4745 · +15V

-9V 16 J2 · C3 .1/50V · -9V

ADDRESS PULLUPS · +5V · R2 10K · RP3 10K · R1 10K · RP2 10K

VCC PROTECTION · U3 TID121 · CR4 IN4745

DEVICE SOCKETS

C6 .01 50V · U5 SOCKET 1

C8 .01/50V · U6 SOCKET 2

C9 .01/50V · U8 2K X 4 SOCKET 3

DIODE PROTECTION · U4 TID121 · U17 TID121 · +5V

PA15 8 · PA14 7 · PA13 6 · PA12 5 · PA11 4 · PA10 3 · PA9 2 · PA8 1 · PA7 26 · PA6 27 · PA5 28 · PA4 29 · PA3 30 · PA2 31 · PA1 32 · PA0 33 J2

PD1 40 · PD2 41 · PD3 42 · PD4 43 · PD5 25 · PD6 24 · PD7 23 · PD8 22 J2

DEVICE SOCKETS

C15 .01 50V · U16 SOCKET 7

C13 .01 50V · U14 SOCKET 6

C12 .01 50V · U13 SOCKET 5

C10 .01 50V · U11 SOCKET 4

DATA SPIKE CLAMPS · U12 TID121 · C11 .1/50V

DATA SPIKE CLAMPS · U15 TID121 · C14 .1/50V

DATA PULLDOWNS · U9 1K · U10 TID121

Q2 2N2222 · PULL DOWN CONTROL 19 J2

DATA SPIKE CLAMPS · U7 TID121

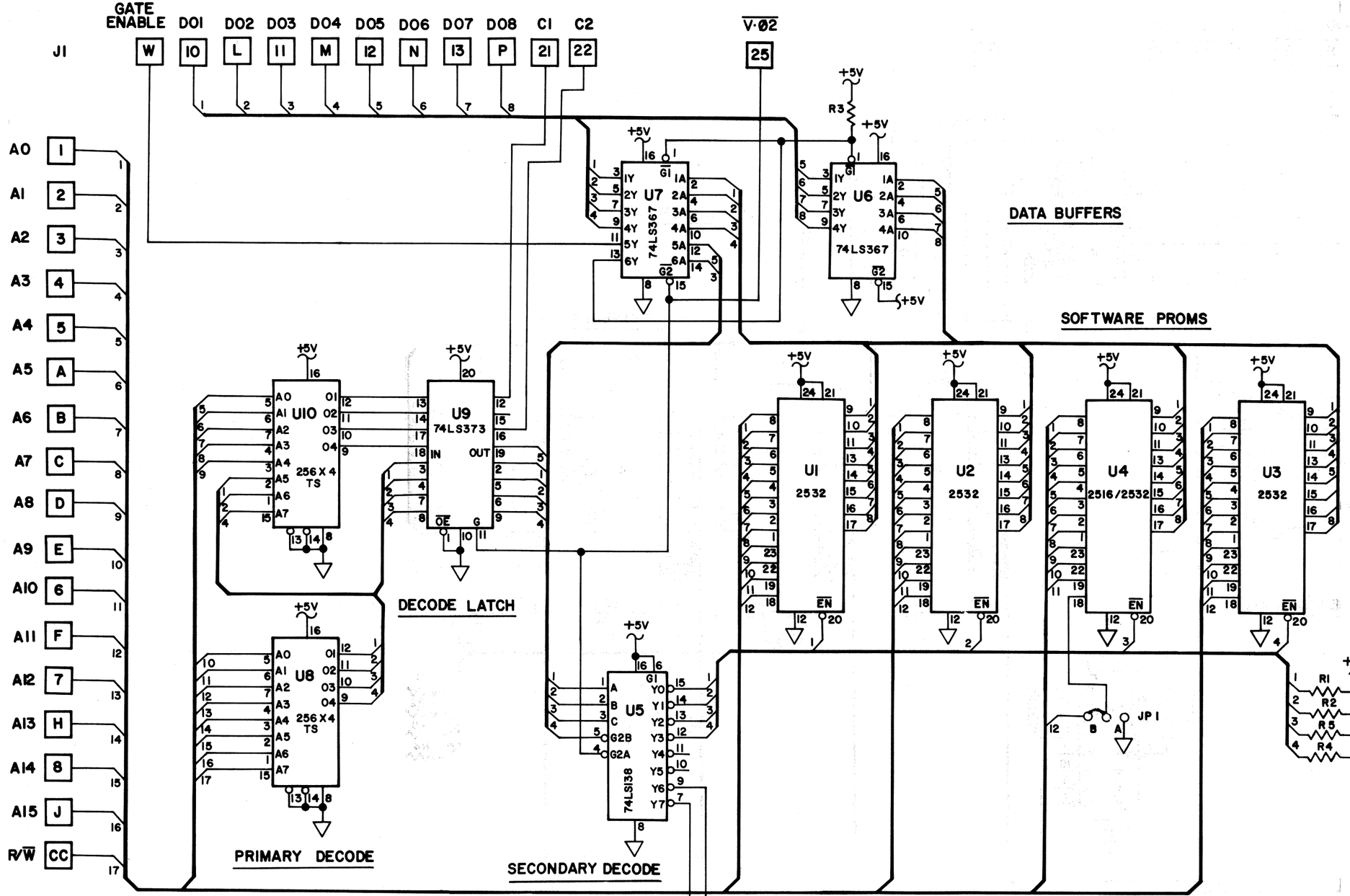
CR2 IN4148 · CR1 IN4148 · C5 .1/50V · R13 4.7K · BIT SUPPLY

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE 1/4 W AND IN OHMS, 5%.
2. ALL CAPACITORS ARE IN MICROFARADS.
3. LAST REFERENCE DESIGNATOR USED:
   - U17 CR17
   - RP3 C15
   - R18 J2
   - DS7 Q2

APPROVALS SEE ECN 4564	TOLERANCES (EXCEPT AS NOTED)	DATA I/O	
PROJ ENG	DECIMAL ±	TITLE	SCHEMATIC DIAGRAM UniPak SOCKET ASSEMBLY
M.C.	ANGULAR ±		
ENGR MGR	DRAWN BY: G. RYDER	SIZE D	CODE IDENT NO. DRAWING NO. 30-702-7995
DATE	CHECKED BY:		SHEET 1 OF 1

REVISIONS					
LTR	DESCRIPTION	DR	CHK	APPR'D	DATE
A	RELEASE	TC			5-4-81
B	INCORPORATED ECP 0061 ADCNA1				



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE 3.3K OHMS 1/4W 5%
2. ALL CAPACITORS ARE IN MICROFARADS.
3. JP 1, POS. A
4. LAST REFERENCE DESIGNATOR USED:
   C8
   R5
   U10
   JP 1

P/N 702-0045-002

APPROVALS:		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES.	DATA I/O IBAQUAH, WASH.	
DSN. ENGR. Gary M. Ball	DATE 5/5/81	TOLERANCES, UNLESS OTHERWISE SPECIFIED: .XX ± .XXX ± ANGULAR	TITLE SCHEMATIC DIAGRAM UNIPAK MEMORY	
MFG. ENGR. K.T. Wanner	DATE 5-5-81			
QUAL. ASSUR.	DATE 5-6-81	DRAWN BY: TCC	DATE 3-16-81	SIZE D / CODE INDENT. NO. 54193 / DRAWING NO. 30-702-0045
ENGR. MGR. Charles H. Lahm	DATE 5-5-81	CHECKED BY: Kathryn Hanz	DATE 5-6-81	SCALE NONE / SHEET 1 OF 1

BRUNING 49270

REVISIONS						
ZONE	LTR	DESCRIPTION	DR	CK	PE	APP	DATE
	A	RELEASE PER ECN 4564					5/82
	B	ECN #5045	SH				8/84

NOTES:

UNLESS OTHERWISE NOTED:

1. RESISTORS ARE 1/4W AND IN OHMS.
2. CAPACITORS ARE IN MICROFARADS.
3. U2, U5, U6, U7 - PIN 7 = GND, PIN 14 = +5V
   U12, U16 - PINS 18 10 = GND, PIN 20 = +5V
   U13, U14, U15 - PIN 8 = GND, 16 = +5V
   U4 PIN 4 = +24, PIN 11 = -9
4. LAST REF. DES. USED:
   RP3
   C40
   Q25
   U16
   CR17
   R70
   TP4
   VR2

P/N 701-7997-002

APPROVALS SEE ECN #4564	TOLERANCES EXCEPT AS NOTED	DATA I/O
PROJ. ENG.	DECIMAL ±	TITLE SCHEMATIC DIAGRAM UniPak WAVEFORM GENERATOR
Q.C.	ANGULAR ±	
ENG. MGR.	DRAWN BY T. CONNERTON	SIZE D; CODE IDENT NO; DRAWING NO 30-701-7997
DATE 1-17-80	CHECKED BY:	SCALE —; SHEET 1 OF 2

REVISIONS						
ZONE	LTR	DESCRIPTION	CK	PE	APP	DATE
	B	SEE SHEET ONE				6/11/82

J1 (34) (10) (11) (37) (12) (18)

VCC
VCC SENSE
$\overline{CE}$ SUPPLY
BIT SUPPLY
CURRENT SOURCE REFERENCE
CURRENT SOURCE KILL
OVERCURRENT CONTROL
CONTROL LATCH BUS
SWITCH CONTROL LATCH BUS

C40 .001

+5V RP2 3.3K U3 74LS04 U2 7406 C37 200PF

+48 RP3 IK Q1 MPQ3799 CR4 IN4003 CR5 IN 4003 CR3 IN4003 R17 5K 10T R2 32.4K 1% Q3 2N2484 R3 4.7K CR13 IN4370 R1 IK

PIN 18 SWITCH

MJE 800 Q4 CR6 IN4003 R21 2.2K 1/2W J1 (38) PIN 18

R49 3.3K U7 7406 C9 470pf Q20 MJE240

+12, +5 SUPPLY

+24 VR2 78L12 C21 .1/50 R22 330 U2 7406 (4) PA 11

R61 1.2K CR17 IN751 R47 2.2K 1/2W U7 7406 (13) PIN 20

R68 3.3K U7 7406 Q2 MPQ3725A U5 7438 U7 7406 Q14 MJE800 CR12 IN4003 R48 330 C14 470pf Q24 MJE240

PIN 20 SWITCH

+18V R50 IK 1/2W TP4 5VREF R60 5K 10T VR1 TL430 Q23 MJE240 R9 3.3K

DAC REF

74LS04 U6 U2 7406

Q18 MJE800 CR11 IN4003 R13 2.2K 1/2W (14) PIN 21

R25 3.3K R24 3.3K Q11 2N2907 U5 7438 U2 7406 R23 3.3K R20 3.3K Q7 MJE240 R27 IK R28 IK R29 470 2N2907 Q5 R26 IK R30 IK Q19 2N2222 C10 470pf Q21 MJE240

-9V R70 300K R51 10K 1%

PIN 21 SWITCH

5VREF R52 4.42K 1% R53 4.99K 1% U6 74LS04 U2 7406 U4 LM148 R5 IK CR16 IN4003 Q15 MJE251 C15 .1/50

-5V SUPPLY OVERCURRENT

R12 4.7K Q25 MJE240 R31 IK R14 5.1 1/2W -9V

-5V, OV SUPPLY

+24V (17)
-9V (16)
+5V (49)
GND (9) (35) (36) (50)

BIT SWITCH

C3 470pf R11 2.2K Q6 MJE251 CR7 IN4003 R46 470 Q12 MJE240 R7 220 R10 330 C4 470pf Q22 MJE240 R8 2.2K 1/2W

J1 (3) PA 10 (39) PIN 19 (5) PA 12 (19) PULL DOWN CONTROL

R67 4.7K

PIN 19 SWITCH

APPROVALS: SEE ECN #4564	DATE		DATA I/O
PROJ. ENG. Victor E Belmont		TOLERANCES DECIMALS .X ± .XX ± .XXX ± ANGULAR DO NOT SCALE DRAWING	TITLE SCHEMATIC DIAGRAM UniPak WAVEFORM GENERATOR
Q.C. John Winston		DRAWN BY T. CONNERTON	DATE 10/79
ENG. MGR. G. Bell		CHECKED BY:	DATE
		SIZE D	CODE IDENT NO
		DRAWING NO 30-701-7997	
		SCALE —	SHEET 2 OF 2

REVISIONS							
ZONE	LTR	DESCRIPTION	DR	CHK	CM	PE	DATE
	A	RELEASE					1-17-80
	B	ECN 3742					7-80
	C	ECN 5045	SH				4/84
	D	INCORP ADDN C1 PER ECP00000					5-12-6

PA0 PA1 PA2 PA3 PA4 PA5 PA6 PA7 PA8 PA9 PA10 PA11 PA12 PA13 PA14 PA15

JI

S1 S2 S3 VPUP SCOPE TRIGGER

TPI

ADDRESS LATCH

PI

A0 A1 A2 A3 A4 A5 A6

U21 74LS373

L.O. ADDRESS LATCH

U1 74LS259

H.O. ADDRESS LATCH

U2 74LS259

SOCKET SELECT LATCH

U14 74LS259

TO RANGE SWITCH

+5V 49

ENABLES

JI 18 OVER CURRENT

SECONDARY DECODE

U15 LS138

U20 LS138

C1 27

V·Ø2 32

C2 28

C3 29

C4 EE

C5 FF

C6 HH

DATA GATE

U24 74LS244

GATED DATA

RP8 3.3K

RP5 3.3K

RP4 3.3K

GND 9 35 36 50

DO1 9

DO2 10

DO3 11

DO4 12

DO5 13

DO6 14

DO7 15

DO8 16

U25 74LS373

LATCHED DATA

RP3 3.3K

DATA LATCH

U7 74LS251

U8 1/4 LM339

R9 100K 1%

R11 100K 1%

R16 100K 1%

R15 33.2K 1%

R10 100K 1%

R12 33.2K 1%

JI 10 VCC SENSE

13 PIN 20

BIT SW.

VREF

COMPARATOR GATE/MUX

JPI

U27 74LS04

GND 23 26 DD

RAW 18V LL

OPERATE BB

SENSE 24

R22 10

R23 10K 1%

+PROG 22

-9V Y

C13 .1/50V

C15 22/15V

C17

CR2 IN751

C3 .1/50V

C14 1/50V

C4 .1/50V

C16 22/15V

R25 10K

+VP

UNUSED GATES

SUPPLY COMPARATORS

NOTES:

UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 1/4W, 5%.
2. ALL CAPACITORS IN MICROFARADS.
3. LAST REF. DES. USED:
   U27 R25 TP4
   CR5 C17 J1
   RP8 Q19 JP1

P/N 701-1998

TOLERANCES (EXCEPT AS NOTED)	DATA I/O ISSAQUAH, WASH.		
DECIMAL ± ANGULAR ±	TITLE SCHEMATIC DIAGRAM UniPak ADDRESS CARD		
APPROVALS	SIZE D	CODE IDENT. NO.	DRAWING NO. 008-1998
PROJ. ENG.	DRAWN BY:		
ENG. MGR.	CHECKED BY	SCALE NONE	SHEET 1 OF 2
DATE 1-4-80			

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
		SEE SHEET ONE	1-17-90	VB

DATA SINKS

DATA SWITCHES

LATCHED ATA

GATED DATA

ENABLES

DATA COMPARATORS

DATA SINK REGISTER

DATA SWITCH REGISTER

U12 74C374

U18 74C374

U26 AD7524

U19 AD7524

U22 LM339 1/4

U23 LM339 1/4

U13 LM148

U11

U16, U17, MPQ3725A

U5 TID139

U4 TID139

U3 TID121

U6 TID121

RP5 3.3K

RP7

RP6 330

RP2 10K

RP1 2K

RI7 220

R1 5K 15T

R2 2.2K 1/2W

R3 4.7K

R4 4.7K

R5 10K

R6 1K

R8 34.8K 1%

R13 10K

R14 2K

R18 10K, 1%

R19 4.99K 1%

R20 4.99K 1%

R21 330

R24 470

C1 .1/50

C2 .1/50

C5 250pf

C6 250pf

C7 250pf

C8 250pf

C9 250pf

C10 250pf

C11 250pf

C12 250pf

CR1 TL430

CR3 IN4148

CR4 IN4148

CR5 IN4733

Q1 2N2907

Q2 2N2484

Q3 MJE240

Q5 MJE251

Q6 MJE251

Q7 MJE251

Q8 MJE251

Q9 MJE251

Q10 MJE251

Q11 MJE251

Q12 MJE240

Q13 MJE240

Q14 MJE240

Q15 MJE240

Q16 MJE240

Q17 MJE240

Q18 MJE240

Q19 MJE240

+5V

+VP

-9V

+PROG

VREF

RAW18V

VREF DAC

LOAD SUPPLY DAC

LOAD AMP

LOAD SUPPLY

HI-LO RANGE SWITCH

LOW RANGE LOADS

HIGH RANGE LOADS

LOAD CLAMPS

LOAD CLAMPS

TO RANGE SWITCH

J1

21 VREF

40 PD1

41 PD2

42 PD3

43 PD4

25 PD5

24 PD6

23 PD7

22 PD8

12 BIT SWITCH

BIT SW

TP2

TP3

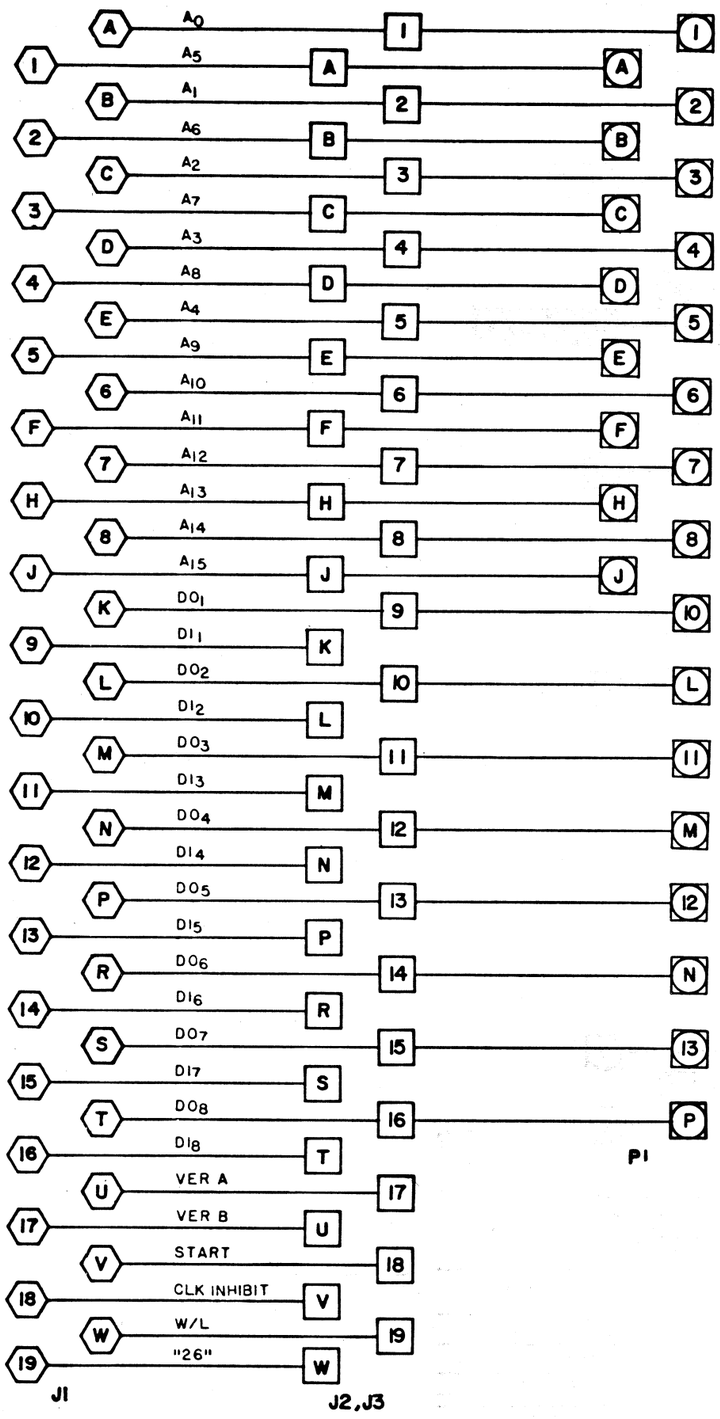
TP4

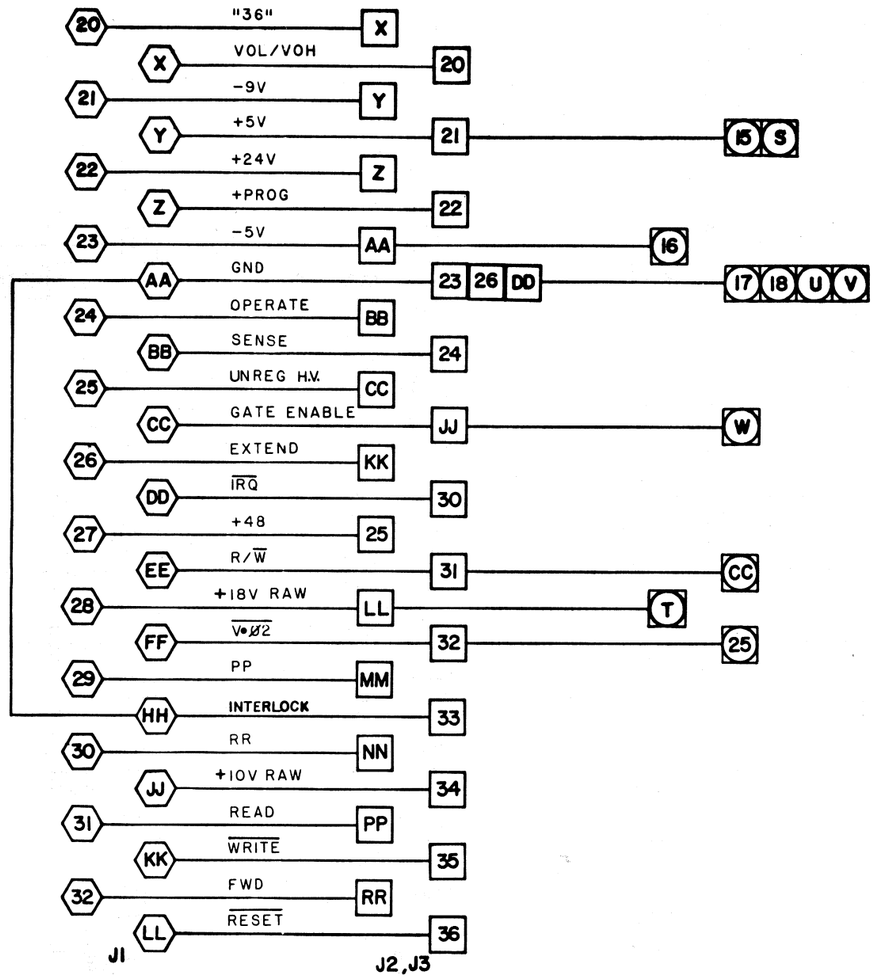
SP 47

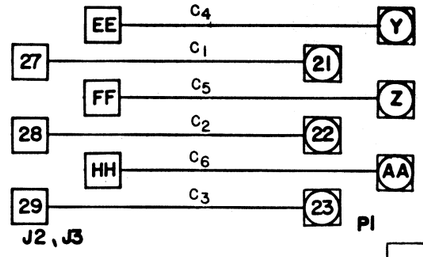
SP 48

TOLERANCES (EXCEPT AS NOTED)	DATA I/O ISSAQUAH, WASH.		
DECIMAL ±	TITLE SCHEMATIC DIAGRAM UniPak ADDRESS CARD		
ANGULAR ±			
DRAWN BY:	SIZE D	CODE IDENT. NO	DRAWING NO. 008-1998
APPROVED BY: Victor E. Redondo	SCALE NONE		SHEET 2 OF 2

REVISIONS						
ZONE	LTR	DESCRIPTION	DWN	CK	PE	DATE
	A	RELEASE	BEL			1-80
	B	INCORP ADCN A1 PER ECP 0237	SH			5/6/82







P/N 702-1999

APPROVALS	TOLERANCES (EXCEPT AS NOTED)	DATA I/O REDMOND WASH	
PROJ. ENG.	DECIMAL ±	TITLE SCHEMATIC DIAGRAM	
PROD. ENG.	ANGULAR ±	MOTHERBOARD	
CONF. MANG.	DRAWN BY: G. RYDER	SIZE D	CODE IDENT NO.	DRAWING NO. 008-1999
ENG. MGR.	CHECK	SCALE NA	SHEET 1 OF 1
DATE 1-17-80			

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