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Find the *Measurement Computing / Data Translation DT2814* at our website: *Click HERE*

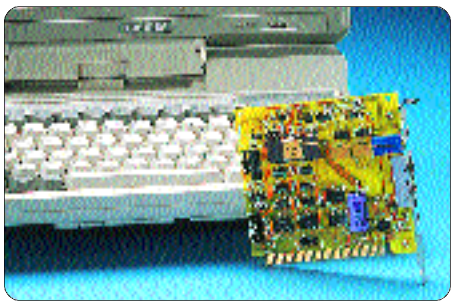
ISA Hardware

DT2814

Analog Input

The DT2814 is a low-cost A/D board for use with IBM PC-compatible computers. While compatible with desktop computers, the DT2814 is designed on a half-size board and uses special power circuitry for compatibility with some laptops. The board provides 40 kS/s throughput and is supported by many application software packages.

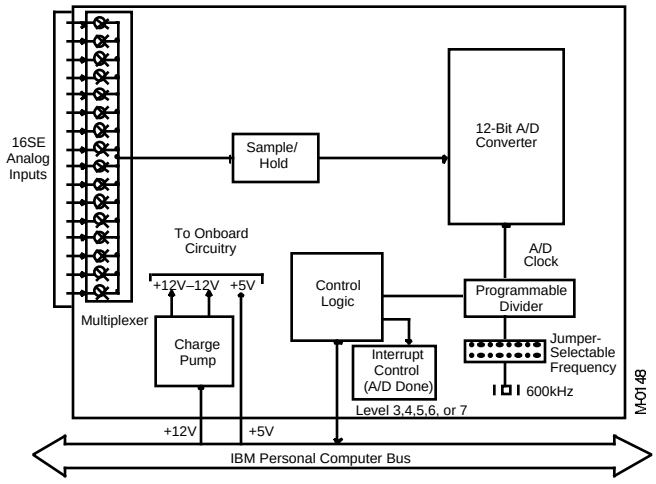
- Onboard pacer clock for determining repeated A/D conversion rates from .005 Hz to 30 kS/s
- Support for interrupts



M-5234

Summary

A/D: 12 bits; 30 kHz* throughput; 16 SE
Clock: One programmable clock
Interface: Programmed I/O; one interrupt
*Maximum of 30 kS/s under DOS, 500 S/s under Windows.



Ordering Summary

All Data Translation products are covered by a 1-year warranty. For pricing information, see a current price list, visit our web site, or contact your local reseller.

DT2814 ships with a DT-Open Layers device driver, example programs for Windows 95,/98 and a comprehensive user manual.

- DT2814

Accessories

- DT757–General purpose screw terminal panel and cable
- DT709-Y–Conditioning panel, 16 DI; needs EP047
- DT756-Y–Conditioning panel, 16 DI; auto thermocouple CJC; needs EP047

Software

The following products include a copy of the software, a single-user license, and a user manual. All software is supplied on CD-ROM, except as noted.

- HP VEE with DT VPI visual programming software Version 5.0 for Windows 95/98 SP1950-CD
- HP VEE Lab with DT VPI visual programming software version 5.0 for Windows 95/98 SP1950-LAB
- TestPoint software for designing test, measurement and D/A applications for Windows 95/98 SPTPXX–CD (see page 32 for details)
- DTx-EZ visual programming tools for Visual Basic and Visual C++ for Windows 95/98 SP0970-CD
- DataAcq SDK Software Development Kit for Windows 95/98 SP0945-CD
- DT-LV Link data acquisition connection to LabVIEW for Windows 95/98, on 3.5 in. 1.4 MB disk SP0810-CL

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Specifications

All specifications are typical at 25° C and rated voltage, unless otherwise specified.

ANALOG INPUTS

Resolution
12 bits

Throughput
30 kS/s

Input Channels
16SE

Gain
One

Ranges
0–5 V; ±2.5, 5 V

System Error
±.03% of FSR

Conversion Time
15 µs

Max. Input Voltage Protection
±30 V On, ±20 V Off

PACER CLOCK

Function
Initiates A/D conversions. Clock is started by software command.

Usable Range
.005 Hz (3 min, 20 sec) to 30 kS/s (33 µs)

Description
Clock consists of a divider with a range of 1 to 10,000,000 in decade increments and an onboard oscillator with a jumper-selectable frequency of 50 kS/s to 600 kS/s.

GENERAL

Interface
ISA bus or EISA bus; I/O mapped; 8-bit data path

Interrupt—one line, jumper-selected level; source: A/D done

Power Requirements
+5V @ 200 mA typical; +12 V @ 50 mA max

Physical/ Environmental

Dimensions—half-size PC/XT/AT board; 10.75 x 10.75 x 2.54 cm (4.23 in. x 4.23 in. x 1 in.)

Connector—
20-pin, double row header

Temperature—operating: 0 to 50° C; storage: –25 to 70° C

Relative Humidity—to 95%, non-condensing

EMICompliance—all models are FCC Class A verified; will not compromise FCC compliance of host computer

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