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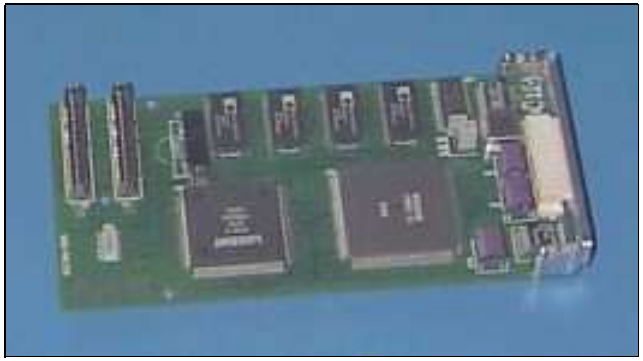
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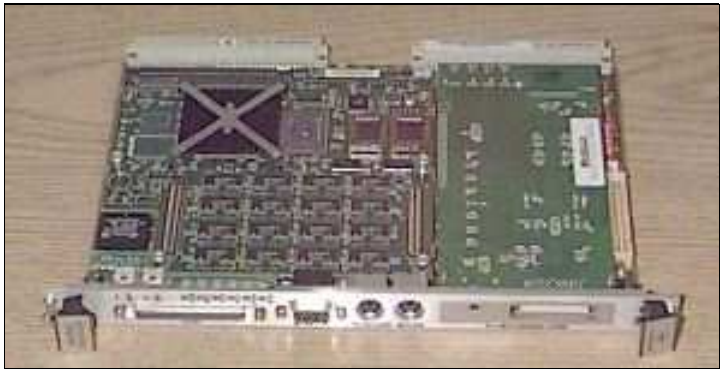
M2M-PMC32C

32-bit, 33MHz, Myrinet-SAN/PCI interface in the PMC form factor

Many single-board Computers (SBCs) accept PCI devices in a standard form factor called "PMC" -- the PCI Mezzanine Card (IEEE P1386.1).



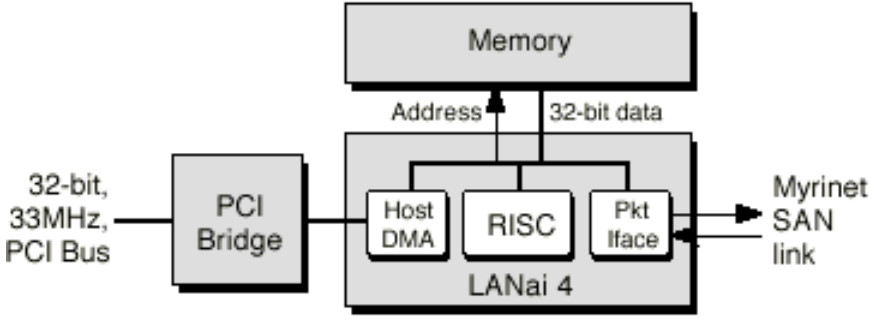
M2M-PMC32C, Myrinet-SAN/PCI interface in the PCI Mezzanine Card (PMC) form factor



M2M-PMC32 interface on a Motorola MVME1603e SBC.
The Myrinet-SAN-port end of the PMC interface fits into the VME front panel.

This PMC Myrinet-SAN/PCI interfaces is functionally identical to the M2M-PCI32C PCI-short-card version except that it adheres to the (single-width) PMC form factor. The small, very flexible, Myrinet-SAN cables that plug into Myrinet-SAN ports are ideal for in-chassis connections on the front-panel side of VME systems. 6U VME SBCs can be clustered using a 6U-VME-form-factor Myrinet-SAN switch.

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Block diagram of the M2M-PMC32B&C

Specifications

PCI-bus Interface: 32-bit, 33MHz, supports all burst modes and write-invalidate, master or slave. Sustained DMA performance at 33MHz with large blocks exceeds 120 MB/s peer mode, or to/from system memory on platforms with good PCI implementations.

Interface processor: LANai-4 RISC operating at the PCI-clock rate.

Interface memory: 1MB (256Kx4B). The interface memory operates at double the PCI-clock rate. With a 33MHz PCI clock, 264 MB/s of memory bandwidth is available to support the Myrinet link, the host DMA, and the RISC processor.

Myrinet-SAN port: 1.28+1.28 Gb/s on the A link; B link unused.

Physical characteristics: PCI Mezzanine Card; ~4 Watts.

Myricom-supported software: Both GM and MyriAPI software, which can be downloaded from the Software & Customer Support page.

Last updated: 1 July 1999

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