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Datasheets:

File: 65570tds.pdf
Rev: G
File Size: 0.13 MB

Ordering Information:

File: OI65570tds.pdf
Rev: G
File Size: 0.03 MB

Manuals:

File: MN-65572TX-001 Hardware Manual for 1 to 2 Channel MIL-STD-1553 Tester/
Rev: D
File Size: 0.58 MB

File: Runtime Library Manual MIL-STD-1553 Tester/Simulator
Rev: F
File Size: 1.08 MB

File: Tester Simulator Menu Manual for MIL-STD-1553 Tester/Simulator
Rev: E
File Size: 1.12 MB

General Documentation:

File: MIL-STD-1553 Designer's Guide
Rev: 6th Edition
File Size: 3 MB

Available Software

BU-69065S0
Description: 32-Bit Tester/Simulator Menu
OS: Win 9x/NT/2000/XP
File Size: 3.1 Mb
Download:
Rev: 4.4.0
Rev: 4.4.15

BU-69068S0
Description: 32-Bit Tester/Simulator Runtime Library
OS: Win 9x/NT/2000/XP
File Size: 3.1 Mb
Download:

CompactPCI Dual Tester/Simulator Card

MODEL: BU-65570T/72T

FEATURES

- CompactPCI Format, One or Two 1553 Buses
- PXI Certified
- PICMG Certified
- Variable Amplitude Transceivers
- 128K Words of Shared RAM per Bus
- Simultaneous Emulation of BC, 31 RTs, and MT for Each Bus
- IRIG-B Interface
- DMA Data Transfers via PCI Master Operation
- Software Configuration of Bus Coupling and Termination
- PCI Plug and Play Compatible
- 32-Bit Time Tag with 1 µsec resolution
- Replay of previously recorded Bus Traffic via Menu and Run Time Library

- Compatible with *data*SIMS/*data*MARS

DESCRIPTION DDC"s BU-65570T/72T is a versatile CompactPCI Card designed for the test and simulation of MIL-STD-1553 systems. It provides full, intelligent interfacing for one or two serial dual redundant MIL-STD-1553 data buses and a CompactPCI chassis.The BU-65570T offers a fixed bus voltage output while the BU-65572T has a variable volt-age output transceivers for each of the two buses.

Support is provided for IRIG receiving 1Khz Pulse Code Modulated input in both the BU-65570T and BU-65572T as a standard feature. IRIG-B (Inter-Range Instrumentation Group) is a Parallel time code defined by IRIG Standard 200-95.

The card design includes a PCI Master Mode, which has the capa-bility to transfer blocks of data to the host via Direct Memory Access in the Monitor mode. PCI true Plug and Play compatibility is also provided, simplifying installation into a CompactPCI compliant system. The bus connection and configuration (transformer or direct) are software controlled, as well as software configuration for the bus termination.

The BU-65570T/72T cards still provide the Bus Controller, Remote Terminal, and Monitor functions of its DDC predecessors.

C, Visual Basic® and LabVIEW™ programming libraries are sup-plied with the card. The software libraries include access to the play-back capabilities of the board.

PXI PCI INDUSTRIAL PICMG COMPUTERS

PACKAGE: Compact PCI

Lead Free: B

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Rev: 4.8.0
Rev: 4.8.19

BU-69068S1
Description: Tester/Simulator Runtime Library
OS: LINUX
File Size: 190 KB
Download:
Rev: 4.5.0
Rev: 4.5.7

BU-69068S2
Description: Tester/Simulator Runtime Library
OS: VxWorks
File Size: 240 KB
Download:
Rev: 1.1.0
Rev: 1.1.8

PRODUCT NUMBER SEARCH: Go SITE SEARCH: Go

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