ARTISAN®
TECHNOLOGY GROUP

Your **definitive** source for quality pre-owned equipment.

Artisan Technology Group
(217) 352-9330 | sales@artisantg.com | artisantg.com

Full-service, independent repair center
with experienced engineers and technicians on staff.

We buy your excess, underutilized, and idle equipment
along with credit for buybacks and trade-ins.

Custom engineering
so your equipment works exactly as you specify.

- Critical and expedited services
- In stock / Ready-to-ship
- Leasing / Rentals / Demos
- ITAR-certified secure asset solutions

Expert team | Trust guarantee | 100% satisfaction

All trademarks, brand names, and brands appearing herein are the property of their respective owners.

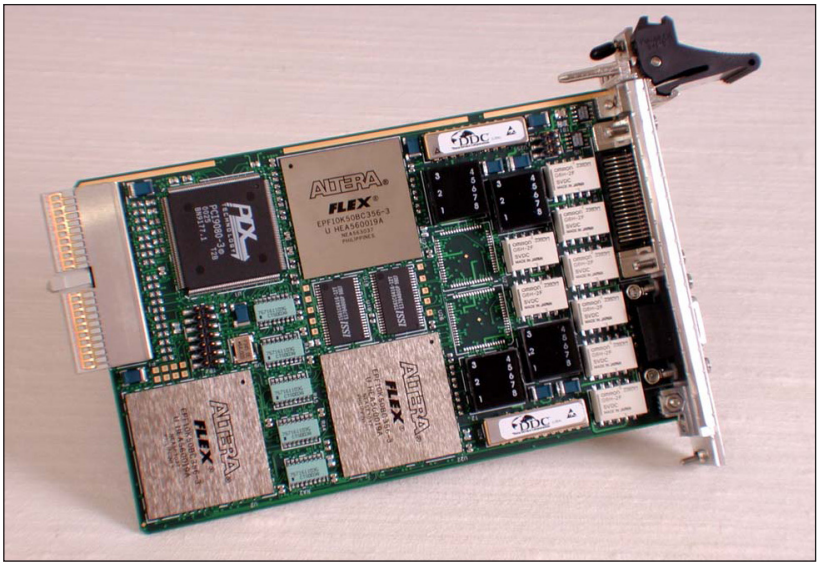
Find the *DDC BU-65572T1-300* at our website: *Click HERE*

BU-65570T, BU-65572T

MIL-STD-1553B BC/RT/MT CompactPCI® DUAL TESTER/SIMULATOR CARD

Make sure the next Card you purchase has...

DDC on Board®



DESCRIPTION

DDC's BU-65570T/72T is a versatile CompactPCI Card designed for the test and simulation of MIL-STD-1553 systems. It provides full, intelligent interfacing for one or two serial dual redundant MIL-STD-1553 data buses and a CompactPCI chassis. The BU-65570T offers a fixed bus voltage output while the BU-65572T has variable voltage output transceivers for each of the two channels. The output of these transceivers is controlled by software with a minimum of 0 V and a maximum of 21.5 V over 1024 steps.

Support is provided for IRIG receiving 1Khz Pulse Code Modulated input in both the BU-65570T and BU-65572T as a standard feature. IRIG-B (Inter-Range Instrumentation Group) is a Parallel time code defined by IRIG Standard 200-95.

The card design includes a PCI Master Mode, which has the capability to transfer blocks of data to the host via Direct Memory Access in the Monitor mode. PCI true Plug and Play compatibility is also provided, simplifying installation into a CompactPCI compliant system. The bus connection and configuration (transformer or direct) are software controlled, as well as software configuration for the bus termination.

The BU-65570T/72T cards still provide the Bus Controller, Remote Terminal, and Monitor functions of its DDC predecessors.

'C', Visual Basic® and LabVIEW™ programming libraries are supplied with the card. The software libraries include access to the playback capabilities of the board.

FEATURES

- CompactPCI Format, One or Two 1553 Buses
- Variable Amplitude Transceivers
- 128K Words of Shared RAM per Bus
- Simultaneous Emulation of BC, 31 RTs, and MT for Each Bus
- IRIG-B Interface
- DMA Data Transfers via PCI Master Operation
- Software Configuration of Bus Coupling and Termination
- PCI Plug and Play Compatible 32-Bit/33Mhz
- 32-Bit Time Tag with 1 µsec resolution
- Replay of previously recorded Bus Traffic via Menu and Runtime Library

PCI INDUSTRIAL COMPUTERS PICMG

PXI

FOR MORE INFORMATION CONTACT:

Technical Support:
1-800-DDC-5757 ext. 7771

DDC® Data Device Corporation
105 Wilbur Place
Bohemia, New York 11716
631-567-5600 Fax: 631-567-7358
www.ddc-web.com

All Trademarks are the property of their respective owners
© 2002 Data Device Corporation

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

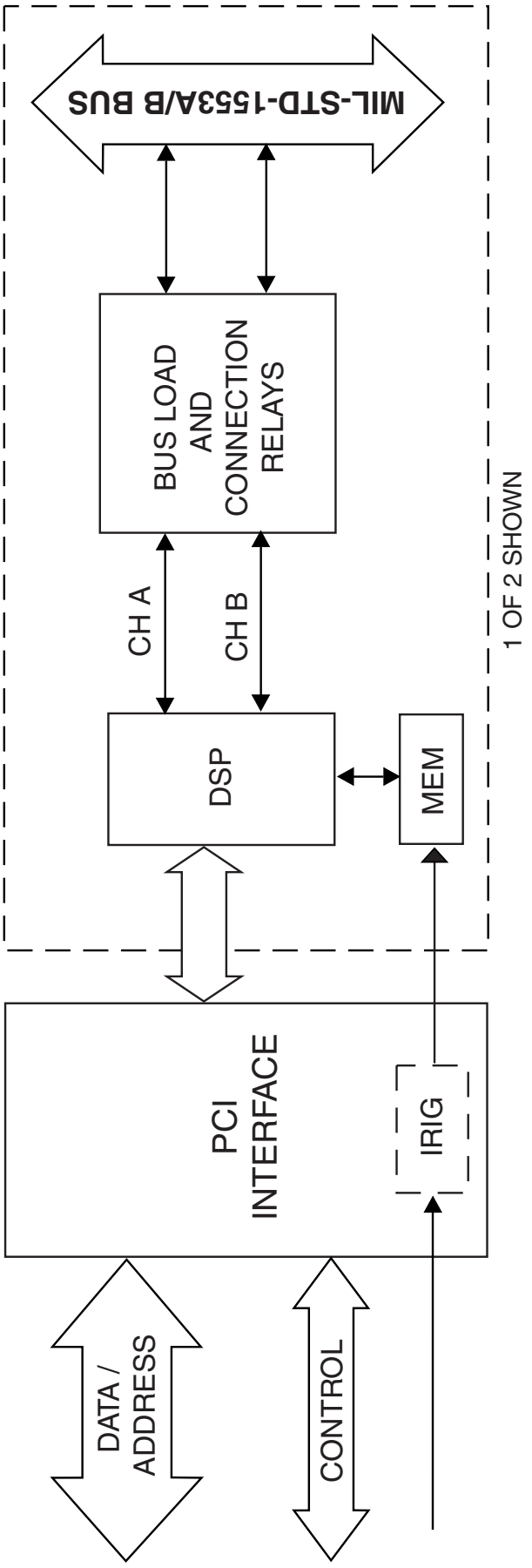


FIGURE 1. BU-65570T / BU-65572T BLOCK DIAGRAM

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

TABLE 1. BU-65570T/72T REQUIREMENTS AND CAPABILITIES

HARDWARE REQUIREMENTS
- A CompactPCI compatible Backplane with an 80386 or better host CPU. One available CompactPCI Peripheral Slot in the Backplane.

SOFTWARE REQUIREMENTS
- Windows® 95/98/2000, Windows NT®
- DDC's BU-65570T/72T software diskettes/CD-ROM

AVAILABLE OPERATIONS FOR EACH CHANNEL
- Tests and Simulates MIL-STD-1553 BC and all 31 RT's
- Provides independent Monitor mode operation
- Error injection and detection

TABLE 2. BU-65570T/72T SPECIFICATIONS

PARAMETER	MIN	TYP	MAX	UNITS
ABSOLUTE MAXIMUM RATINGS				
+5 V Supply Voltage	-0.3		7.0	V
RECEIVER				
Threshold Voltage, Transformer Coupled, Measured on Stub		0.56		Vp-p
TRANSMITTER				
Differential Output Voltage				
BU-65570T (See NOTE 5)	18	20	27	VP-P
BU-65572T (See NOTES 5 and 6)	0		21.5	VP-P
POWER SUPPLY REQUIREMENTS PER 1553 BUS				
Voltages/Tolerances				
• + 5 V	4.75		5.5	V
Current Drain @ + 5.0 V				
• Idle	220		240	mA
• 50% Transmitter Duty Cycle	720		800	mA
• 100% Transmitter Duty Cycle (See NOTE 1)	870		1000	mA
1553 MESSAGE TIMING				
RT Response Time (See NOTE 2)		10		µsec
BC Intermessage Gap (See NOTE 3)	25			µsec
BC/RT/MT Response Timeout (See NOTE 4)	2		29	µsec
Transmitter Watchdog Timeout		668		µsec
MT Minimum gap for capture	4			µsec
THERMAL				
• BU-65570T/72T Operating Inlet Air Temperature (1 m/s airflow)	0		+55	°C
Storage Temperature	-40		+85	°C
PHYSICAL CHARACTERISTICS				
Size "3U"	6.30(W) x 3.94(H) (160 x 100)			in (mm)
Weight	7.1 203			oz (g)

NOTES:
1. 100% Duty Cycle at MAX Transmit Amplitude [28 V p-p]
2. This time assumes that this card is not emulating BC.
3. This hardware time is enforced by the firmware. If an attempt to reduce this time is made, the time will be stretched to 25 µsec.
4. This time is programmable from 2 µsec to 29 µsec in 1 µsec increments.
5. Transformer Coupled, Measured on Stub
6. Programmable in 1024 steps, within approximate range listed.

GENERAL

DDC's next generation CompactPCI Tester/Simulator, the BU-65570T/72T simulates a MIL-STD-1553 Bus Controller (BC), multiple (up to 31) Remote Terminals (RTs), and an intelligent Monitor (MT) simultaneously on both 1553 buses.

The cards support the PCI "Plug and Play" installation and simplifies the installation of it into a CompactPCI compatible Backplane.

Full error detection features are provided in all modes of operation. In addition, user specified errors - including bit count, Manchester II errors - may be injected in BC and any of the emulated RT modes.

Operational characteristics of the BU-65570T/72T include variable output voltage level, bus termination, and coupling configuration which are all software controllable using functions provided in the software library.

TABLE 3. J1 - DISCRETE I/O

PIN	DESCRIPTION	PIN	DESCRIPTION
1	Chassis Ground	21	Chassis Ground
2	IRIG-In	22	IRIG Mod
3	Chassis Ground	23	Chassis Ground
4	Channel 2 BC Trig Out	24	Channel 2 Monitor Trig In
5	Channel 2 BC Trig In	25	Channel 2 Monitor Trig Out
6	Chassis Ground	26	Chassis Ground
7	Channel 2 Discrete Out 5	27	Channel 2 Discrete Out 7
8	Channel 2 Discrete Out 6	28	Channel 2 Discrete Out 8
9	Chassis Ground	29	Chassis Ground
10	Chassis Ground	30	Chassis Ground
11	+5 Volts	31	+5 Volts
12	+5 Volts	32	+5 Volts
13	Chassis Ground	33	Chassis Ground
14	Chassis Ground	34	Chassis Ground
15	Channel 1 Discrete Out 1	35	Channel 1 Discrete Out 3
16	Channel 1 Discrete Out 2	36	Channel 1 Discrete Out 4
17	Chassis Ground	37	Chassis Ground
18	Channel 1 BC Trig Out	38	Channel 1 Monitor Trig In
19	Channel 1 BC Trig In	39	Channel 1 Monitor Trig Out
20	Chassis Ground	40	Chassis Ground

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

TABLE 4. J2 - 1553 BUS 'D' CONNECTOR	
PIN	DESCRIPTION
1	$\overline{\text{CH1_TXA}}$
2	CH1_TXA
3	CH1_TXB
4	$\overline{\text{CH1_TXB}}$
5	GND (connected to shell)
6	$\overline{\text{CH2_TXA}}$
7	CH2_TXA
8	CH2_TXB
9	$\overline{\text{CH2_TXB}}$

BUS CONTROLLER MODE

The BU-65570T/72T Bus Controller supports all MIL-STD-1553B message formats. Up to 1024 unique receive, transmit, mode code, and RT to RT messages may be defined at one time for each of the installed channels.

Programmable attributes within a message include time to next message, bus (channel A or channel B), intermessage routines, and injected error. The time to next message defines the time from the start of the present message to the start of the next message. The time to next message is programmable up to 65,535 µsec in 1 µsec increments.

MINOR AND MAJOR FRAMES

The execution of messages is controlled by a message list referred to as a frame. The frame specifies the contents and timing of complete communication runs by the BC. Each entry in the frame is either a reference to a message or a special frame symbol. The entire frame is referred to as a major frame and is divided into minor frames each of equal time duration.

The minor frame time is based on a programmable 32-bit counter with 1 µsec resolution. The BU-65570T/72T supports major frames of up to 1000 messages per installed channel, with a period of up to 72 minutes.

BC ERROR INJECTION

Error conditions may be injected on a message by message basis. The BU-65570T/72T supports three categories of injected BC Errors: length errors, encoding errors, and gap errors. Length errors include both word count errors and bit count errors.

Word Counts of -32 to +1 words may be programmed. Bit counts of +3, +2, +1, -3, -2, or -1 bit may be programmed on any word within the message.

Encoding errors are implemented through the use of two simple yet powerful mechanisms for modifying the output of the card's Manchester encoder. The two modifying functions are glitch and inverse. A glitch error will force the output of the encoder to an idle bus condition for the specified period of time. An inverse error will invert the output of the encoder for the specified period of time. The word number, starting time, and width specify the placement of the error. The error may be placed in any word within the message and its starting time may be programmed in 500 nsec increments with a width of up to 3 µsec. This error injection is capable of generating a host of errors including invalid sync patterns, parity errors, and Manchester bi-phase errors.

A gap of 3, 4, or 5 µsec (measured mid-parity crossing to mid-sync crossing) may be inserted between any two words in a message allowing a "dead time" gap between words of 1, 2 or 3 µsec.

INSERTING ASYNCHRONOUS MESSAGES

The BU-65570T/72T allows an asynchronous message to be inserted while the card runs. The inserted message will be executed upon completion of the current message. The user will define all asynchronous messages after the End-Of-Major frame symbol and insert the message into the running frame by calling the insert message routine. The hardware does all of the work.

BC INTERMESSAGE ROUTINES

Upon completion of a BC message, the BU-65570T/72T's on-board processor will execute up to 2 intermessage routines. Intermessage routines are used to execute automatic retries on failed messages and other "end of message" functions. TABLE 5 provides a summary of the card's intermessage routines.

RESPONSE TIMEOUT

The BU-65570T/72T BC, RT's, and MT support programmable response timeout values ranging from 2 to 29 µsecs in 1 µsec increments.

RT MODE

The BU-65570T/72T can simulate the operation of 31 unique remote terminals (RTs) plus a broadcast address for each of the installed channels. The card maintains 31 independent "last status" and "last command" words allowing for full support of transmit last command and transmit status mode commands. The cards support full RT command illegalization for each transmit or receive message based on RT address and sub-address, also individual mode commands may be illegalized.

RT ERROR INJECTION

Error conditions may be injected on an individual RT basis. The BU-65570T/72T supports five categories of injected RT errors: length errors, encoding errors, gap errors, status address errors, and response errors. Length errors include both word count errors and bit count errors. Word count errors of -32 to +1 words may be programmed. Bit count errors of +3, +2, +1, -3, -2, or -1 bit may be programmed on any word within the message.

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

Encoding errors are implemented through the use of two simple yet powerful mechanisms for modifying the output of the BU-65570T/72T's Manchester encoder. The two modifying functions are glitch and inverse. A glitch forces the output of the encoder to an idle bus condition for the specified period of time. An inverse inverts the output of the encoder for the specified period of time. The word number, starting time, and width specify the placement of this error. The error may be placed in any word within the message. The starting time is programmed in 500 nsec increments from the beginning of the specified word.

The width of the error is specified in 50 nsec increments up to 3 µsec. This error injection scheme lends itself to generating a host of errors including invalid sync patterns, parity errors, and Manchester bi-phase errors.

A gap of 3, 4, or 5 µsec (measured mid parity crossing to mid-sync crossing) may be inserted between any two words in a message. This allows for a "dead time" gap between words of 1, 2, or 3 µsec. A status address error may be injected in which the RT responds with a status word containing an RT address, which does not match the terminal's RT address. The RT may be programmed to respond with any value from zero to 31 in its status response.

The BU-65570T/72T supports three types of response errors: no response, a late response, or a response on the wrong bus. No response errors may be programmed for a single bus (Bus A or Bus B) or for both buses. Injecting a no response error on one bus provides a simple mechanism for testing bus controller retry conditions. A late response may be programmed in the range of 12 to 30 µsecs in 1 µsec increments.

RT INTERMESSAGE ROUTINES

The RT section of the BU-65570T/72T also supports intermessage routines. Completed RT messages trigger the card's onboard processor to execute two intermessage routines. The data table that was used by the RT for a given message specifies which intermessage routines will be executed. Refer to TABLE 5 for a summary of the card's intermessage routines.

TABLE 5. BC/RT INTERMESSAGE ROUTINES

NO OPERATION
RETRY CURRENT MESSAGE ON ALTERNATE BUS *
RETRY CURRENT MESSAGE AND REMAIN ON ALTERNATE BUS *
RETRY ON SAME BUS *
INTERRUPT ON END OF MESSAGE
INTERRUPT ON FRAME SYMBOL *
SET SERVICE REQUEST BIT IN STATUS **
RESET SERVICE REQUEST BIT IN STATUS **
INTERRUPT AFTER ACCESSING TX/RX DATA TABLE
INTERRUPT AFTER MODE COMMAND **
INTERRUPT AFTER TX/RX COMMAND TEMPLATE MATCH **
INTERRUPT AFTER MODE COMMAND TEMPLATE MATCH **
TIME-TAG (STORE RTC IN A CIRCULAR QUEUE)
RETRY ON SAME BUS AND THEN ON ALTERNATE BUS *
SET OUTPUT TRIGGER
WAIT FOR INPUT TRIGGER *
NO RESPONSE ON BOTH BUSES **
SET BUSY BIT IN STATUS **
RESET BUSY BIT IN STATUS **
SKIP NEXT MESSAGE *
SKIP NEXT MESSAGE ONCE *
BLOCK_DATA_BC *
BLOCK_DATA_RT **
SET_DISCRETE_0
RESET_DISCRETE_0
SET_DISCRETE_1
RESET_DISCRETE_1
SET_DISCRETE_2
RESET_DISCRETE_2
SET_DISCRETE_3
RESET_DISCRETE_3

* Applicable to BC only.
** Applicable to RT only.

BC/RT DATA TABLES

For each of the installed 1553 channels, the BU-65570T/72T maintains 1000 data tables within the shared RAM. Each data table can have 32 words in length. These data tables are common to both BC and RT. Internal lookup tables map each RT address, T/R, sub-address combination (RT mode) and message number (BC mode) to a chosen data table. Data tables may be read or written to in real time by the user and may be either single or double buffered. Double buffering can be used to avoid the memory access contention that occurs when the PC's application and the 1553 bus access data tables simultaneously. The cards provide an optional block data mode in which the data table number associated with a given BC or RT message is incremented after completion of the message. The block data mode is implemented as a circular data structure. Each BC message and RT command (RT address, T/R, and sub-address) has three data table numbers associated with it: first, last, and current. The current data table number is incremented after completion of a message until the value of 'last' is reached, then the current table number rolls over to the value of first. The incrementing of the current data table is done through the use of an intermessage routine.

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

MONITOR MODE

The BU-65570T/72T contains an independent message monitor for each bus with the ability to filter messages in real time. Monitor selection or filtering is performed through the use of a lookup table based on the RT address, T/R bit, and sub-address of command words. Monitored messages are stored in the shared RAM on the BU-65570T/72T. Each entry in the monitor buffer contains a header followed by a variable number of data words. Contained with the message header are the receive/transmit command(s), receive/transmit status, message format, Bus (A or B), a capture flag, word count (actual number of words in the message), a detected error field, and a 32-bit time tag (1 µsec resolution).

The transfer of the messages from the card's circular buffer to the host memory/disk is determined by the capture flag, which is set upon detection of a predefined event. Capture events include immediate, command template match, exception, or trigger. The command template event is based on a 16-bit command word with a 16-bit mask. Exception events may be programmed for any exception: invalid command, invalid data, invalid status, gap preceding data, response time error, wrong RT address error, status set condition or an illegal command. The trigger event uses the input pin on the 9-pin D-type connector as a trigger input.

Direct Memory Access (DMA) transfers are possible using the PCI Master Mode. The Monitor mode can independently transfer monitored data to RAM in PCI address space without host CPU intervention. This improves the efficiency of both the host and the cards.

INTERRUPTS

For each of the installed channels, both the BC/RT and the Monitor may request interrupts on a common output to the CompactPCI Backplane. The hardware interrupt level used by the BU-65570T/72T is selected by the Plug-and-Play capability of the CompactPCI Backplane and BIOS. An important aspect of PCI interrupts is that the cards can share an interrupt for all the buses.

BC INTERRUPT GENERATION

BC interrupts may be enabled by a global interrupt mask for successful messages, communication errors, status set conditions, or on selected frame symbols (skip, break point, major frame, and minor frame symbols). The criteria for a status set condition are programmed globally through the use of a status mask. The status mask allows any of the 16 bits within the RT status word to be ignored. The status mask affects the generation of interrupts as well as the detected error field that is stored in the message structure.

BC interrupts are issued by the intermessage routines associated with messages allowing for selective interrupt generation on a message by message basis. A two-word vector is pushed onto a circular queue for each interrupt request and is transparent to the user. The queue holds up to 64 interrupt vectors; thus, the host computer doesn't need to immediately recognize the interrupt request.

RT INTERRUPT GENERATION

RT interrupts may be enabled by a global interrupt mask for transmit/receive messages with no message error, mode commands with no message error, transmit/receive messages with the message error bit set, or mode commands with the message error bit set.

RT interrupts are issued by intermessage routines associated with data tables allowing for selective interrupt generation on a message by message basis. A two-word vector is pushed onto a circular queue for each interrupt and is transparent to the user. The queue holds 64 interrupt vectors; thus, the host computer doesn't need to immediately recognize the interrupt request.

MONITOR INTERRUPTS

Monitor interrupts may be generated after each message is received or after one third of the monitor's circular buffer has been filled (approximately 4K words). This allows for either real-time analysis or mass collection/storage of monitored data.

OTHER FEATURES

VARIABLE AMPLITUDE TRANSCEIVER

The BU-65572T provides variable amplitude transceivers for each 1553 channel installed on the card. The output of the variable transceiver is software controllable in the range of 0 volts to 21.5 volts. This range is covered in 1024 steps. The user application can modify this amplitude in realtime by calling the appropriate library function.

The transceiver outputs can be individually controlled for each of the installed channels, while the two transceivers for a given channel will be varied simultaneously. Functions for controlling the transceiver outputs are added to the 'C' library that is provided with the card.

IRIG-B SUPPORT

Inter-Range Instrument Group (IRIG) Standard 200-95 is provided in the BU-65570T/72T cards. Using this standard with the 1553 tester/simulator board enables all messages to be tagged with time-of-day information, allowing accurate correlation between messaging on the 1553 bus and other hardware and software events that are occurring in the system. This is especially useful when trying to identify the cause of a system problem that may be directly related to message data sent over the

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

1553 bus. The IRIG-B timestamp for each of the installed channels may be individually selected. This allows one channel to use the internal 32 bit Time-Tag clock, while another channel will stamp the messages with the IRIG-B time. The IRIG-B timestamp, if enabled, will be combined with the internal timestamp to provide µsec accuracy.

MONITOR DMA TO HOST BUFFER

The BU-65570T/72T is fully compliant to the PCI standards for both Target and Master applications. As a PCI master, the cards are capable of performing DMA block data transfers from the monitor buffers on the card to a user buffer in the host memory. This will allow the transfer of data without interrupting the host processor, enabling it to perform other more important tasks.

PCI PLUG AND PLAY

PCI Plug and Play is provided on the BU-65570T/72T. Both BIOS and the operating system can determine the memory and interrupt resources to be used by this card. The memory is selected from the system memory above 1 Meg. The system will generally select unused locations starting at the 4 Gig address and work its way down. The interrupts for PCI are sharable. This means that if there are no free interrupt resources, the operating system can now assign an interrupt that is used by another PCI card. The driver will handle all of the elements of sharing.

SOFTWARE CONTROL OF BUS CONFIGURATION

The BU-65570T/65572T is capable of being connected to the bus as either Transformer Coupled or Direct Coupled. Transformer coupling enables the card to be connected to the 1553 bus with a long stub of up to 20 feet. Direct coupling requires that the card be connected to the 1553 bus by a stub of no more than 1 foot. The coupling configuration is dynamically configurable via functions provided in the 'C' library. Relays on the board are used to select the correct signal path direct or transformer coupled for each of the installed channels. This card also has the capability of modifying the bus termination to one of three different loads. If the bus termination is set to None, then the effective impedance is infinite ohms. If the bus is set to Full, then the bus termination will be 37.5 ohms. A setting of Half will cause a bus termination of 75.0 ohms. This control makes it quite simple to connect the cards directly to another 1553 device without the need for an external load. As with the bus coupling mode, the bus termination mode is dynamically configurable via software functions provided in the 'C' library which control relays for each installed channel.

DISCRETE OUTPUTS

This Tester/Simulator design includes four discrete outputs per channel. These four outputs are set to logic '1' and cleared to logic '0' by use of eight new intermessage routines. The formats of the routine names are SET_DISCRETE_X and RESET_DISCRETE_X. These discrete outputs may be used for signaling external equipment when a specific event or message occurs. The intermessage routine can be attached to any message, data table, or frame symbol providing a wide variety of debug possibilities.

SOFTWARE SUPPORT

The BU-65570T/72T is provided with a 32-bit 'C' library that supports all of the functionality of the previous versions of the DDC Tester/Simulator cards. This will make re-targeting of existing software as simple as a re-compile. The new library also contains new functions that will enable full use of all of the new hardware functionality such as IRIG-B, Monitor DMA, Dynamic Bus Configuration, and Dynamic Bus Loading.

A full function Menu is also supplied with the cards. This new menu runs on Windows® 95/98/2000 and Windows NT®, and provides access to all hardware functions. The menu is an extension of the existing Tester/Simulator menu and is completely compatible with setup and stack files that are used with the older version.

This new menu has the ability to setup and run multiple Tester/Simulator channels simultaneously. This allows the menu to be run on both channels of a BU-65572T and at the same time run other tester/simulator channels that are located on other testing cards that are installed in the system.

Each installed channel is accessed through a different document view allowing complete separation of the setup and run parameters.

Additionally, there is a 'Run All' button that allows all installed channels to be started simultaneously, which provides very close synchronization of the local 32-bit time tag counters for each channel.

A new feature added to the menu is a “C code Generate” button that will convert an entire setup to editable/compilable ‘C’ source code. This will allow the major portion of Setup to be done in the menu, and any data reduction routines can be added to the generated source. The final product can then be compiled under any ‘C’ compiler and run.

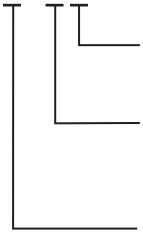
Additional support is provided for LabVIEW application development.

Drivers for the cards are provided for Windows 95/98/2000, and Windows NT running on an Intel® platform in a CompactPCI chassis.

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

ORDERING INFORMATION

BU-6557X T X-300 Simulator and Tester CompactPCI Card



Temperature Range:
3 = 0 to +55 °C

Board Configuration:
1 = One Dual Redundant 1553 Channel
2 = Two Dual Redundant 1553 Channels

Transceiver Configuration:
0 = Fixed Bus Voltage Output
2 = Variable Bus Voltage Output

Software Included:

BUS-69068
32-bit Windows DLL/Driver supports Microsoft 32-bit C/C++ Compilers, and National Instruments Laboratories development.

BUS-69065
32-bit Windows Menu Applications. Supports BC, RT, and MT, and Replay Functionality.

Note: 1) The above products contain tin-lead solder.

STANDARD DDC PROCESSING FOR DISCRETE MODULES/PC BOARD ASSEMBLIES		
TEST	**METHOD(S)**	**CONDITION(S)**
INSPECTION / WORKMANSHIP	IPC-A-610	Class 3
ELECTRICAL TEST	DDC ATP	—

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

PICMG and the PICMG logo [and/or CompactPCI and the CompactPCI logo, and/or AdvancedTCA and the AdvancedTCA logo] are registered trademarks of the PCI Industrial Computers Manufacturers Group.

The information in this application note is believed to be accurate; however, no responsibility is assumed by Data Device Corporation for its use, and no license or rights are granted by implication or otherwise in connection therewith. Specifications are subject to change without notice.

Please visit our web site at **www.ddc-web.com** for the latest information.

DDC®
©Data Device Corporation
105 Wilbur Place, Bohemia, New York 11716-2426

For Technical Support - 1-800-DDC-5757 ext. 7771

Headquarters, N.Y., U.S.A. -Tel: (631) 567-5600, Fax: (631) 567-7358
United Kingdom - Tel: +44-(0)1635-811140, Fax: +44-(0)1635-32264
France - Tel: +33-(0)1-41-16-3424, Fax: +33-(0)1-41-16-3425
Germany - Tel: +49-(0)89-15 00 12-11, Fax: +49-(0)89-15 00 12-22
Japan - Tel: +81-(0)3-3814-7688, Fax: +81-(0)3-3814-7689
World Wide Web - http://www.ddc-web.com

DATA DEVICE CORPORATION
REGISTERED TO ISO 9001:2000
FILE NO. A5976

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

RECORD OF CHANGE

For BU-65570/72T Data Sheet

Revision	Date	Pages	Description
H	8/09	3	1 m/s airflow requirement added to Operating Temperature in Table 2.

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

Artisan Technology Group is an independent supplier of quality pre-owned equipment

Gold-standard solutions

Extend the life of your critical industrial, commercial, and military systems with our superior service and support.

We buy equipment

Planning to upgrade your current equipment? Have surplus equipment taking up shelf space? We'll give it a new home.

Learn more!

Visit us at **artisantg.com** for more info on price quotes, drivers, technical specifications, manuals, and documentation.

Artisan Scientific Corporation dba Artisan Technology Group is not an affiliate, representative, or authorized distributor for any manufacturer listed herein.

We're here to make your life easier. How can we help you today?

(217) 352-9330 | sales@artisantg.com | artisantg.com

ARTISAN® TECHNOLOGY GROUP