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Find the *DDC BUS-65515II* at our website: *Click HERE*

DDC
ILC DATA DEVICE CORPORATION®

BUS-65515

MIL-STD-1553 BC/RTU/MT IBM PC® INTERFACE UNIT



DESCRIPTION

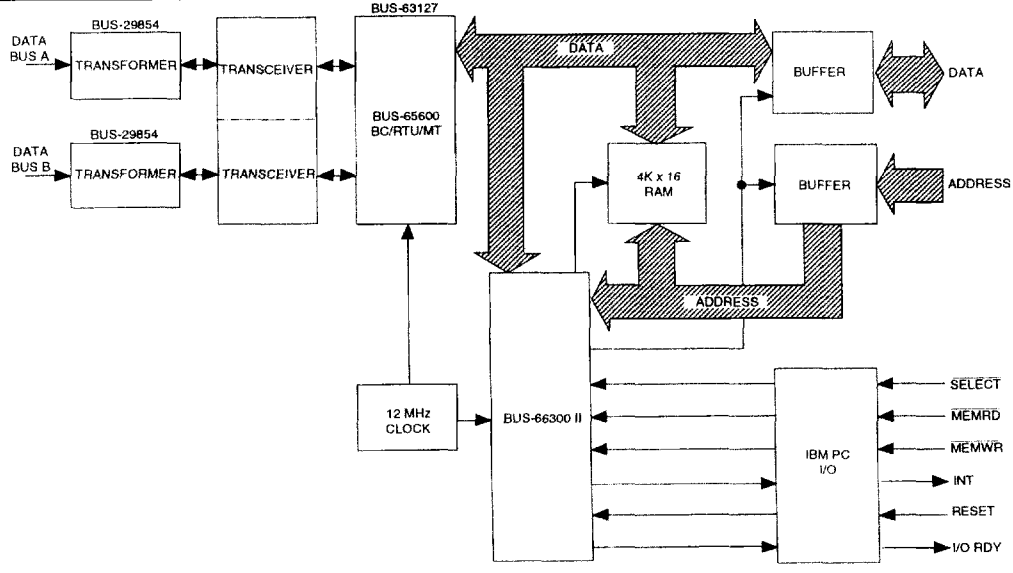
The BUS-65515 provides full, intelligent interfacing between the serial dual redundant MIL-STD-1553B Data Bus and the IBM PC Bus. Software controls the BUS-65515's operation as either a 1553 Bus Controller (BC), Remote Terminal Unit (RTU), or Bus Monitor (MT).

The BUS-65515 is packaged on one full size IBM PC printed circuit card. Its onboard 4K x 16 dual access RAM is double buffered, preventing partially updated data from being read by the CPU or transmitted to the 1553 Data Bus.

Additional features include a dynamic wrap-around Built-In-Test (BIT), onboard Time Tag Counter, jumper programmable interrupt level and BUS-65515 RTU address selection. The BUS-65515 supports all dual redundant mode codes and message formats. Its full compliance with MIL-STD-1553B makes it an excellent choice for dynamic simulation applications.

FEATURES

- ***IBM PC Interface with Intelligent BC/RTU/MT***
- ***Standard IBM PC Card Size***
- ***Supports All 1553B Dual Redundant Message Formats and Mode Codes***
- ***Dynamic Wraparound Built-In-Test***
- ***On-Board Time Tag Counter***
- ***Dedicated 4K x 16 Dual Access RAM***
- ***Double Buffer Memory Architecture to Insure Data Integrity***
- ***Memory Mapped Interface***
- ***Software Programmable RTU Address and Data Block Allocations***
- ***Jumper Programmable Interrupt Level Selection***

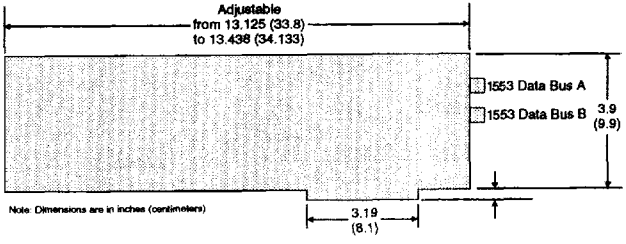


BUS-65515 BLOCK DIAGRAM

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BUS-65515 PIN FUNCTIONS		
PIN	SIGNAL NAME	DESCRIPTION
A1	I/O CHCK	Not Used
A2	D7	Data Bit 7 (MSB)
A3	D6	Data Bit 6
A4	D5	Data Bit 5
A5	D4	Data Bit 4
A6	D3	Data Bit 3
A7	D2	Data Bit 2
A8	D1	Data Bit 1
A9	D0	Data Bit 0
A10	I/O CHRDY	I/O Ready
A11	AEN	Address Enable
A12	A19	Address Bit 19 (MSB)
A13	A18	Address Bit 18
A14	A17	Address Bit 17
A15	A16	Address Bit 16
A16	A15	Address Bit 15
A17	A14	Address Bit 14
A18	A13	Address Bit 13
A19	A12	Address Bit 12
A20	A11	Address Bit 11
A21	A10	Address Bit 10
A22	A9	Address Bit 9
A23	A8	Address Bit 8
A24	A7	Address Bit 7
A25	A6	Address Bit 6
A26	A5	Address Bit 5
A27	A4	Address Bit 4
A28	A3	Address Bit 3
A29	A2	Address Bit 2
A30	A1	Address Bit 1
A31	A0	Address Bit 0

BUS-65515 PIN FUNCTIONS (CONT.)		
PIN	SIGNAL NAME	DESCRIPTION
B1	GND	Signal Ground
B2	RESET	Reset
B3	+5V	Logic Power Supply
B4	IRQ2	Interrupt Request 2
B5	-5V	Not Used
B6	DRQ2	Not Used
B7	-12V	System Power Supply
B8	NOT USED	Not Used
B9	+12V	System Power Supply
B10	GND	Signal Ground
B1 1	- MEMW	Memory Write
B12	- MEMR	Memory Read
B13	- IOW	Not Used
B14	- IOW	Not Used
B15	- DACK3	Not Used
B16	DRQ3	Not Used
B17	- DACK1	Not Used
B18	DRQ1	Not Used
B19	- DACK0	Not Used
B20	CLK	Not Used
B21	I RQ7	Interrupt Request 7
B22	IRQ6	Interrupt Request 6
B23	IRQ5	Interrupt Request 5
B24	IRQ4	Interrupt Request 4
B25	IRQ3	Interrupt Request3
B26	- DACK2	Not Used
B27	TIC	Not Used
B28	ALE	Address Latch Enable
B29	+ 5V	Logic Power Supply
B30	OSC	Not Used
B31	GND	Signal Ground



BUS-65515 MECHANICAL OUTLINE

ORDERING INFORMATION BUS-65515

Software

- Contact factory for free software P/N BUS-69052
- Contact PTI:

Phase Two Industries
4800 Great America Parkway
Suite 400
Santa Clara, CA 95054
Phone (408) 986-8801; Fax (408)986-0646
For driver and application software (PCX-AID-D):
Mr. Andy Werbach

J

H-ABR

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