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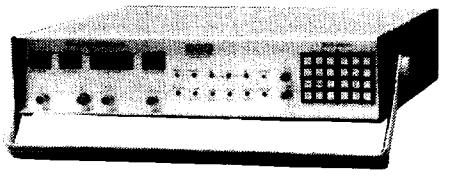
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Find the *DDC BUS-68005* at our website: *Click HERE*

DDC
ILC DATA DEVICE CORPORATION®

BUS-68005

MIL-STD-1553 DATA BUS EXERCISER



FULL DATA SHEET AVAILABLE

IMPROVED POWER SUPPLY CONTACT FACTORY

DESCRIPTION

The BUS-68005 is a low cost portable instrument for testing and troubleshooting MIL-STD-1553A and 1553B systems. It provides full function simulation of Remote Terminal Unit (RTU) and Bus Controller (BC) operation. The BUS-68005 implements the protocol tests of the proposed SAE AE-9A Production Test Plan for Remote Terminals. These tests include RT Address, Word Count, Subaddress, Error Injection, Dual-Redundant Operation, Mode Commands, Status Word, Broadcast Messages, and RT to RT Transfers. BUS-68005 offers simple and flexible operation including either local programming via the front panel 24 pad keyboard and 10 character alphanumeric display, or remote programming via an 8 bit parallel I/O port, RS-232, or IEEE-488 interfaces. The IEEE-488 interface is a Direct Memory Access (DMA) port, allowing for real-time operation. Additional features include error generation and error detection, single, repeat, or halt-on error operation modes, variable response time, variable message gap, variable transmitter amplitude, a real-time monitor port, programmable response time-out, and built-in self test. Its compact size, 3.47" x 14.5" x 17.0", and price/performance makes it a leader for test applications requiring BC and RTU simulation.

FEATURES

- *IMPLEMENTS PROTOCOL TESTS OF SAE AE-9 PROPOSED PRODUCTION TEST PLAN*
- *1553A OR 1553B SYSTEM TESTS*
- *SIMULATES:*
  BUS CONTROLLER
  MULTIPLE REMOTE TERMINALS
- *REAL-TIME MONITOR OUTPUT*
- *ALL PARAMETERS MANUAL AND REMOTE PROGRAMMABLE:*
  KEYBOARD/DISPLAY
  IEEE-488 WITH DMA
  RS-232
  8 BIT PARALLEL
- *ERROR GENERATION/DETECTION*
- *VARIABLE TRANSMITTER*
- *LOW COST*

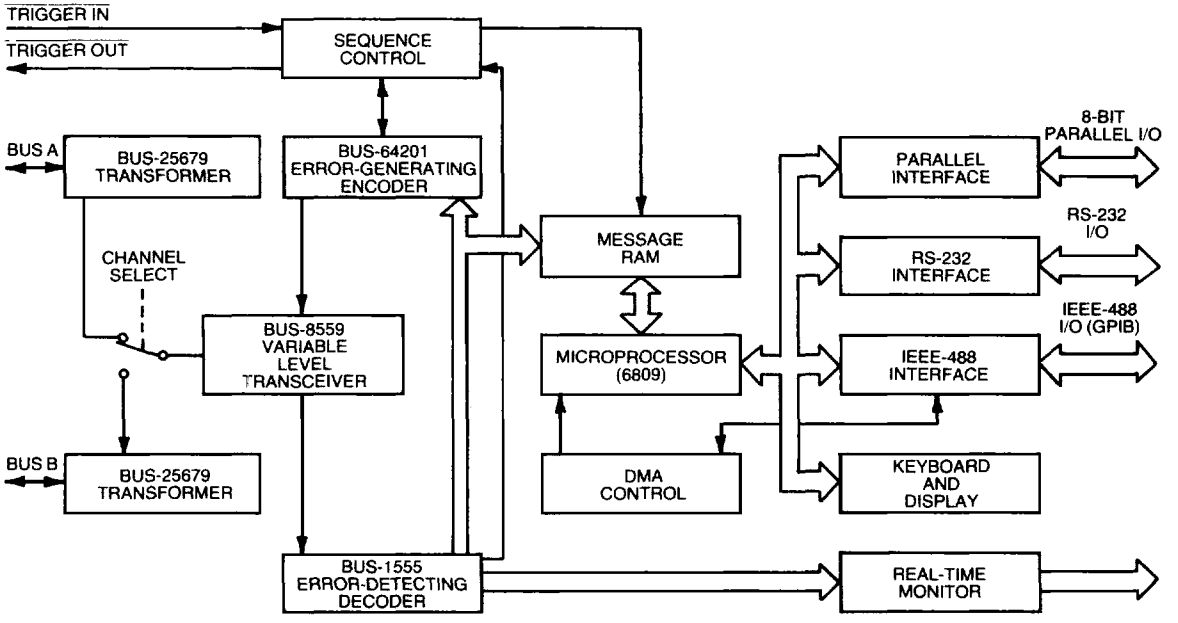
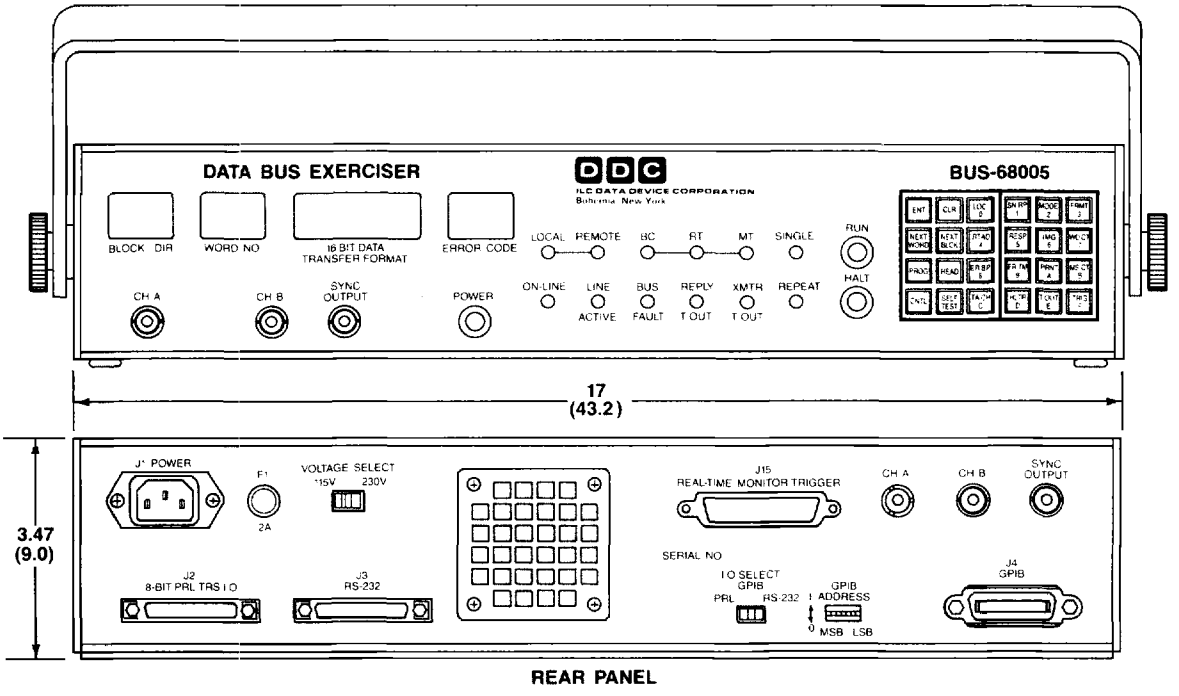


FIGURE 1. BUS-68005 BLOCK DIAGRAM

	TABLE 14. MANUAL SWITCH/CONTROL FUNCTIONS	
SWITCH	**FUNCTION**	**REMARKS**
RUN	Starts the 1553 data bus message transfer cycle.	The ON-LINE indicator lights up and the display reads BUSY.
HALT	Stops the 1553 data bus message transfer cycle.	The ON-LINE indicator shuts off and the display reads HALT.
I/O SELECT	Selects I/O interface as either Parallel, IEEE-488 or RS-232.	Rear panel switch.
GPIB ADDRESS	Selects the Exerciser's GPIB (IEEE-488) address.	Address has 5 bits. Rear panel switch.
POWER	Turns on the internal power supply.	An initialization and self-test sequence is performed immediately after power turn-on.
VOLTAGE SELECT	Selects 115V or 230VAC line voltage.	Rear panel switch.

DIMENSIONS IN INCHES (CENTIMETERS)
17 x 14.5 x 3.47 (43.2 x 37 x 9.0)

FRONT PANEL



REAR PANEL

FIGURE 6. MECHANICAL OUTLINE

ORDERING INFORMATION

BUS-68005

Mating Connector for J15 Amphenol 17-10500-1-390 included.

1553 Mating Connector Trompeter PL75-47 (2 included).

Also included are 2 rack adapters and hardware for full-rack mounting.

C-ABR

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