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>> CHEETAH VME



CHEETAH VME

Cheetah is a high performance digital signal processing board optimized for high-bandwidth, low-latency digital signal processing applications. It is an excellent match for accelerating frequency-domain algorithms in applications requiring state-of-the-art performance. The VME form factor provides excellent integration with VME backplane systems. The input and output PMC daughtercards allow for high speed data movement in a variety of formats without going across the host bus.

COMPUTING

- Software Support .....Driver support includes VX Works, MCOS and Windows NT 4.0. DRS-TS software API and System Development Framework (SDF) included. Also supported is a large class of libraries and DSP functions such as FFTs, fast convolutions, polyphase filters, and matched filters.
- Processing Speed: .....120 MHz clock rate
- PCI Signaling: .....64-Bit 66 MHz at 3.3V (transfers at 60 MHz)
- Performance (throughput)*: .....1K CFFT in 9.8 microseconds .....256K CFFT in 3.3 milliseconds .....*Performance can be improved by stacking
- The Cheetah processing engine can perform windowed FFTs at the following maximum achievable rates:

  FFTs with complex inputs:
  - 16 to 256 points .....240 Complex MSPS
  - 512 to 64K points .....120 Complex MSPS
  - 128K to 256K points .....80 Complex MSPS

  FFTs with real inputs:
  - 32 to 512 points .....240 Real MSPS
  - 1K to 128K points .....160 Real MSPS
  - 256K to 512K points .....120 Real MSPS

POWER

- Max current at 5V 6A
- Max current at 3.3V 6A

ENVIRONMENTAL

- Operating temperature .....0° to 50°C
- Cooling .....500 LFM minimum

Also available in PMC and PCI versions. Consult factory for details.

>> CHAMELEON II VME



CHAMELEON II VME

The heart of the Chameleon II VME board consists of three Xilinx® Virtex® -II 6000-4 FPGAs, providing over eighteen million reconfigurable system gates and 432 18x18 hardware multipliers the user can apply to the application. Connected to FPGAs A, B, and C are twelve independently addressable ZBT ram blocks shared between processors. The FPGAs on the Chameleon II VME link together in a ring with dedicated 72-bit paths: A to B, B to C, and A to C. These FPGA busses can be clocked at rates up to 133 MHz.

The Chameleon II VME has three high-speed I/O daughtercard sites compatible with the industry's PCI Mezzanine Card (PMC) standard. By leveraging PMC, the effort to tailor the Chameleon II VME to specific I/O requirements decrease the driver integration instead of a full custom I/O daughtercard development task.

COMPUTING

- PCI Signaling .....64-Bit 66 MHz at 3.3V
- Processing Speed .....133 MHz clock rate
- Performance .....Programming dependent

ENVIRONMENTAL

- Operating temperature .....0°- 50° C
- Cooling .....500 LFM minimum

Also available in PMC version. Consult factory for details.

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