

Model I/O 612: Dual 24-Bit Parallel Input/Output Register

DESCRIPTION

The I/O 612 Parallel Input/Output Register is a single-width CAMAC module which contains two 24-bit registers to transfer data between the crate Dataway and an external device. The I/O 612's external data lines are bi-directional. When a register bit is cleared to zero, the corresponding external line can be read as input data from the external device.

Simultaneous input and output operation is possible using one register for input and the other for output. This simplifies both hardware and software requirements by eliminating the need to install and address independent modules.

Data outputs are available on two 31 pin front panel connectors, one for each register, and 48 front panel LED's provide visual display of both registers.

Data outputs are negative true, open collector drivers capable of sinking 37 milliamps at +5 volts. An external strobe signal for each register indicates when it has been overwritten.

APPLICATION

Transfers up to 48 bits of data between the CAMAC Dataway and an external device.

TECHNICAL SPECIFICATIONS

ELECTRICAL

Power

requirements: +6 volts @ 1A

Outputs: Open collector TTL

1 sink = 37 milliamps

Strobe output pulse width equal to Dataway S1

FRONT

PANEL

Connectors: Two Cannon 2DA-31 P

Display: LEDs for register outputs

CAMAC COMMANDS

Command

Action

0 Response

F(0)A(0) Read Register 1 onto Read lines 1

F(0)A(1) Read Register 2 onto Read lines 1

F(16)A(0) Overwrite into Register 1 from
Dataway 1

F(16)A(1) Overwrite into Register 2 from
Dataway 1

MECHANICAL

Single-width CAMAC module, fully shielded

Weight: 1 lb., 8 oz.

Operating

temperature: 0°C to 50°C at less than 80%
humidity

ORDERING INFORMATION

Mating Connectors: (2 ea.) P/NÁÍ€IEFDÍNE€JFÍ€IGHÁ