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Find the *Curtiss-Wright / DY-4 IP4-660* at our website: ***Click HERE***

IP4-660 A/D Module



See SVME/DMV-209 IP I/O System Data Sheet

News Release

The IP-16ADC implements 16 single-ended or 8 differential 16-bit analog-to-digital conversion channels.

Internal amplification and buffering are implemented with very low distortion differential amplifiers. Linearity is 0.001% typical. The internal reference has a temperature drift coefficient of 3ppm/ºC max. Internal calibration is accurate to better than ±2 LSB.

Input circuitry consists of a multiplexer group feeding a low-noise, low-distortion instrumentation amplifier. Input impedance is 1 GW. The multiplexers include built-in overvoltage protection to -40 V to +55 V.

Programmable interrupt logic is provided to generate an interrupt when conversion is complete. Start of conversion may be initiated by software command or by external trigger. An external pacer clock may be used to implement a semi-automatic data acquisition capability (interrupt or sample). Maximum aggregate conversion rate is 100 kHz.

Full scale input voltage ranges are 0 to +5, ±5, 0 to +10 and ±10 V.

FEATURES:

- 16-bit resolution
- 16 channels single ended or
- 8 channels differential
- +5 V, +10 V, ±V, ± V ranges

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