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PRODUCT

RELEASE NOTE

DPMC-700-100

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REVISION NOTICE

Rev	Title	Description of Changes	By	Affected Pages	Date
-	Product Release Note, DPMC-700-100	Production Release	PS		June 25, 2001
A	Product Release Note, DPMC-700-100	Update to PWB 31016-001	JT	3, 4	August 27, 2002

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1. Introduction

This document describes the options and features specific to the DPMC-700-100. Briefly this product consists of the following:

- 33/66 MHz/32-bit standard PCI Mezzanine Card, compliant with IEEE P1386.1
- Permedia®3 Graphics Accelerator
- DVI (PanelLink®) digital video output supporting resolutions up to 1280 x 1024@ 60Hz
- 32 Mbytes of SDRAM graphics memory
- Non Interlaced, Analogue RGB video output capable of providing resolutions up to 1920 x 1600 @ 60Hz refresh
- X11/OpenGL programming interface

1.2 Related Documents

901504-002 superceded by 901504-103	Circuit card assembly, DPMC-700-100 superceded by PD CCA DPMC-700-100/200 PWB 311016-001
809789	Hardware Users Manual, PMC-700

2. Requirements

2.1 Configuration Requirements

2.1.1 Hardware Requirements

- Mezzanine assembly components are plastic devices
- Mezzanine assembly is configured via 0 Ohm resistors

2.1.2 Software Requirements

- There are no software requirements for this product

2.2 Test Requirements

- For Acceptance Test Procedure refer to product BOM

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2.3 Other Requirements

- None

3. Compliance to Ruggedization Guidelines

Note: The table below details the compliance of the DPMC-700-100 to the environmental conditions listed, when it is installed on a Dy 4 Systems DMV level 100 basecard.

HEADING	REQUIREMENT FOR DMV LEVEL 100 CCA	COMPLIANT
BOARD TEMPERATURE	Operating -40ºC to 71ºC Card edge temp	YES
	Storage -55ºC to 85ºC	
HUMIDITY	Operating 0 to <100 % non-condensing	YES
	Storage 0 to <100% non-condensing	
VIBRATION	Sine 10G peak, 15-2KHz	YES
	Random 0.1g2/Hz, 15-2KHz	
SHOCK	40 g peak half sine 11ms	YES
CONFORMAL COAT	Yes	YES

4. Optional Configuration

4.1 Optional Devices

- No optional devices

4.2 Strapping Explanation

- No user configurable jumpers

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4.3.2 Circuit Card Modifications

- None

5. Memory Map

5.1 Local Memory Map

PCI Address Regions

Region	Address Space	Description	Size
Config	Configuration	PCI Configuration	256 bytes
Zero	Memory	Control. Registers	128 Kbytes
One	Memory	Memory Aperture One	64 Mbytes
Two	Memory	Memory Aperture Two	64 Mbytes
Three	I/O	Indirect Access I/O	16 bytes
ROM	Memory	Expansion ROM	64 Kbytes
VGA	Memory & I/O	VGA Access	---

Note: The PMC-700's PCI interface has seven base address regions as shown above. For a complete address map refer to the Permedia3 Reference Guide available from 3Dlabs.

5.2 VME Slave Memory Map

- Not applicable for this product

6. Firmware

- Not applicable for this product

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