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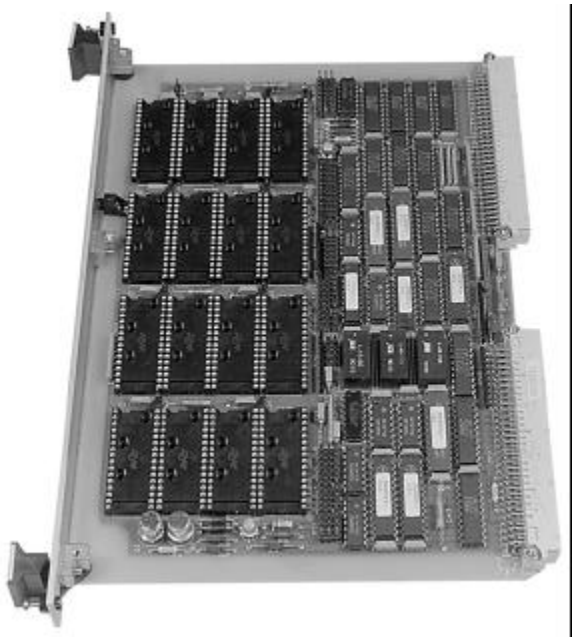
Find the *Curtiss-Wright / DY-4 SVME-537* at our website: *Click HERE*

SVME-537/DMV-536

Byte-Wide Memory Card

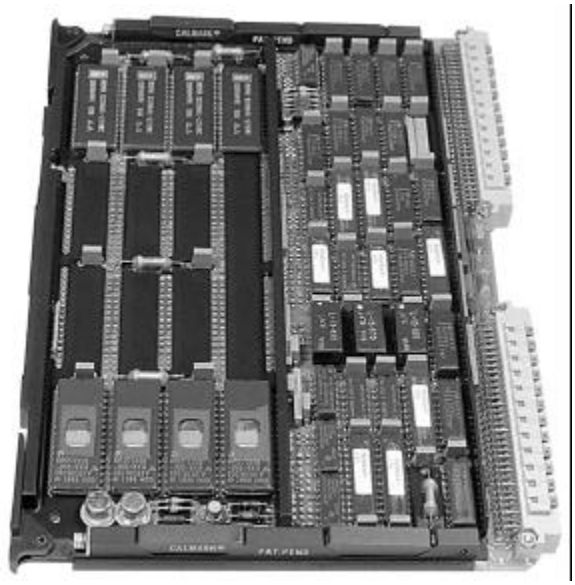
Features

- *16 32-pin JEDEC-standard device locations*
- *Designed to support:*
  - *up to 8M bytes of SRAM*
  - *up to 8M bytes of EEPROM*
  - *up to 16M bytes of EPROM*
- *A32:D32 VMEbus (IEEE 1014) slave interface*
- *Programmable VMEbus base address*
- *Location monitor*
- *Built-In-Test (BIT)*
- *Bus Isolation mode (BI-mode ®)*
- *On-board EPROM programmability*
- *Conduction cooled per IEEE 1101.2 (0.65-inch pitch) for MIL-E-5400/4158, and MIL-STD-2036 applications*
- *Optional levels of ruggedization available*



Description

The SVME-537/DMV-536 Byte-Wide Memory Card is a byte-wide memory module which supports a variety of EPROM, EEPROM, and SRAM devices. It provides a means of adding non-volatile storage capacity to a VMEbus-based system. Up to sixteen 32-pin JEDEC-standard devices are accommodated for a capacity of up to 16M bytes of EPROM or 8M bytes of SRAM.



The card supports DY 4 Systems' common features such as a location monitor, Built-In-Test (BIT) and Bus Isolation mode (BI-mode).

DY 4 Systems' common features support diagnostics and logistics maintenance on-line. The location monitor provides an addressed mechanism for entry into and exit out of BI-mode®. Built-In-Test (BIT) provides hardware features to verify all operational circuit blocks on the module. Bus Isolation mode (BI-mode) provides a means to electrically isolate the module from the VMEbus. These features allow the users to perform fault detection and

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isolation during operation to build fault-tolerant systems. In addition, they allow for on-line maintenance and system configuration control to minimize field maintenance, time, and sparing logistics.

All versions of the SVME-537/DMV-536 are functionally identical. SVME versions for air-cooled environments are available in ruggedization levels 0, 2 and 3.

The SVME-537/DMV-536 is designed for use in airborne, land-mobile, and naval military applications in which the circuit cards must be installed in a sealed chassis to prevent contamination due to moisture, salt-fog, sand, and dust.

DY 4 Systems' conduction-cooled products are designed for severe environmental conditions as defined by MIL-E-4158, MIL-E-5400, and MIL-STD-2036. Thermal management is provided by an aluminum plane, bonded to a standard VMEbus-compatible circuit module. Integral to the cooling plane are vibration stiffening channels which improve the thermal dissipation ability, vibration resistance, and maximizes usable real-estate area without sacrificing the environmental abilities of the modules. Standard wedgelock fasteners provide a reliable thermal connection in the chassis.

Figure 1 is a functional block diagram of the SVME-537/DMV-536.

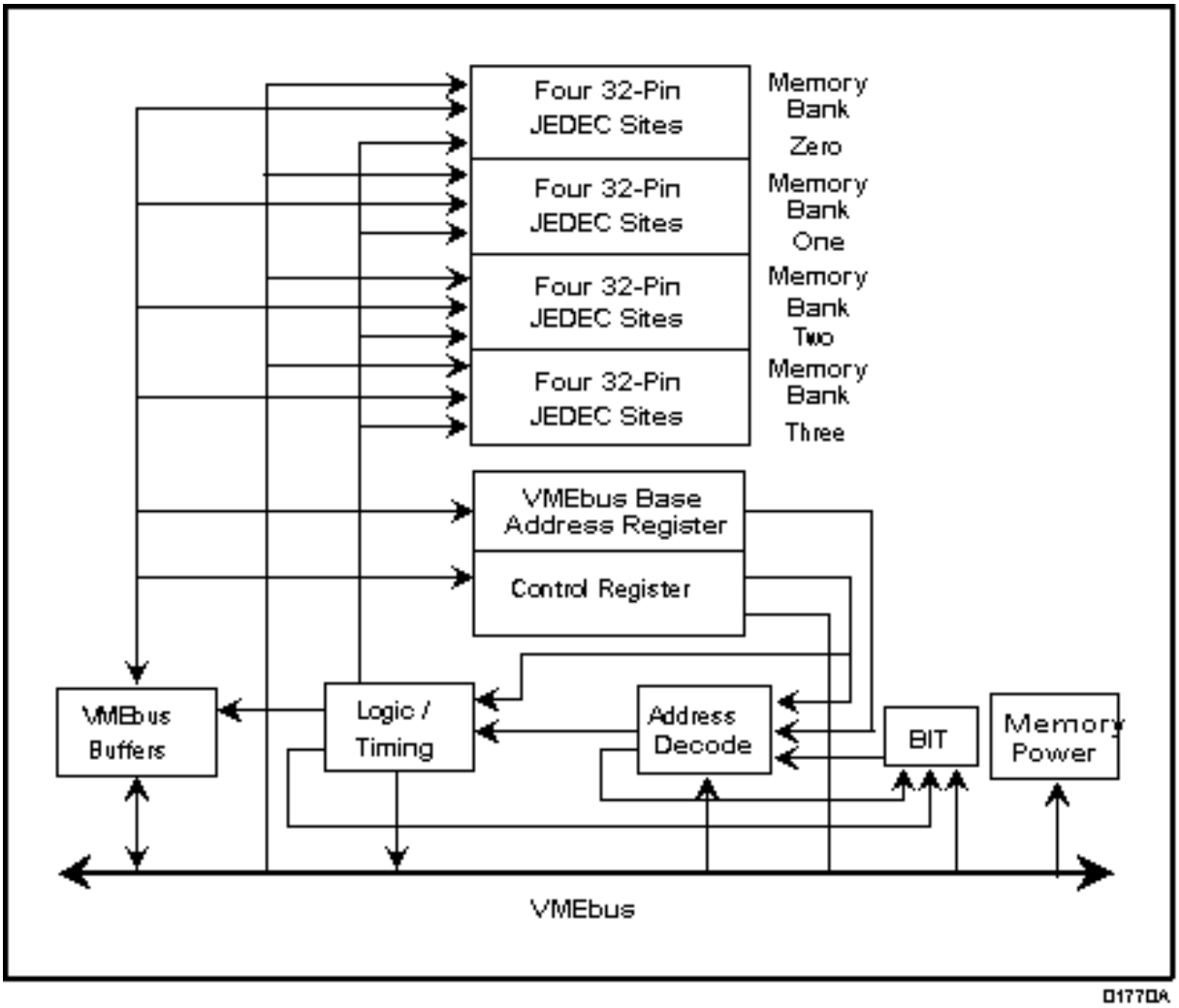


Figure 1: SVME-537-DMV-536 Byte-Wide Memory Card Block Diagram

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Memory

The SVME-537/DMV-536 accepts up to 16 JEDEC-standard memory devices in four banks. Each bank can support up to 2M bytes of SRAM or EEPROM, or 4M bytes of EPROM, for a total of 8M bytes of SRAM or EEPROM, or 16M bytes of EPROM. Furthermore, each of the four memory banks can be populated with a different type of memory.

Applications in severe vibration environments require that memory devices be soldered directly onto the board. To make this practical for EPROM devices, the SVME-537/DMV-536 allows EPROMs to be programmed on-board via a special test jig.

In the event of a power outage on the main +5V supply, circuitry on-board the SVME-537/DMV-536 can draw power from the +5V STDBY supply to maintain the contents of any static RAMs installed.

Bus Interfaces

The memory on the SVME-537/DMV-536 is accessible to the VMEbus in the A24 and A32 address spaces. D32, D16, and D08(OE) data transfers are allowed. Accesses to the SVME-537/DMV-536 are controlled via a base address register and a control/status register - both are accessible to the VMEbus in the A16 address space.

Low Power Dissipation

The maximum power dissipation is 16 watts. Extensive use of CMOS components minimizes power consumption.

Product Variants

Optional variants of this product are available to provide customer flexibility in achieving the desired trade-offs of functionality, performance and ruggedization versus cost. SVME-537/DMV-536 Byte-Wide Memory Card options include memory type and speed. all in standard DY 4 ruggedization levels 0, 2 and 3.

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Table 1
Specifications

ENVIRONMENTAL SPECIFICATIONS		
Temperature	**(Levels 0)**	
†Operating	0°C to 50°C	MIL-STD-810
Storage	-40°C to 85°C	Methods 501.3 & 502.3
Temperature	**(Levels 2,3)**	
†Operating	-55°C to 85°C	MIL-STD-810
Storage	-62°C to 125°C	Methods 501.3 & 502.3
Humidity (DMV CCA and SVME CCA - Level 3)		
Operating	0 to 95% non-condensing	MIL-STD-810 Method 507.3
Non-Operating	0 to 100% condensing	
Vibration (DMV CCA)		
Sine	5g at 15 to 2,000 Hz	MIL-STD-810 Method 514.4
Random	$0.1g^2/Hz$	
Shock (DMV CCA)		
	40g/11ms half sine	MIL-STD-810 Method 516.4, Proc 1
Altitude (DMV CCA)		
	21,350m (70,000 ft)	MIL-STD-810 Method 500.3

PHYSICAL DIMENSIONS	**DMV CCA**	**SVME CCA**
Height	233.4 mm (9.2 in.)	233.4 mm (9.2 in.)
Depth	160 mm (6.3 in.)	160 mm (6.3 in.)
Thickness	15.0 mm (0.6 in.)	20.0 mm (0.8 in.)
Weight	<900g (<2 lb)	<570g (<1.28 lb)

POWER REQUIREMENTS		
+5v (+5%, -2.5%)	3.0 A (maximum)	1.6 A (typical)
Actual power depends on installed memory devices		

† As a general design objective, the junction temperature of all components on the DMV-536-XXX is limited to 110°C maximum (when the chassis cold-wall temperature is 85°C.) When reliability or performance factors permit, a component's junction temperature may exceed 110°C marginally.

SVME board operating temperature is based on air flow of 11 cfm.

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