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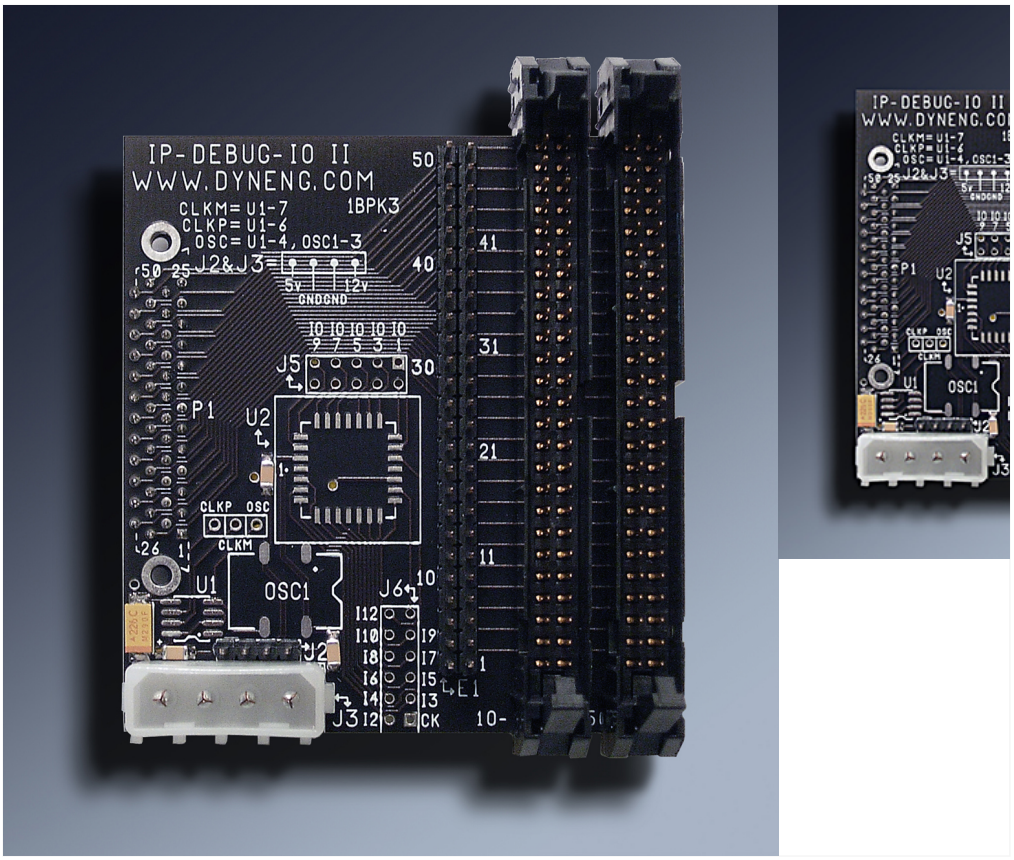
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IP-Debug-IO



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Standard Processing

Quantity 1 Add to Cart View Cart

Description Features Benefits Ordering Info

IP-Debug-IO Description

IP-Debug-IO is a specialized extender card optimized for Integration, and test of IP Modules. The IndustryPack IO signals are accessible via test points. The testpoints are .025 sq. posts for ease of connection to your logic analyzer or scope. The testpoints are labeled in the silkscreen for easy identification. 50 pin headers are supplied to provide ribbon cable or similar connections to the IP Module while using IP-Debug-IO. In addition loop-back or similar simulated system operation can be provided by IP-Debug-IO

Dynamic Engineering uses IP-Debug-Bus and IP-Debug-IO together for design/integration and production test.

Allow the host computer to connect the driver to the installed IP module. After that you can power the IP Module down [with IP-Debug-Bus] to add test points, swap units etc. and power on to immediately use the same software "connection" to the IndustryPack device. Since the address is static no reload issues like you have with PCI, PCIe etc. IP-Debug-IO provides the loop-back connections or the IP can be connected to the system through the ribbon cable connector and probed at the same time..

Do you need to connect your system to an IndustryPack while debugging your design? You can connect the IP IO connector to the rest of your system and have access to the component side of the IndustryPack module. IP-DEBUG-IO is designed to allow test circuits to be added to simulate part of the system that may not be available. For example: terminations or loopback connections. Test points and ribbon cable connectors are also provided.

Test points are provided on each of the 50 IO lines. The test points are tall enough to allow wire wrap connections, and scope or analyzer probes to be used. standard .025 in. sq posts.

High quality ejecting ribbon cable connectors [2] are supplied to make isolation and connection to the system a snap. The IP-Debug-IO card can be installed as a bridge between cable segments to provide testpoints.

PC style power connector and matching header for easy connection to a power supply.

Use with IP-Debug-Bus (ipdbgbus.html) for a complete debugging solution. IP-Debug-Bus (ipdbgbus.html) provides testpoints, hotswap, and access to the component side of the IP module.

Related Products

PCIe3IP (PCIe3IP.html)
IP adapter for PCIe with 3 IndustryPack Module positions

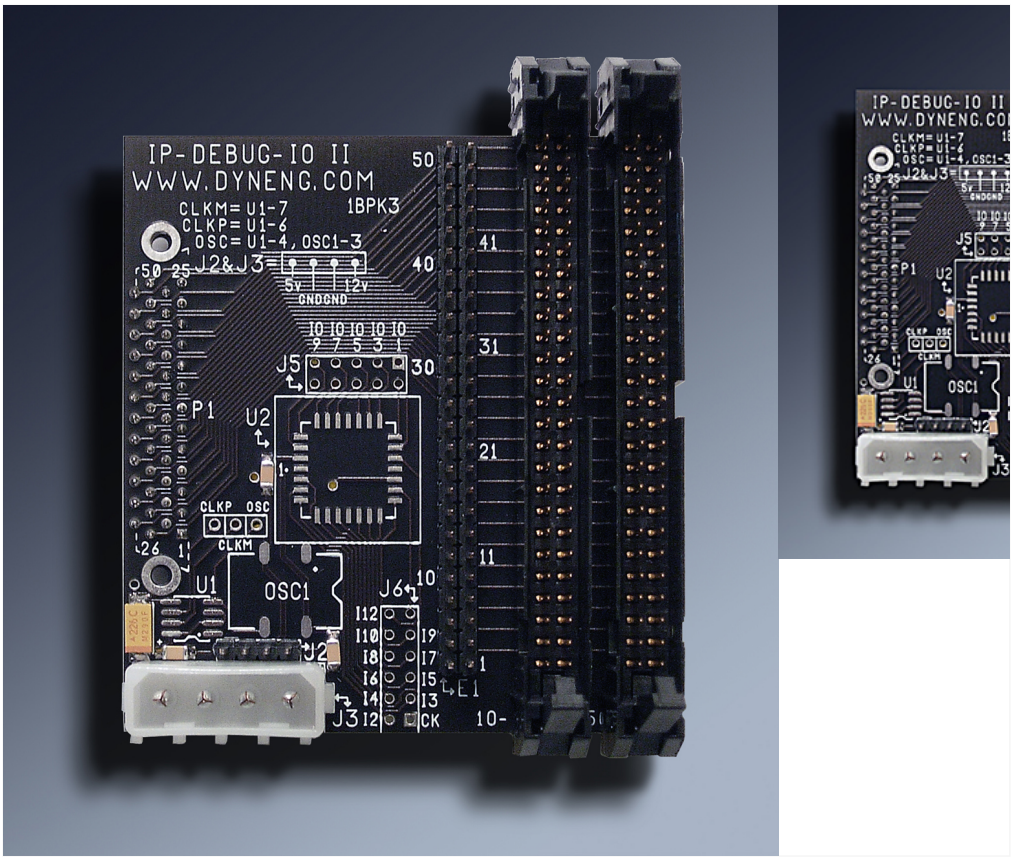
PCIe5IP (PCIe5IP.html)
IP adapter for PCIe with 5 IndustryPack Module positions

PCI3IP (pci_3_ip.html)
IP adapter for PCI with 3 IndustryPack Module positions

IP-BiSerial-VI (IP-BiSerial-VI.html)
IP module with 24 differential IO - LVDS or 485, Spartan 6 - 100 FPGA, PLL

HDRterm50 (HDRterm50.html)
50 pin ribbon cable ito screw terminal adapter with DIN rail mounting.

IP-Debug-IO



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Description | Features | Benefits | Ordering Info

IP-Debug-IO Features

Size
2.63" x 2.85"

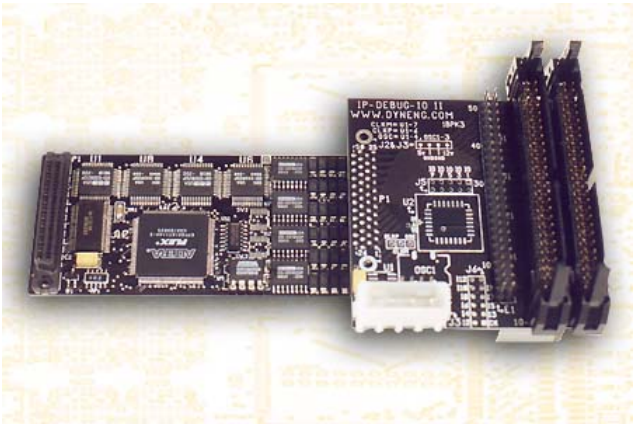
Power
Power connector provided to allow for loop-back with active circuits.

Mounting
Vertical connector to IndustryPack Module. IP Mounting holes provided.

Test Points
Logic Analyzer etc. connections to the IP module IO provided. Labeled in silk-screen. .025 in sq. posts are compatible with most logic analyzers and ocilloscopes.

IO

All 50 IO lines are routed from IndustryPack IO connector throught testpoints to dual ribbon cable headers. The dual headers allow use of IP-Debug-IO without an IP module attached - testpoints for our ribbon cable system.



IP Debug IO in use. Left side of IP Module connects to IP-Debug-Bus

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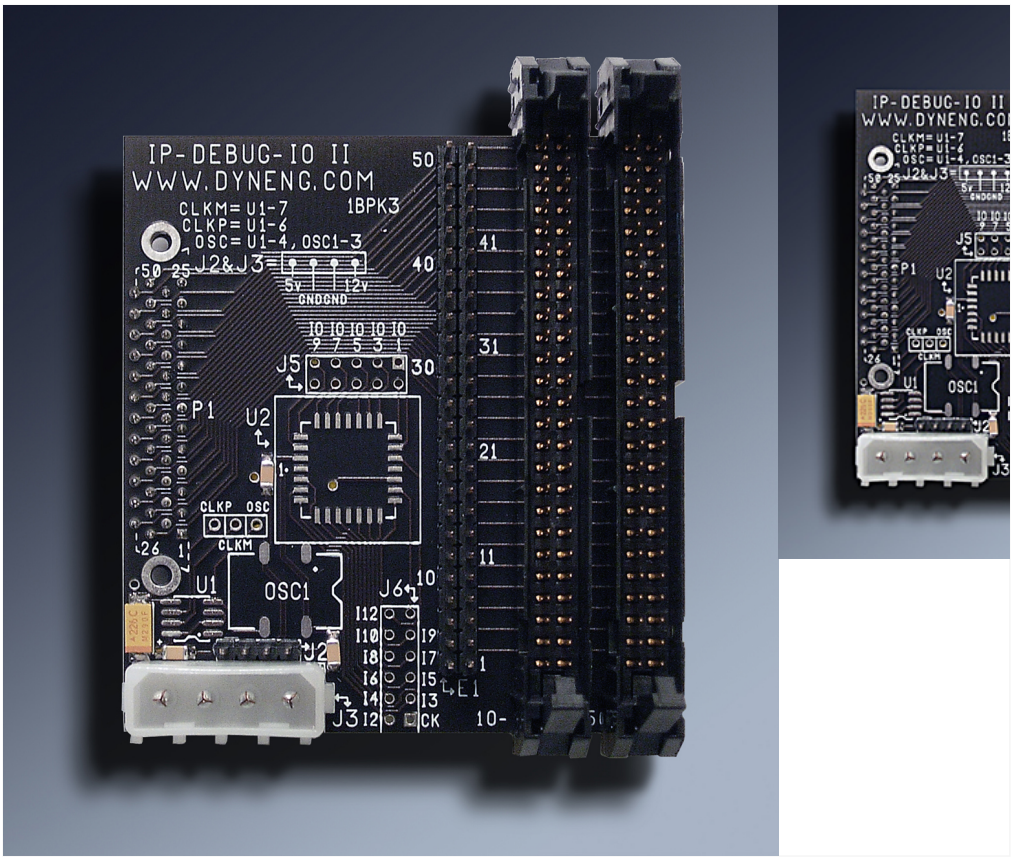
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50 pin ribbon cable ito screw terminal adapter with DIN rail mounting.

IP-Debug-IO



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Description Features Benefits Ordering Info

Part Number:IP-Debug-IO
Ordering Options

Standard board with testpoints on IP Module IO signals, User patterns for added active loop-back as well as passive loop-back or connection to the system. -ROHS add for ROHS version.

Related Products

PCIe3IP (PCIe3IP.html)

IP adapter for PCIe with 3 IndustryPack Module positions

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