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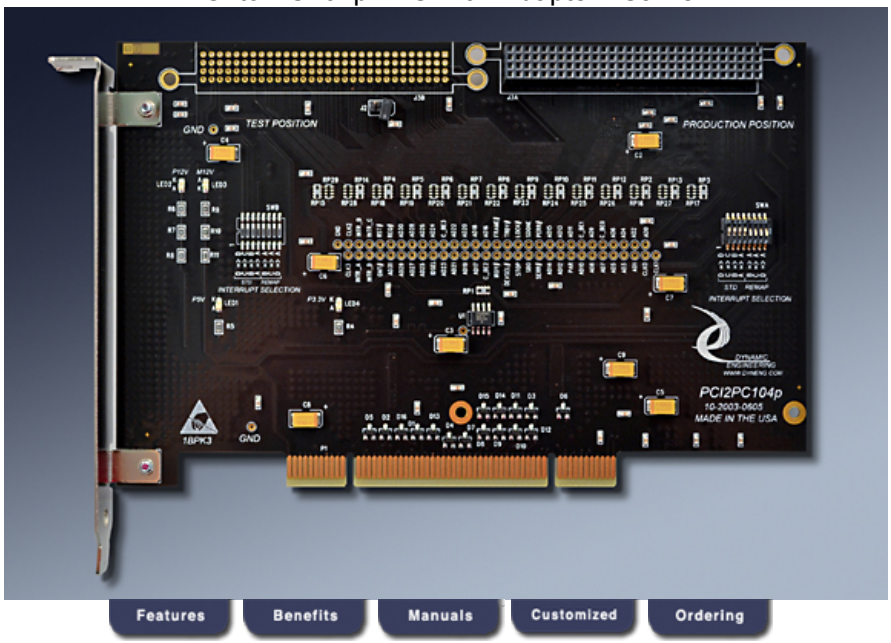
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PCI2PC104p

PCI to PC104p / PCI-104 Adapter / Carrier



PCI2PC104p PCI and PC/104p Compatible Adapter Carrier Front View

Save money with the PCI2PC104p... If you need to get your product to market fast, think about reusing existing PC104p and PCI-104 designs in PCI applications. For small production runs it will be less expensive without paying for a new design and layout. For new PC104p and PCI-104 designs the PCI2PC104p can decrease your time to market by allowing your engineers to debug in a PC, instead of the target hardware, until the design is "mature". A PC will provide better tools and visablility into the hardware.

Contact Dyanamic Engineering about converting your PC104p design to PCI and vice-versa.

The PCI2PC104p (PCI to PC/104p - PCI-104) adapter / carrier converter card provides the ability to install a PC104p or PCI-104 card into a standard PCI slot. The PCI2PC104p has a PC104p and PCI-104 compatible card slot mounted to a universal 1/2 length PCI card.

The PCI bus is buffered with series resistors and clamped with Schotky diodes. The PCI clock is distributed with a zero delay buffer. The PCI2PC104p design is passive with no added delays to access the PC104p / PCI-104 hardware. The traces are carefully routed with proper attention paid to the impedance and reference planes to maximize compatibility with your PCI system. The passive design of the PCI2PC104p reduces system latency.

The PCI bus is interconnected to the PC104 via 32 bit 66 MHz capable layout. The slower and more narrow device will determine the interface characteristics. M66EN is supported via interconnect to the PC104p or PCI-104 device and user shunt.

The PCI VIO is interconnected to the PC/104p directly. The PCI backplane will determine the bus voltage reference. It is left to the user to properly select the PC/104p and PCI motherboard for PCI voltage level considerations. Many PC104p and PCI-104 designs are "universal" and can work with 3.3 or 5V PCI backplanes. If you need to use a 3.3V card on a 5V backplane or vice-versa please contact Dynamic Engeering about a bridged version of the PCI2PC104p.

The PCI2PC104p follows the PC104p specs for maximum power consumption and heat dissipation. The 5v, 3.3v, and +/- 12V lines are supported with mini-planes with more capacity than the installed PC104p or PCI-104 is allowed to draw.

The IO area allowed by the PC104p and PCI-104 specifications are left clear to allow for standard cable options based on the module installed

If you have custom requirements please call or e-mail us with the details. We can make customer specific versions of the PCI2PC104p.

PCI2PC104p Features

- **Size** — Half size PCI card.
- **Slots** — 1 PC104p / PCI-104 Slot provided.
- **Clocks** — PCI bus can operate at 66 or 33 MHz. The installed module must be 66 MHz capable for 66 MHz operation to work properly. M66EN has a user shunt to allow or disable 66 MHz. operation.
- **Access Width** — Standard PCI byte lanes supported for byte, word and long access dependent on installed PC104p or PCI-104 module. 32 bit operation supported.
- **Software Interface** — Module register definitions as defined by installed hardware. No software set-up required by PCI2PC104p.
- **Interrupts** — INTA, B, C, D routed to PCI connector from PC104p or PCI-104.
- **Signal Conditioning** — AD signals are series terminated. Schottky clamping Diodes on critical PCI signals. Zero delay buffer for PCI clock distribution.
- **Power** — +5, +3.3, +12, -12V supplied to module.
- **VIO** — PCI IO Voltage is set by the PCI backplane. VIO is routed from the PCI connector to the module.
- **IO Interface** — Standard IO supported with clearance area on PCI2PC104p.
- **LEDs** — +3V, +5V, +12V, -12V.

PCI2PC104p Benefits

- **Speed** — With the passive PCI2PC104p direct connect to the PCI bus the latency to the PC104p / PCI-104 is optimized.
- **Price** — Save money by ... Making use of existing PC104p and PCI-104 designs in PCI applications without paying for the expense of a new design and layout. Decreasing your time to market reusing existing designs. Decreasing your time to market debugging in a PC instead of the target hardware until the PC104p or PCI-104 design is "qualified". Contact Dyanamic Engineering about converting your design to PCI and vice-versa.

  Quantity discounts available

- **Warranty** — 1 year warranty
- **Ease of Use** — The PCI2PC104p is easy to use. A plug and play interface to the PC104p and PCI-104 site. Two basic versions are available. Development is supported with access to the PC104p or PCI-104 component side and testpoints. Production is supported with full mounting and minimized system height.
- **Availability** — The PCI2PC104p is a popular board. We keep the PCI2PC104p in stock. Send in your order and in most cases have your hardware the next day - delivered to you via FedEx.
- **Size** — The PCI2PC104p is a half size PCI board which conforms to the PCI mechanical specifications. Eliminate mechanical interference issues. The PCI2PC104p can be used in all PCI slots including the new narrow chassis.
- **PC104p and PCI-104 Compatibility** — The PCI2PC104p is **PC104p and PCI-104** compliant per the specification. All Dynamic Engineering PC104p and PCI-104 Modules are compatible with the PCI2PC104p. All other PC104p and PCI-104 Modules which are compliant with the PC104p or PCI-104 specifications are compatible with the PCI2PC104p.
- **PCI Compatibility** — The PCI2PC104p is not **PCI** compliant. The PCI2PC104p has trace lengths in excess of the maximum specified by the PCI specification. The PCI2PC104p design includes several features to minimize the effects of the longer traces. The zero delay clock buffer keeps the PCI side of the clock length within specification, the series resistors and clamping diodes help to control the AD and control signals. Single PCI2PC104p adapters can be expected to work in any PCI bus stub. If you need to operate multiple adapters per stub please contact Dynamic Engineering about a bridged version of the design.

PCI2PC104p Ordering Information

PCI2PC104p: Standard PCI2PC104p: PCI2PC104p with production version connectors.
PCI2PC104p-test: Test Version: Testpoints and reversed connectors provided to give access to the component side of the PC104p or PCI-104 card and reference PCI signals.

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