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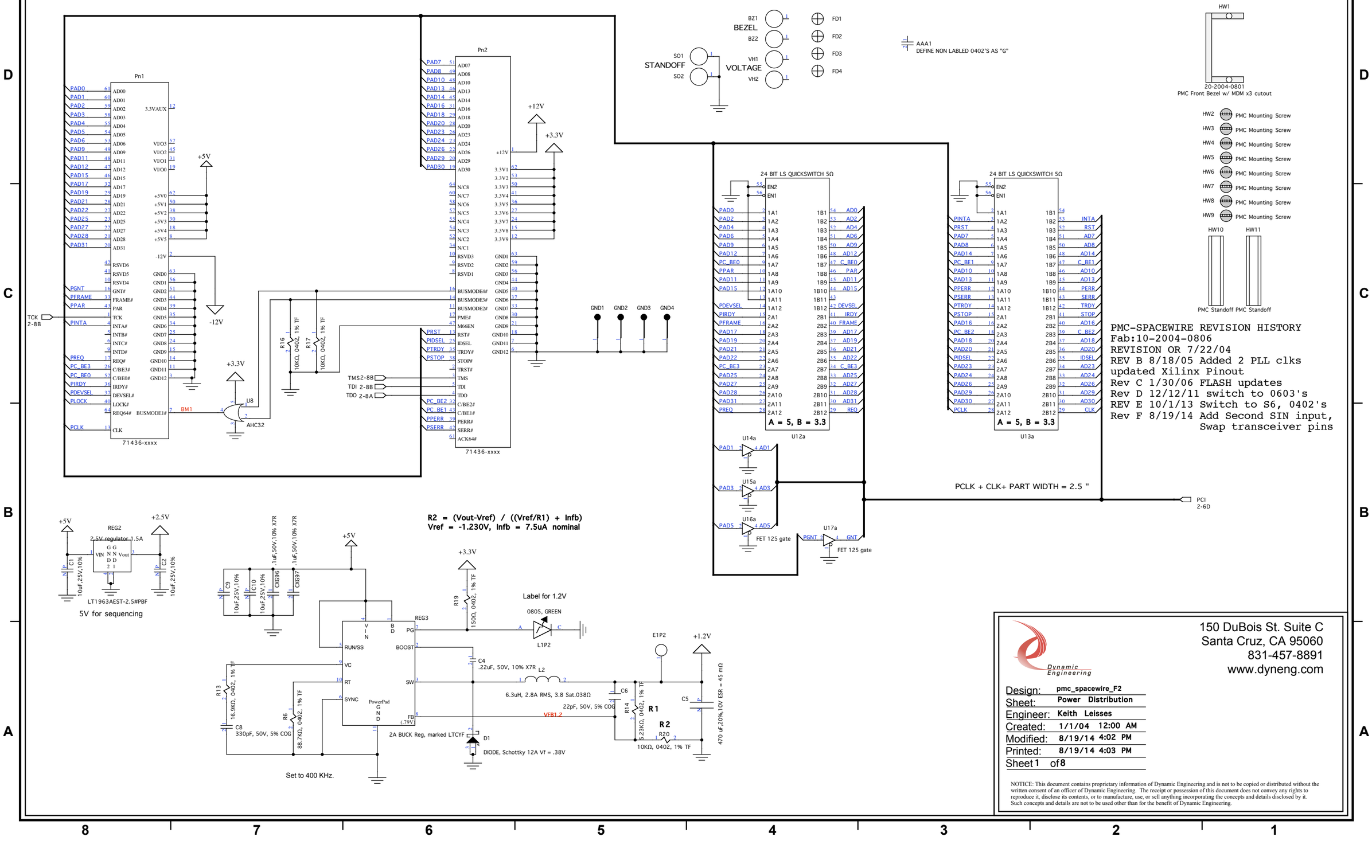
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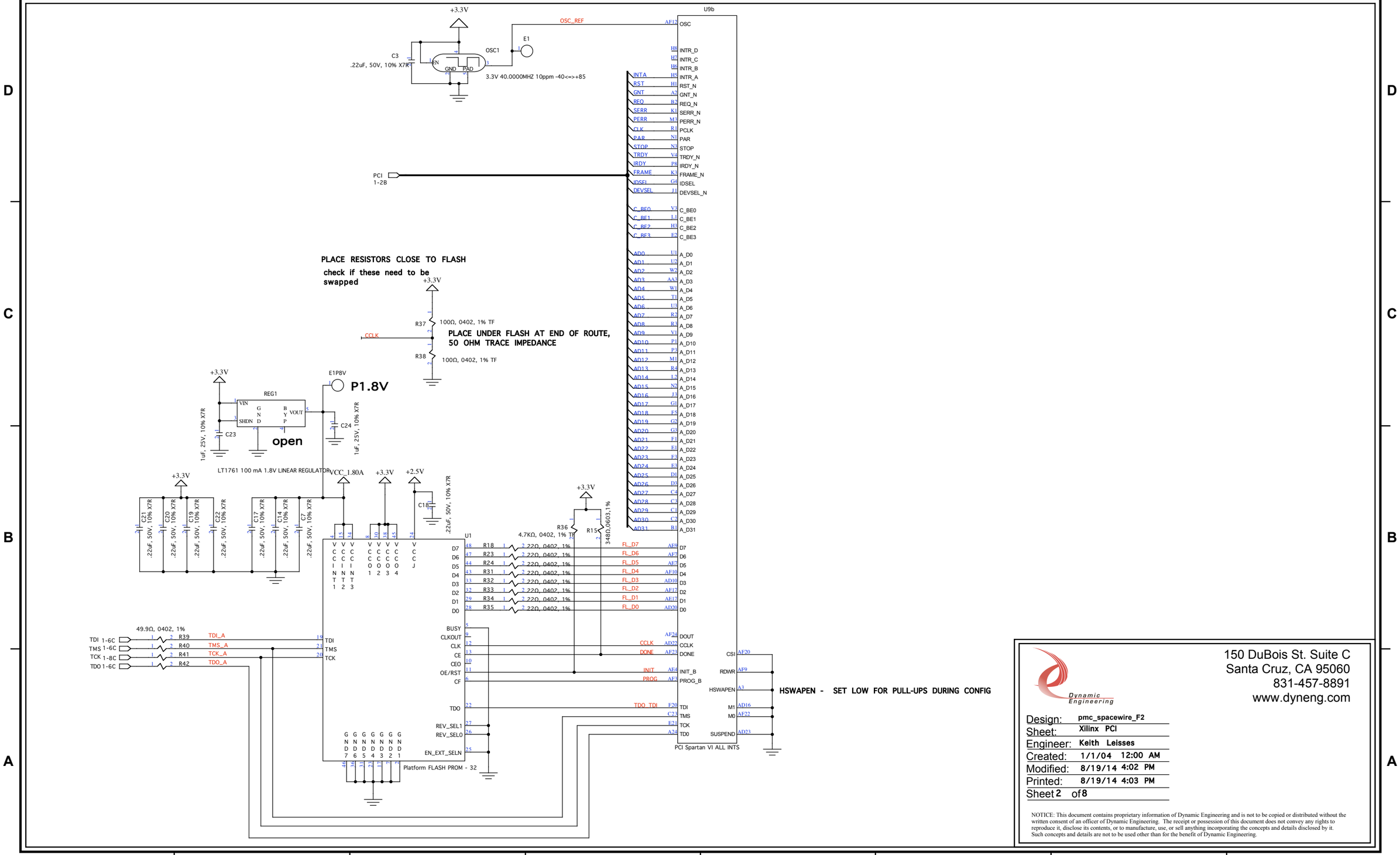


PMC-SPACEWIRE REVISION HISTORY
Fab:10-2004-0806
REVISION OR 7/22/04
REV B 8/18/05 Added 2 PLL clks
updated Xilinx Pinout
Rev C 1/30/06 FLASH updates
Rev D 12/12/11 switch to 0603's
REV E 10/1/13 Switch to S6, 0402's
Rev F 8/19/14 Add Second SIN input,
Swap transceiver pins

Dynamic Engineering
150 DuBois St. Suite C
Santa Cruz, CA 95060
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Design:	pmc_spacewire_F2
Sheet:	Power Distribution
Engineer:	Keith Leisses
Created:	1/1/04 12:00 AM
Modified:	8/19/14 4:02 PM
Printed:	8/19/14 4:03 PM
Sheet	1 of 8

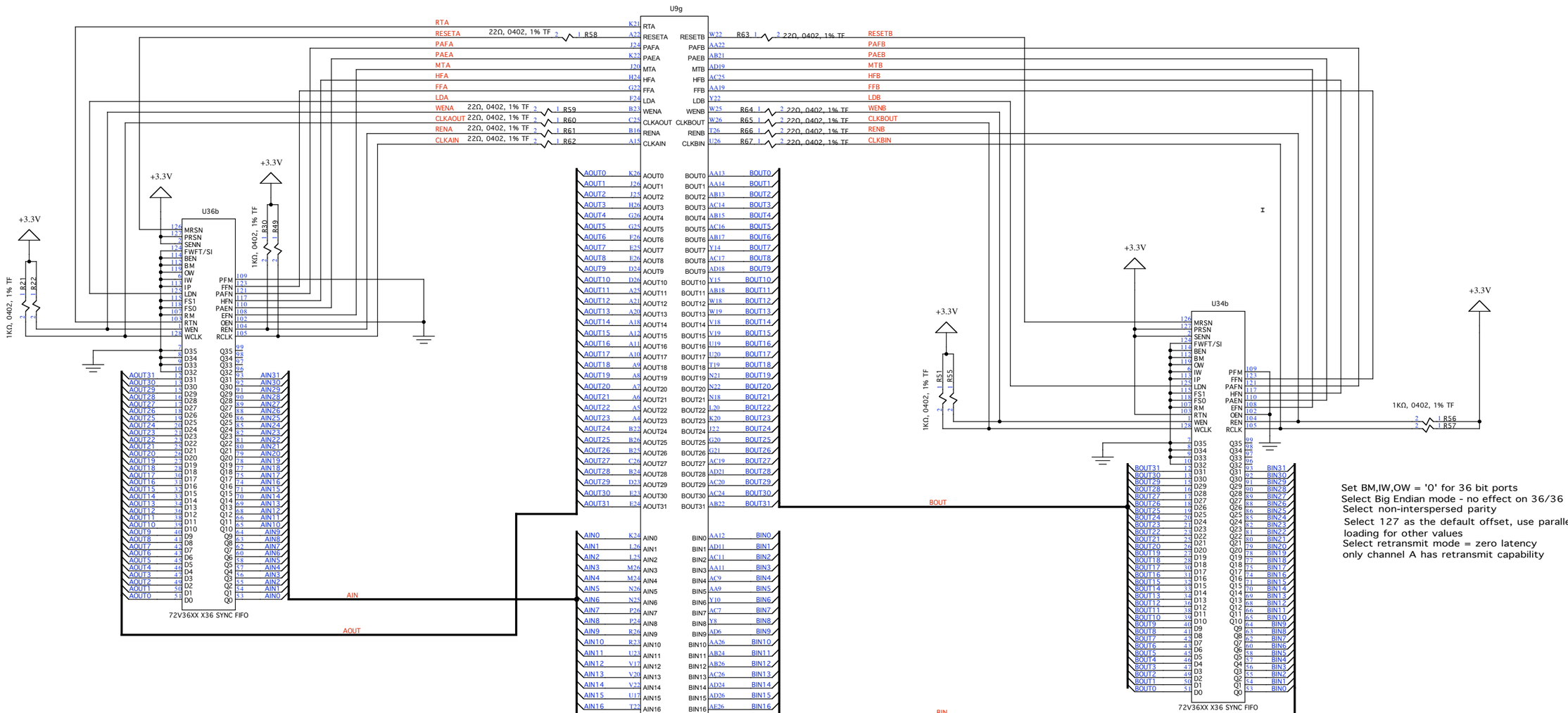
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Design:	pmc_spacewire_F2
Sheet:	Xilinx PCI
Engineer:	Keith Leisses
Created:	1/1/04 12:00 AM
Modified:	8/19/14 4:02 PM
Printed:	8/19/14 4:03 PM
Sheet	2 of 8

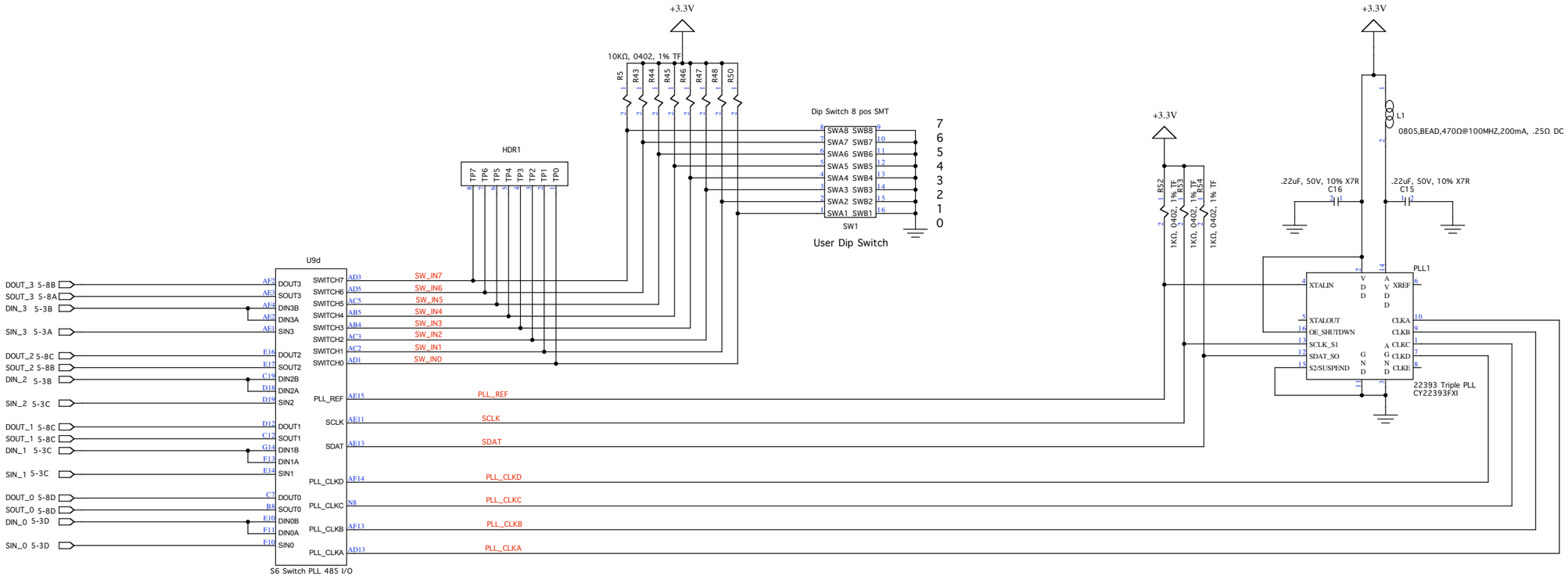
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Design:	pmc_spacewire_F2
Sheet:	Xilinx FIFO
Engineer:	Keith Leisses
Created:	1/1/04 12:00 AM
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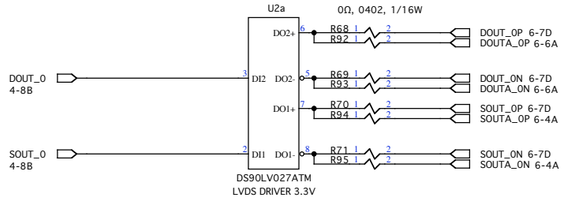


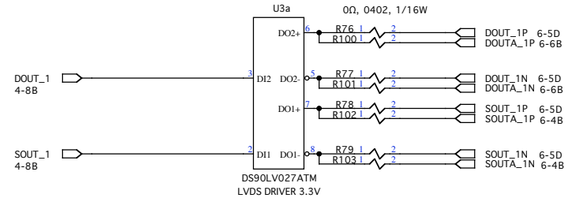
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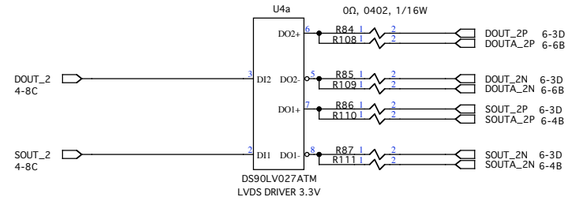
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Engineer:	Keith Leisses
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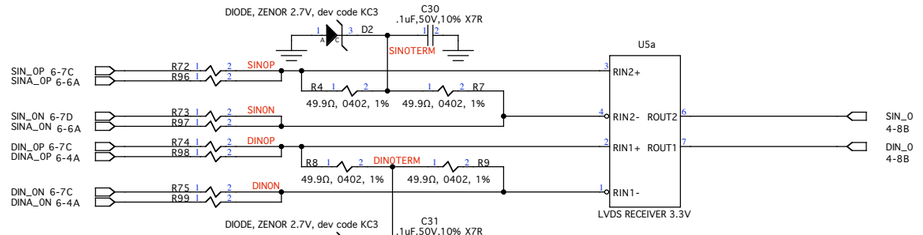
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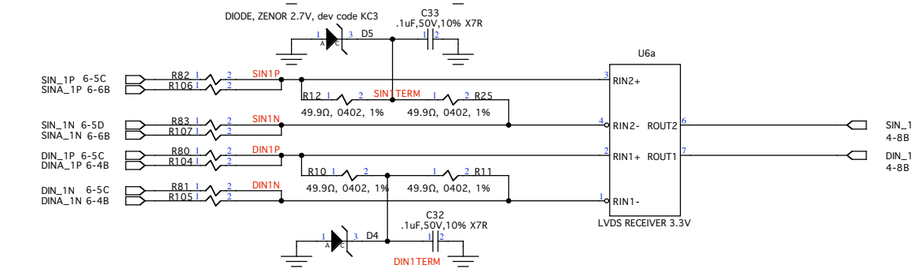
Diode and cap only installed for "LM11" option

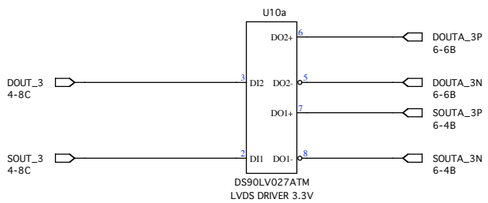


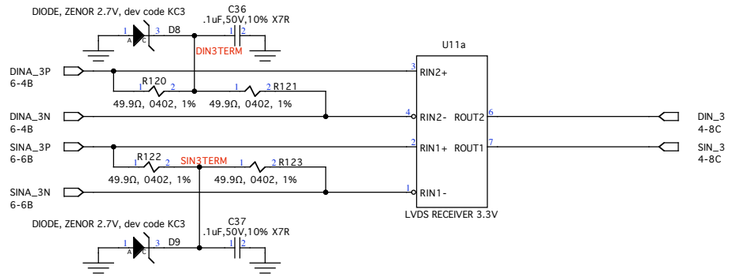










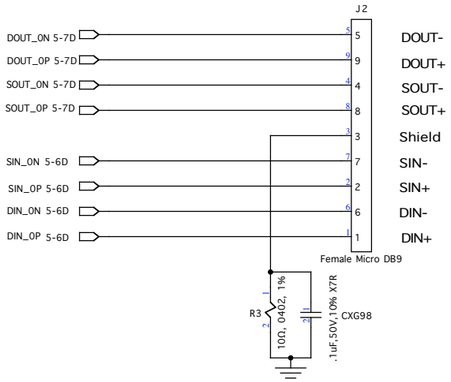


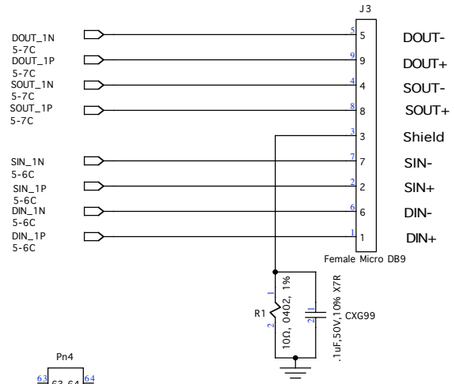
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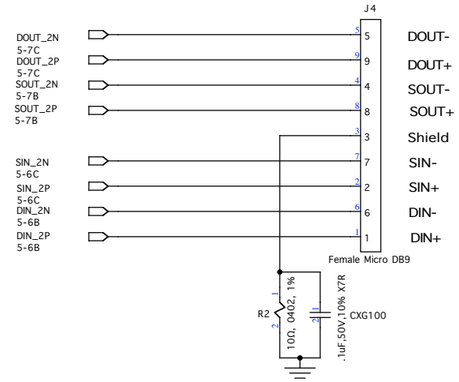
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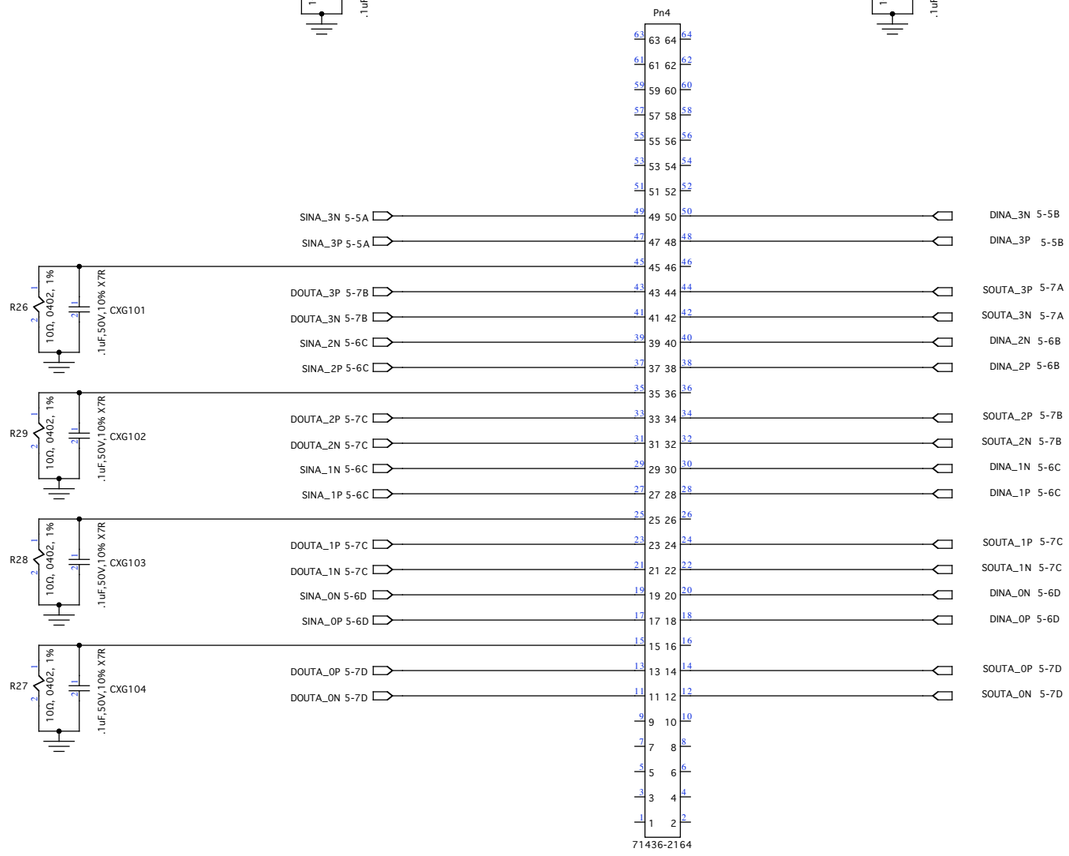
Design:	pmc_spacewire_F2
Sheet:	I/O BUFFERS
Engineer:	Keith Leisses
Created:	1/1/04 12:00 AM
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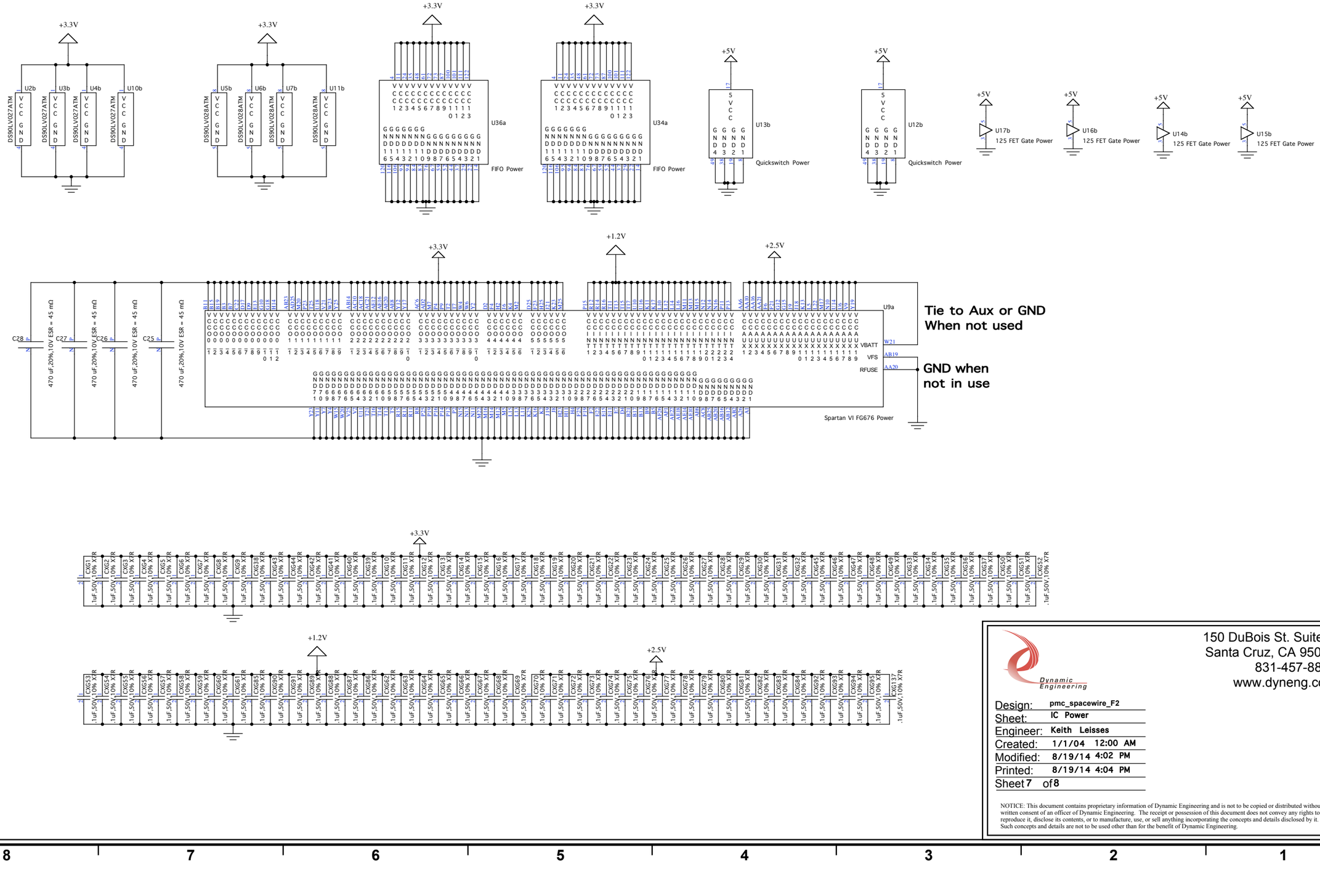


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Design:	pmc_spacewire_F2
Sheet:	IO Connector
Engineer:	Keith Leisses
Created:	1/1/04 12:00 AM
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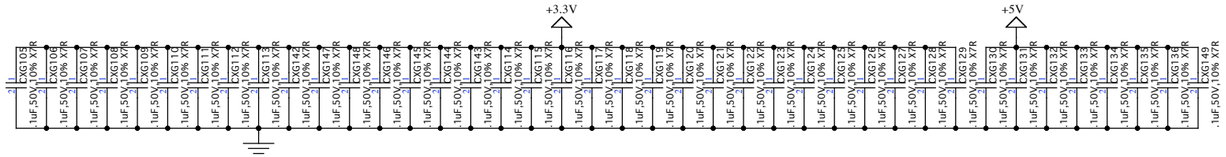


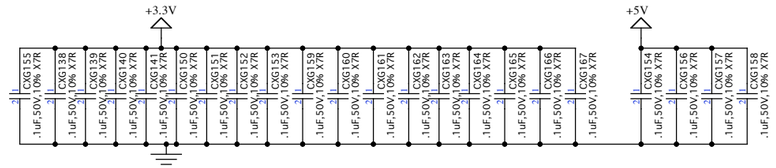
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Design:	pmc_spacewire_F2
Sheet:	IC Power
Engineer:	Keith Leisses
Created:	1/1/04 12:00 AM
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Design:	pmc_spacewire_F2
Sheet:	BiPass
Engineer:	Keith Leisses
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