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V420 - 12-bit Transient Recorder

General Description

The V420 is a double-width, C-sized VXIbus module that functions as a one- or two-channel transient recorder with data storage. It contains two 12-bit ADCs and two track/hold amplifiers for simultaneous sampling, and has one, two or four megasamples of on-board memory. Depending on the ordering option, the module is configured either as a dual channel unit with a maximum conversion rate of 500 kilohertz, or as a single channel device with a maximum conversion rate of one megahertz.

The input signal for each channel is received on a two-contact LEMO connector mounted on the V420 front panel. The sample rate is controlled by a programmable internal clock that is selectable from 50 hertz to one megahertz, or by an external TTL-level clock connected to a front-panel LEMO connector. A clock-out connector is also provided for synchronizing multiple recorders.

Pre-trigger and post-trigger sample sizes are programmable. The Stop (event trigger) LEMO accepts a TTL-level signal. When an event trigger pulse is received, the recorder continues sampling until the end of the post-trigger period is reached. An interrupt can be set at that time.

The amount of active memory (the size of the recording "loop") is programmable down to 2 kilosamples. Memory readout is in two's complement format with the sign extended to 16 bits. Samples for each channel can be read out contiguously. Readout can also occur in a streaming fashion, with the channel data interleaved. The available memory per channel is the total memory divided by the number of active channels. The ratio of active memory used for pre-trigger samples is programmable from 0/8 to 8/8 in 1/8 increments. As an aid in system setup, "present value monitoring" Read commands are included. A separate two-word memory is included on the module. The sample rate clock controls the updating of this memory, which can be read while the module is in either the sample or the non-sample state.

A Start Sample command causes the digitizer to begin sampling and digitizing the input channels after readout of the previous samples. Additionally, a Stop Sampling command allows software emulation of the external event trigger. This feature is useful for system diagnostics as well as for applications where a periodic wave form is being monitored.

The V420 supports both static and dynamic configuration capabilities. Access to the data is through memory locations indicated by the Offset Register within the VXIbus Configuration Register set, using A24/16, D16 data transfers.

Specifications

Ordering Information

Model V420-MA11
1-channel, 12-bit, 1MHz Transient Recorder; one megasample memory
Model V420-MB11
1-channel, 12-bit, 1MHz Transient Recorder; two megasample memory

Model V420-MC11
1-channel, 12-bit, 1MHz Transient Recorder; four megasample memory
Model V420-MA21
2-channel, 12-bit, 500 kHz Transient Recorder; one megasample memory
Model V420-MB21
2-channel, 12-bit, 500 kHz Transient Recorder; two megasample memory
Model V420-MC21
2-channel, 12-bit, 500 kHz Transient Recorder; four megasample memory

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- Next Module: V425 - 8-channel, 12-bit Transient Recorder

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