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Find the *Electro Cam PL-1746-C02-R1* at our website: ***Click HERE***

Electro Cam Corp.

New Features and changes to the PL-1746-C02-R1

PLμS® Plug-In module for the Allen-Bradley SLC 500

* This affects all modules purchased after 4/25/2004. Date codes 0417 and newer.

Active Program Selection: ***Register M0:S.121 is now used to select the active program.*** *No longer available are output word O: S.1 & M0:120 from previous versions.* If you were using the active program output word O:S.1 to set the active program, it can be mapped to the new M0:S.121 register.

Fast Update Machine Position: The Machine position is based on Scale Factor and Machine Offset values. A faster updated position value is now available on input word **I:S.9**. This affords faster update time and does not require moving the M0 file data to an integer file. The same value was previously available from M0:S.24.

Output Assignment: Now you can assign any of the 32 outputs to each of the 6 physical outputs on the front of the module. Previously, only outputs 0 through 5 were available. This allows for greater flexibility when using output groups and input modes.



PL-1746-C02

Hardware Error Register: Errors within the PLS Module are reported via the Hardware Error Register I:S.5. When an error occurs, the PLS will turn off all outputs and they will remain off until the error is cleared by toggling the Clear Error bit O:S.0/8. The "any error" bit I:S.5/15 should be monitored in the ladder program to detect if an error has occurred.

Programming Error Reporting: Programming errors will no longer stop further programming instructions as in previous versions. Now these errors will accumulate until cleared by toggling the Clear Error bit O:S.0/8.

System Busy Bit: The System Busy Bit I:S.5/13 is no longer available and has been assigned "reserved status". If you require an indicator that the SLC is "talking" to the PLS Module, you can use the Backplane Interlock bit M0:S.866

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