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Find the *Emerson / Motorola M68302ADS* at our website: ***Click HERE***

The LAPB, LAPD, and X.25 modules are available in C source code form. The source code for these modules requires a license from Motorola.

The EDX kernel is available in assembler source code form. The source of the EDX kernel requires a license from Motorola.

B.3 THIRD-PARTY SOFTWARE SUPPORT

Since the IMP is a memory-mapped device based on a full M68000 core, existing compilers, source-level debuggers, assemblers, and linkers designed for the M68000 Family may be successfully used. Many third parties supply such tools.

B.4 IN-CIRCUIT EMULATION SUPPORT

Full in-circuit emulation support is available from multiple third parties, but is not discussed in this manual.

B.5 302 FAMILY ADS SYSTEM

The M68302FADS is an integrated Family Applications Development System (FADS) designed to aid hardware and software developers of the MC68302, MC68LC302, MC68PM302, and MC68EN302 in quickly evaluating and developing applications for these devices. All of the hardware resources needed to download and debug application software are provided, such as large blocks of flash and static RAM for the processors, serial ports, clock generation circuitry, logic analyzer connectors, expansion connectors as well as monitor/debugger hardware and software. The logic analyzer connectors provide the user with access to all of the processors' pins in order to monitor bus activity. The expansion connectors let the user attach hardware applications and to use board resources to verify a design.

To serve as a convenient platform for software development, the M68302FADS is provided with a monitor/debugger for the Integrated Multiprotocol Processor (IMP) section. The monitor/debugger provides the following operations: memory dump and set (with optional disassembly of 68K instructions), single instruction execution, breakpoints, and downloads. The debugger interface can work together in separate Windows 3.0 DOS shells on the same x86 based PC to communicate with the on-board IMP hardware. Future support for SUN platforms is planned.

The M68302FADS board has sockets for and is shipped with the MC68302RC25, MC68LC302RC20, MC68PM302RC20. An adapter card for the MC68EN302 will be available separately.

- General Features
  - —Supports the 68302 family of processors: MC68302, MC68LC302, MC68PM302 and MC68EN302 (with an adaptor).
  - —On board IMP(68302) debugger software with host debugger interface.
  - —Separate external clock generators for the IMP(68302)

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—PCMCIA port connector with extender card to plug directly into PCMCIA sockets.

—Expansion connectors providing all the 68302 family device signals.

—68000 bus signals brought out to logic analyzer connectors.

—Single +5Vdc power supply with onboard 5V to +/-12Volt converter.

- IMP(68302) Support Features Included

—MC68302 at 25 MHz

—MC68LC302 at 20 MHz

—MC68PM302 at 20 MHz.

—512 kbyte, zero wait state static RAM, expandable up to 1 Mbyte. (16 bit orientation)

—1 Mbyte FLASH. (16 bit orientation)

—2 kbyte EEPROM. (8 bit orientation)

—MC68681 DUART, with two RS232 serial ports.

—Serial RS-232 terminal connectors

—RESET and ABORT controls.

—RUN and HALT status indicators (LED’s).

—Bus expansion connector pin out, is compatible with the 302ADS

—ADI port connector.

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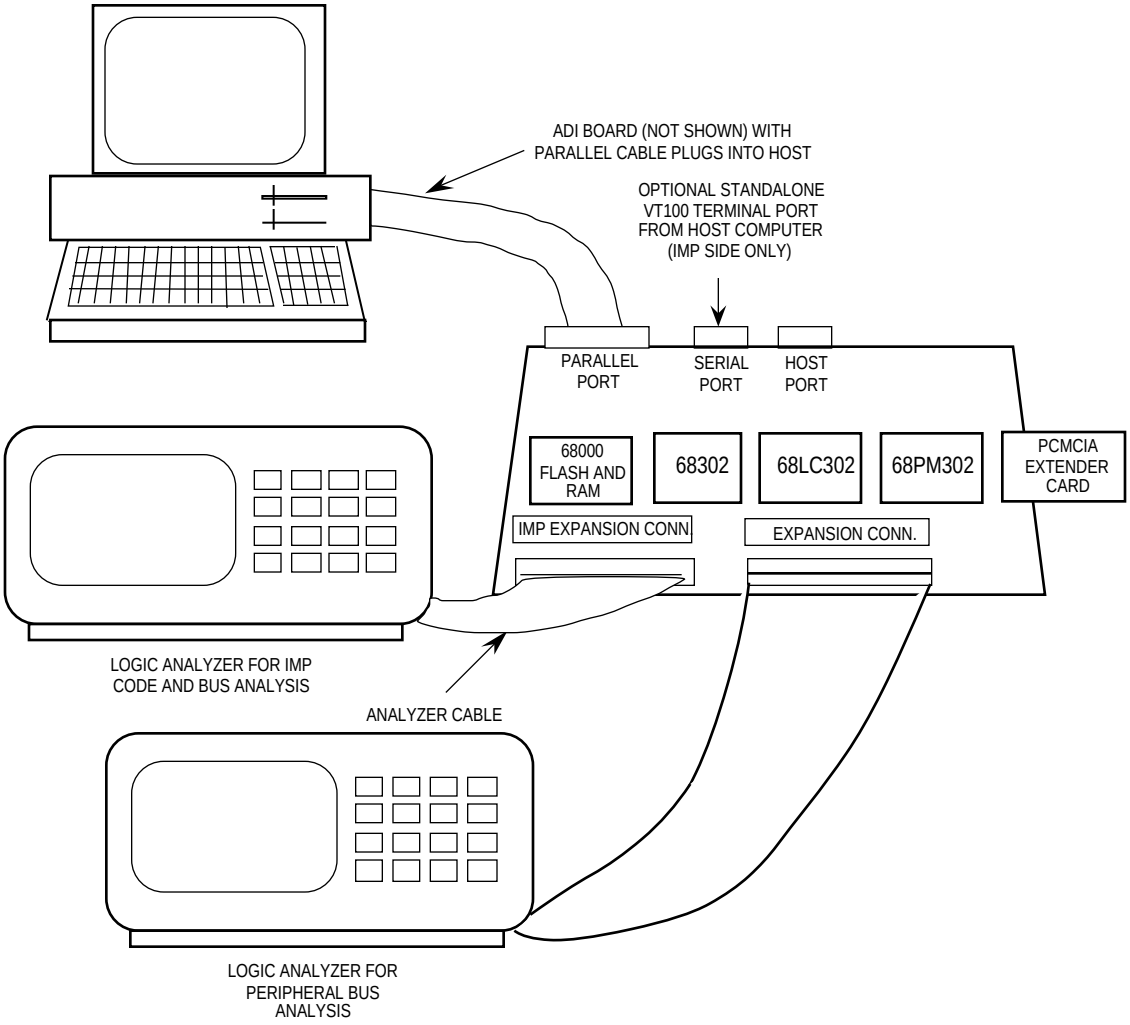


Figure B-2. MC68302FADS

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