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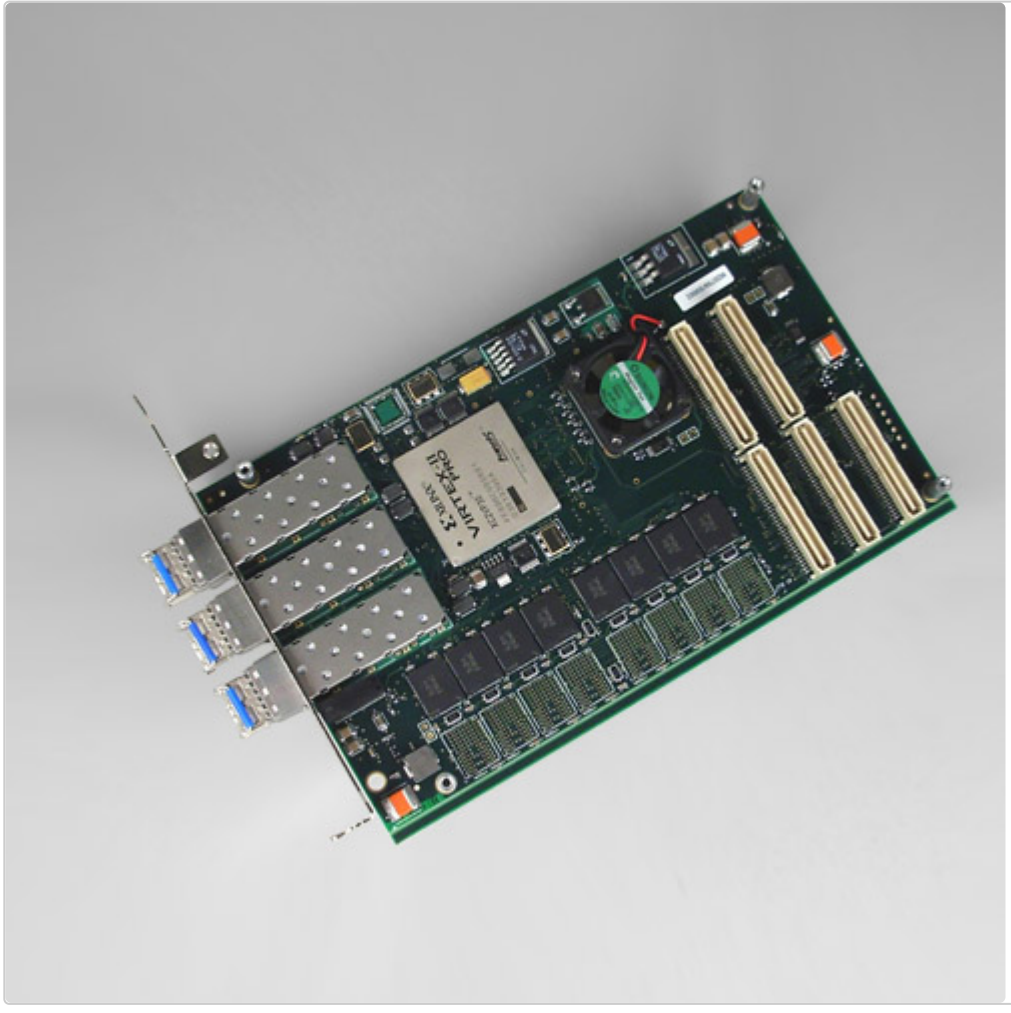
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Find the *Engineering Design Team PCI-GS* at our website: ***Click HERE***

3x3G Mezzanine



Xilinx Virtex II Pro | 3Gbps

The 3x3G is a mezzanine board that pairs with an EDT main board (for PCI or PCI Express) for high-speed data transfer. It has three channels, each having one 3Gb Xilinx RocketIO transceiver and one SFP for either an electrical RJ45 1GbE (1000 Base-T) Ethernet adapter or an optical LC.

The 3x3G has four additional RocketIO transceivers that connect to an internal 40-pin connector. Two quad-frequency oscillators allow eight different clock speecs.

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For details on system requirements and EDT-provided software driver packages, see specifications for your EDT main board (PCI SS (https://edt.com/product/pci-ss-main-board/), PCI GS (https://edt.com/product/pci-gs-main-board/), or PCIe8 LX (https://edt.com/product/pcie8-lx-fpga-board/)).



PCI GS Main Board (https://edt.com/product/pci-gs-main-board/)

The PCI GS is a PCI main board that provides powerful high-speed DMA, memory, and programmable FPGA resources. It is designed to support any one of numerous EDT mezzanine boards. The PCI GS has a programmable FPGA (Xilinx Virtex II…

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PCI SS Main Board (https://edt.com/product/pci-ss-main-board/)

The PCI SS has been discontinued and is no longer available. The information on this page is provided for use by existing customers. The PCI SS (for PCI) is a main board that provides powerful high-speed DMA, memory, and programmable…

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