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Find the *Engineering Design Team SCD-60* at our website: *Click HERE*





SCD-60

60 Megabyte per second
configurable DMA interface
for SBus computers

Features

- Implements a high speed DMA channel between an external device and host computer.
- Transfer rates up to 60 MB per second.
- 2 KByte FIFO
- EIA-644 LVDS compatible driver/receivers
- Customizable by a RAM based programmable gate array
- Synchronous protocol
- Clock timing from DMA interface, user device or both
- Sun SPARCstation compatible
- Device drivers for Solaris 1.x (SunOS 4.1.x) and Solaris 2.x (SunOS 5.x)
- Single SBus slot

Description

The SCD-60 interface provides a high speed 16-bit parallel DMA channel between an external device and host computer. The device interface uses EIA-644 Low Voltage Differential Signals (LVDS) compatible driver/receivers connected to a RAM based programmable gate array and 1 KByte FIFOs. The SCD-60 uses Direct Virtual Memory Access (DVMA) and asynchronous I/O to achieve transfer rates to or from memory of up to 60 MB per second. The hardware protocol is synchronous. That is, all data and control signals are sampled with a clock transmitted along with the data and control signals. This clock may be generated by the DMA interface or the user device or both. All signals are differential using EIA-644 LVDS specifications. The SCD-60 includes a versatile, straightforward protocol but optional customer-specific protocols may be developed.

Applications

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- Imaging devices
- Scanners
- Plotters
- Research and development

Software

A standard SunOS loadable device driver or the SPARCstation is included with the SCD-60. The driver provides access to the SCD-60 through standard and asynchronous read and write system calls. Continuous transfers are implemented with asynchronous I/O.

The SCD-60 is provided with minimal Codes PROM for identification only. User Code PROMS MAY be developed for specific applications.

Documentation

The SCD-60 comes with a detailed user's guide, which is also available on-line. Click Here for access.

Support

EDT provides several levels of customer support, from phone consultation to custom design of hardware, software, or FCode drivers, at hourly rates. The SDV driver source code is available for an additional charge. Technical support is also provided though the Technical Support section of this web site.

Specifications

SBus compliance

DVMA master
1, 2, 4, 8, or, 16 words per transfer - the largest accommodated by the host computer
25 Mhz maximum clock
Level 5 interrupt
SBus memory space approximately 66K bytes

Device interface

Protocol:
Synchronous streaming

Buffers:

2 KB FIFO for input 2 KB FIFO for output

Connectors:
AMP 749075-8
high density 80 pin

Environmental

Temperature:
Operating: 10° to 40° C
Nonoperating:-20° to 60° C

Humidity:

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Operating: 20% to 80% noncondensing @ 40° C
Nonoperating: 95% noncondensing @40° C

Physical

Single standard SBus slot
Dimensions: 3.3" x 5.78" x 0.5"
Weight: 3.5 oz.

Power

1.6A at 5 volts

Software

Software drivers for Solaris 1.x (SunOS 4.1.x) and Solaris 2.x (SunOS 5.x)
Standard read/write system call support
Asynchronous I/O support for continuous transfers

More Information

- Complete user's guides in Adobe Acrobat™ format
- Updated software support packages for EDT products

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