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Find the *EPIX PIXCI CL1* at our website: *Click HERE*



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PXIPL

Optional
TTL Module

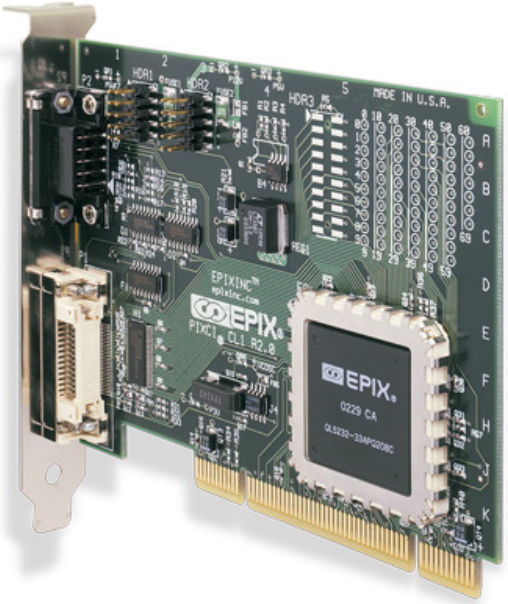
PDF

PIXCI® CL1

Base Camera Link for PCI

Features

- PCI bus frame grabber
- 132 megabytes per second burst DMA transfer rate
- 100 megabytes per second sustained transfer rate
- 32-bit memory addressing



Capture

- Base Camera Link camera
- Line scan or area scan cameras

- **Base Camera Link Frame Grabber**
- **32 Bit PCI Bus**
- **Windows & Linux, 32 & 64–bit**

CAMERA Link® REGISTERED PRODUCT

- Monochrome or color cameras
- Individual lines or frames
- Continuous video
- Up to 4,096 pixels per line
- Up to 4,096 lines per image (area scan)
- Up to 65,534 lines per image (line scan)
- Triggered image sequence

Trigger and I/O

- Camera exposure and asynchronous reset control

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- Trigger and frame enable inputs
- Strobe output
- General purpose inputs and outputs
- 26 pin MDR Camera Link connector
- 9 pin Dsub for trigger and I/O

Specifications

- 5 volts @ 530 mA
- 32 bit / 33 MHz PCI bus master, or 64 bit / 66 MHz PCI bus master slot
- 3.3 or 5volt PCI signaling
- 12.5 cm long, 8.3 cm high, 4.91” long by 3.27” high
- 77 grams, 2.7 ounces
- 0 to 70 degrees Celsius operation
- CE, FCC, ROHS compliant
- Camera pixel clocks from 20 to 85 MHz
- Made in U.S.A.

Software

- XCAP-Lite image capture, display, and save program included with frame grabber purchase
- XCAP-LTD captures video to computer memory and more..
- XCAP-STD streams video to disk and performs image processing and more..
- PIXCI Selection Guide lists cameras supported with dedicated Capture & Adjust dialogs
- XCAP supports all camera link cameras connected to PIXCI® frame grabbers
- XCLIB programmer libraries for frame grabber and camera control
- PXIPL image processing, measurement, and analysis libraries
- Windows & Linux, 32 & 64-bit
- TWAIN compatible
- 3rd Party software solutions also available.

TTL MODULE



The optional TTL Module minimizes false triggers when using a TTL trigger with a cable longer than one foot. The TTL MODULE converts the TTL trigger into a differential signal for reliable triggering with up to 200 feet of cable. The TTL MODULE also can generate a TTL strobe from the attached PIXCI® board. Compatible with PIXCI® CL1, CL2, CL3SD, D2X, D3X, SI, and SI4.

SPECIFICATIONS

RESOLUTION:

8 to 4,096 pixels per line
1 to 4,096 lines per image (area scan)
1 to 65,534 lines per image (line scan)

SIGNALS:
Differential RS-644
Differential Trigger IN / Strobe OUT
2 TTL input/outputs

CONNECTIONS:
1 26-pin Camera Link
1 9-pin D-Subminiature
2 10-pin headers

MAXIMUM FRAME RATE:
Camera Dependent

CE / FCC CERTIFICATION:
PIXCI CL1 was tested per EMC directive 89/336/EEC and performed to class B.

BUS REQUIREMENTS:
32 bit, 33 MHz PCI bus master, or
64 bit 66 MHz PCI bus master
3.3V or 5V PCI Signaling

POWER DRAW:
Camera attached, capturing video: 530mA
Camera attached, not capturing video: 390mA
Camera not attached: 290mA

DIMENSIONS:
12.48 cm long by 9.4 cm high (4.913" x 4.20") [short slot]

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