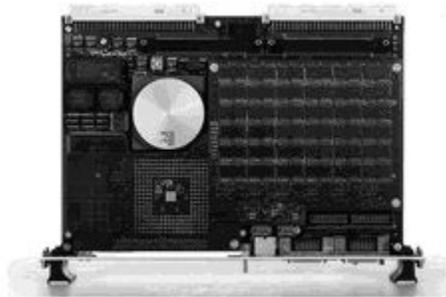# SPARC CPU-10

Ultimate scalability in performance

Benefits

- Ultra-high performance for the most demanding embedded UNIX and real-time applications
- Easy upgrade to higher performance through plug-in MBus modules
- Significant performance increases without software changes
- Faster time to market with leading-edge solutions
- Extra SBus expansion slot for maximum design flexibility
- Choice of hundreds of SBus expansion cards for specialized functions

The SPARC CPU-10 introduces a whole new level of performance and flexibility to the design of embedded applications.

The SPARC CPU-10 is fully scalable through the installation of a SuperSPARC or hyperSPARC MBus processor module. MBus modules are available with secondary cache and in numerous frequency options to meet performance requirements. This modular design lets you keep pace with the rapid advances in processor technology. As new levels of performance are introduced, you can scale up to them quickly and easily without redesign or software changes.

The scalability of the SPARC CPU-10 makes it an excellent platform for leading-edge applications requiring ultra-fast processing speed. It can be used in the most advanced designs for military command and control, industrial control and factory automation, medical imaging, graphics and simulations, CAD/CAM, and other applications.

For additional flexibility, the SPARC CPU-10 adds ISDN and CD-quality audio ports to the already impressive set of workstation I/O interfaces, including fast SCSI-II, Ethernet, floppy disk, serial, parallel, and keyboard and mouse ports.

Feature highlights

- 100% compatibility with Sun SPARCstation 10 family and Solaris 1.x and 2.x operating environment
- Choice of 75 MHz SuperSPARC II, or 90 MHz or 125 MHz hyperSPARC MBus modules
- Performance ratings:
  - 75 MHz:
    - 100.6 SPECint92 and 96.8 SPECfp92
  - 90 MHz:
    - 98.0 SPECint92 and 116.0 SPECfp92
  - 125 MHz:
    - 125.0 SPECint92 and 143.0 SPECfp92
- Includes 32-, 64-, 128, or 192 MBytes of local DRAM with Error Correction Code (ECC)
- 1 MByte on-board programmable User Flash EEPROM
- Two standard SBus slots for I/O expansion
- 32-bit VMEbus interface
- Rugged dual-slot 6U VMEbus board with stiffener bars and mezzanine memory modules