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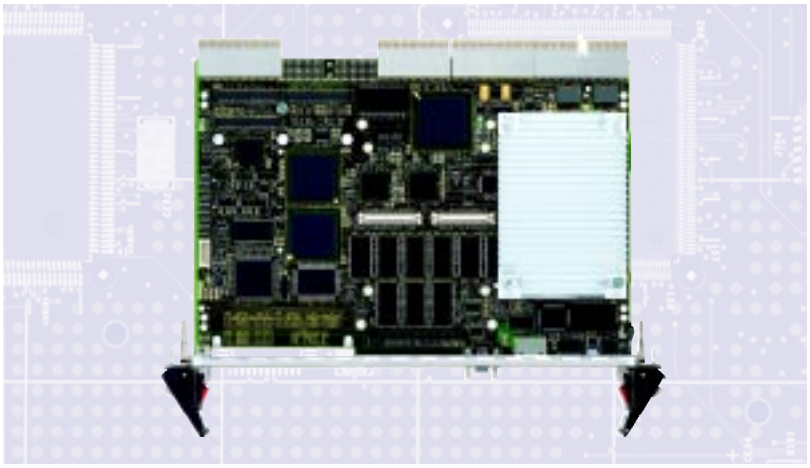
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Find the *Emerson / Motorola / Force Computers CPSB-560* at our website: *Click HERE*

DATASHEET

CPSB-560 UltraSPARC-IIi+ Processor-Based PICMG 2.16 Server Blade



The CPSB-560 PICMG 2.16 server blade offers high-performance UltraSPARC-IIi+ 650MHz processor performance.

- **Telecommunications**
- **Datacommunications**
- **Industrial Control**
- **Defense & Aerospace**

STRATEGIC DEVELOPER

Features

- UltraSPARC-IIi+ 650MHz processor—low-power consumption processor on Sun Microsystems embedded roadmap
- Onboard IDE hard-disk drive (30 to 60GBytes)
- UltraSPARC platform, ideal to run Solaris 8 applications
- Supports PICMG 2.16 specification for CompactPCI Packet-Switching Backplane (cPSB) applications
- PICMG 2.9-compliant IPMI controller
- 1GByte SDRAM (option to 2GBytes)
- Two Gigabit Ethernet interfaces
- Two Fast Ethernet interfaces—one on front panel or RTB; second on RTB
- Two RS-232 interfaces with hardware handshake capability
- Two RS-232 interfaces (keyboard/mouse interface via RTB)
- Two USB interfaces
- One Wide Ultra3 SCSI (LVD) interface (160MBytes/sec)
- One PCI mezzanine card (PMC) interface

Highlights

The CPSB-560 is a PICMG 2.16-compliant UltraSPARC-IIi+® 650MHz processor-based CompactPCI server blade for use in CompactPCI Packet-Switching Backplane (cPSB) systems. The CPSB-560 is ideal for switch-fabric applications and for building-block solutions such as Force's Centellis CO 21000-12U series systems. The CPSB-560 provides an ideal solution for 2.5G (GPRS) and 3G (UMTS) wireless, voice over packet (VoP) and other demanding telecom/data com applications and offers a low-power consumption processor on Sun Microsystems' embedded roadmap—ensuring a clear upgrade path into the future.

UltraSPARC-IIi+ Processor-Based CPSB-560

SPECIFICATIONS

Powerful Solaris 8 operating system

The CPSB-560 provides maximum Solaris™ 8 operating system performance in a single slot. The CPSB-560 is built around the UltraSPARC-IIi+ processor with its high-speed 650MHz performance and low-power consumption characteristics. The use of the UltraSPARC-IIi+ processor with a powerful set of I/O interfaces enables the CPSB-560 to address a wide range of communications applications. And with support for the PICMG 2.16 specification for CompactPCI Packet-Switching Backplane (cPSB) requirements, it's an ideal solution for 2.5G and 3G wireless, IP telephony, high-density embedded servers for Internet data centers and other demanding telecom/data com applications in need of advanced switch-fabric technologies. A variety of SCSI and Ethernet interfaces make the CPSB-560 a perfect building block for creating highly reliable, scaleable and small-footprint systems.

Processor/memory

- UltraSPARC-IIi+ 650MHz processor
- 239SPECint2k, 260SPECfp2k
- 512KBytes integrated (on-die) L2 Cache memory
- Up to 1GByte onboard SDRAM (option to 2GBytes via memory module)

User Flash memory

- Up to 8MBytes User Flash

Chipset

- Rio (PCI-to-EBUS)

Storage

- Onboard IDE hard-disk drive (30 to 60GBytes)

Server blade I/O capabilities

- PCI Rev 2.2 (32-bit, 66MHz)
- Two Gigabit Ethernet interfaces
- Two Fast Ethernet interfaces
- Two RS-232 interfaces with hardware handshake capability
- Two RS-232 interfaces (keyboard/mouse interface via RTB)
- Two USB interfaces
- One Wide Ultra3 SCSI (LVD) interface
- One PCI mezzanine card (PMC) interface or one additional memory module
- 1GByte SDRAM (option to 2GBytes)
- PICMG 2.9-compliant IPMI controller

Operating system

- Solaris 8 and later revisions

Environmentals

- Operating temperature: 0 to +50°C
- Storage requirements: -40 to 85°C, 5% to 95% (non-condensing) relative humidity

Physical dimensions

- 6U Eurocard (160mm x 233.5mm)
- One slot

Power consumption

- 28W

Accessories

- RTB-505/PSB
  - Rear-transition board for CPSB-560
- MEM-550
  - Memory module with 1GByte SDRAM

Sun microsystems STRATEGIC DEVELOPER

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Force Computers is the leading designer and worldwide supplier of standard and custom systems, board-level computer platforms and services for the embedded market



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