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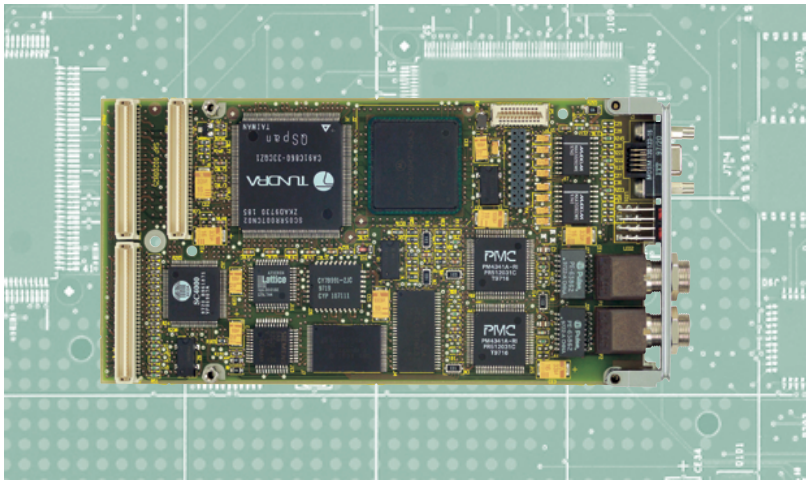
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Find the *Emerson / Motorola / Force Computers PPC/PMC/860 66-T1-R* at our website: ***Click HERE***

PMC/860

DATASHEET

High-performance E1 or T1 Interface Telecom Controller for SS7, LAPD and V5.x Applications



PMC/860

- **Telecommunications**
- **Datacommunications**
- **Industrial Control**
- **Government**

Features

- PowerQUICC MPC860MH processor, 66 MHz
- 16 MB DRAM, 256 KByte fast SRAM
- 2 MByte Flash
- Two E1/T1 interfaces on front panel
- Debug port and S/W controlled status LEDs
- Optional CompactPCI RTB (Rear Transition Board) for rear I/O routing of E1/T1 and debug port
- TDM bus interface to SCbus backplane on PowerCore-6603/4 and to H.110 backplane on PowerCore CPCI-6750
- LAPD/ISDN, Frame Relay, X.25, MTP-2/SS7 Protocol software via Force Partner Program

Highlights

The PMC/860 is a PCI mezzanine card that combines E1 or T1 and TDM telephony bus connectivity with the Motorola PowerQUICC processor. With PMC/860, you'll bring your telecom products to market faster because you'll have the tools you need in-hand to develop IN/AIN and wireless infrastructure products and services in the simplest, most convenient way possible.

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SPECIFICATIONS

General

Processor Subsystem

- PowerQUICC MPC860MH
- 87 MIPS @ 66 MHz
- 16 MByte shared EDO DRAM
- 256 KByte fast SRAM
- 2 MByte Flash PROM
- PCI Rev. 2.1 interface, up to 33 MHz supported

I/O Subsystem

- Two E1 or T1 interfaces
- RS-232 Debug port
- SCbus on PMC User I/O connector

Front Panel

- Two E1 or T1 interfaces via shielded circular connector
- Debug port on Micro D-Sub9 connector
- 2x 3 LEDs

Switching Subsystem

- Time slot multiplexer
- Connects to external SCbus
- Connects locally to two E1 or T1 interfaces and two PowerQUICC TDM channels (32 x 64 Kbit/s each)
- Supports SCbus at 512, 1024 or 2048 time slots bandwidth
- Time slots are simplex bi-directional with broadcast support

SCbus and Local Clocking

- E1 or T1 or SCbus can be selected as clock master for the local system
- On-board clock for local clock generation
- SCbus master, slave and clock fallback support

Product Variants

- PMC/860/66-T1-100: 2 T1/100 Ohm interfaces
- PMC/860/66-E1-120: 2 E1/120 Ohm interfaces
- PMC/860/66-E1-75: 2 E1/75 Ohm interfaces
- PMC/860/66-T1-R: 2 T1 interfaces to PMC user I/O connector
- PMC/860/66-E1-R: 2 E1 interfaces to PMC user I/O connector

Environmental

- Storage Temperature: -40°C to +85°C
- Operating Temperature: 0°C to +55°C
- ETS 300 019-1-1/1.1 (storage), ETS 300 019-1-3/3.3 (operation)

Power Consumption

- 1100 mA @ 5 V max.
- 600 mA@3.3V max.

Dimensions

- PMC Module Type 1 (single width), IEEE P1386 compliant
- 150 mm x 75 mm / 5.90" x 2.95"

Factory Assembly Options *(Please ask for availability)*

- No SC4000/HSCX, but Framer to PowerQUICC direct connection
- No PowerQUICC assembled, but Framer, etc. controlled from host
- One V.11 interface on front panel (instead of debug port)
- Debug port routed to front panel connector
- 4 MByte Flash PROM (replacing 2 MByte)

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Accessories

- Cable, RJ-45 (male) to circular connector for usage with E1/120 Ohm and T1/100 Ohm product variants
- Cable, BNC to circular connector for usage with E1/75 Ohm
- Cable, debug port
- RTOS BSP (Tornado™)

Agency Approval/Compliance

- EMC/EMI: FCC Part 15, CE approval (EN 55022, EN 50082-2)
- Safety: UL 1950, EN 60950
- Telecom: ITU G.703/704, ITU G.823
- FCC Part 68
- ETS 300 011 (layer 1)

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Force Computers is the Partner of Choice for Open, Scalable, Highly Available Embedded Computing Platforms to the leading OEM's in the Communications Market

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